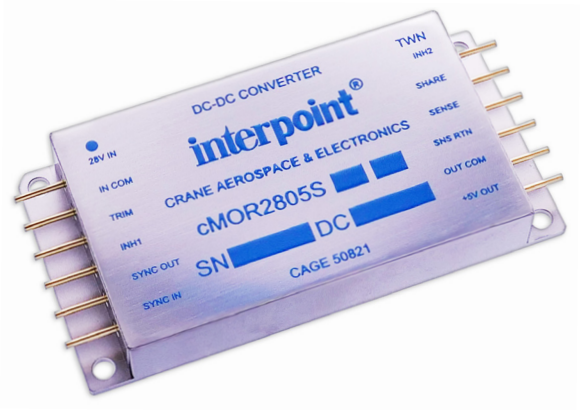
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

FEATURES

- Parallel operation with current share, up to 5 units (540 watts)
- Output flexibility, trim of 80% to 110%
- Operating temperature -55 °C to +105 °C
- Input voltage 15 to 50 V
- Fully isolated, magnetic feedback
- Fixed high frequency switching
- Remote sense on single output models
- Inhibit function
- Sync In and Sync Out
- Indefinite short circuit protection
- High power density with up to 90% peak efficiency
- Soft-start function limits inrush current during start-up



MODELS OUTPUT VOLTAGE (V)	
SINGLE	DUAL
3.3	N/A
5	±5
6.3	±6.3
9.5	±9.5
12	±12
15	±15
28	N/A

DESCRIPTION

The Interpoint® cMOR Series™ of DC-DC converters offers up to 120 watts of power in a low profile package with a 15 to 50 volt input. The cMOR converters are manufactured in our fully certified and qualified MIL-PRF-38534 Class H or class K production facility and packaged in hermetically sealed steel cases. cMOR converters are ideal for use in programs requiring high reliability, small size, and high efficiency. Full operation over the temperature range, -55 °C to +105 °C, makes the cMOR Series an ideal choice for military, aerospace, space, and other high reliability applications. Use Interpoint FMCE-1528 EMI filter to meet the requirements of MIL-STD-461C CE03 and MIL-STD-461D-G CE102 levels of conducted emissions.

The converters are offered standard screening or “ES” screening.

The cMOR Series converters uses an active-clamp reset, single-ended forward converter with synchronous rectification design. It uses a constant frequency Pulse Width Modulator (PWM) current mode control design with a constant switching frequency of 500 kHz.

Span Voltage

The dual models can be used as a single output voltage by connecting the load between positive and negative outputs, leaving the common unconnected resulting in double the output voltage. For example, cMOR2815D can be used as a 30 volt output. When using a dual to double the output voltage (span voltage) the maximum load capacitance across the span voltage is half that specified for each output.

Short Circuit Protection

The converters also provide short circuit protection by restricting the current to 125% of the full load output current, typical.

Inhibit Function

All models offer two inhibits, one referenced to input common and one referenced sense return (single output models) or to output common (dual output models). A remote sense function is available on single output models.

Trim Function

Using the trim function, the cMOR Series can adjust any output between 80% -110% of the nominal voltage for both single and dual output models.

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HOW TO USE THE FUNCTIONS

INPUT VOLTAGE

Steady state voltage range is 15 to 50 V. All models include a soft-start function to prevent large current draw and minimize overshoot.

EMI INPUT FILTERS

Internal 500 volt capacitors (dielectric working voltage (DWV) 700 volts) are connected between the case and input common and between the case and output common.
Use Interpoint FMCE-1528 or equivalent EMI filter to meet the requirements of MIL-STD-461C CE03 and MIL-STD-461D thru G CE102. When using an external input filter it is important that the case of the filter and the case of the converter be connected through as low an impedance as possible. Direct connection of the baseplates to chassis ground is the best connection. If connected by a single trace, the trace should be as wide as it is long. See Figure 1.

TRIM

Both single and dual output models include a trim function. Output voltage can be trimmed from 80% up to 110% of nominal Vout. When trimming up, do not exceed the maximum output power. When trimming down, do not exceed the maximum output current. At 110% trim high, low line drop out may occur below minimum input voltage. See Figure 2. On dual models the positive output is regulated and the negative output is transformer coupled (cross-regulated) to the positive output. When trimming the duals, both

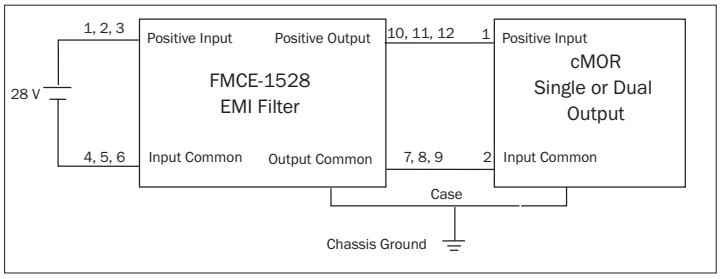

FIGURE 1: EXTERNAL FILTER CONNECTION

output voltages will be adjusted equally. See Figure 3.

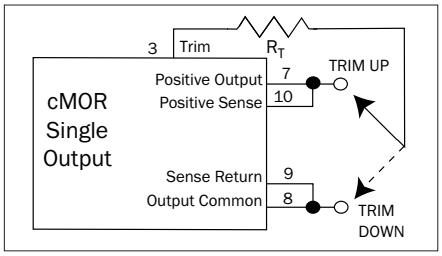

FIGURE 2: TRIM – SINGLE

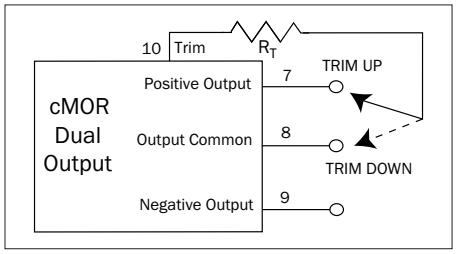

FIGURE 3: TRIM – DUAL

V normal (V)	X	Rint
3.3	0.757	30.1
5	0.5	40.2
6.3	0.396	40.2
9.5	0.262	40.2
12	0.208	40.2
14 (28 VS)	0.179	40.2
15	0.167	40.2

$$X = \frac{2.5}{V_{out\ normal}}$$

Trim Up:

$$R_T\,(k\Omega) = \frac{(1-X)*12}{X - \frac{2.5}{V_{o\ Trimmed}}} - R_{int}$$

Example $V_{out\ nominal} = 5V$, $V_{out\ trimmed} = 5.5V$

$X = 0.5$, $R_T = 91.8\ (k\Omega)$

Trim Down:

$$R_T\,(k\Omega) = \frac{X * Vo\ trimmed * 12}{2.5 - X * Vo\ trimmed} - R_{int}$$

Example $V_{out\ nominal} = 5V$, $V_{out\ trimmed} = 4.5V$

$X = 0.5$, $R_T = 67.8\ (k\Omega)$

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Inhibit 1 and 2

Two inhibit terminals disable switching, resulting in no output and very low quiescent input current. The two inhibit pins allow access to an inhibit function on either side of the isolation barrier to help maintain isolation.

An open collector is required for interfacing with both of the inhibit pins. Pulling either inhibit pin low will inhibit the converter. Leaving the pins open will enable the converter. Inhibit 1 is referenced to Input Common. Inhibit 2 is referenced to Sense Return for single output models and to Output Common for dual output models (for cMOR2828S model, reference to SEC GND, Pin 8).

The open circuit voltage (unit enabled) for Inhibit 1 is 19 V and for Inhibit 2 it is up to 4.9 V. Leave the Inhibit pins unconnected if not used. The required active low voltage level is 0.8 V maximum for Inhibit 1 and 0.2 V maximum for Inhibit 2.

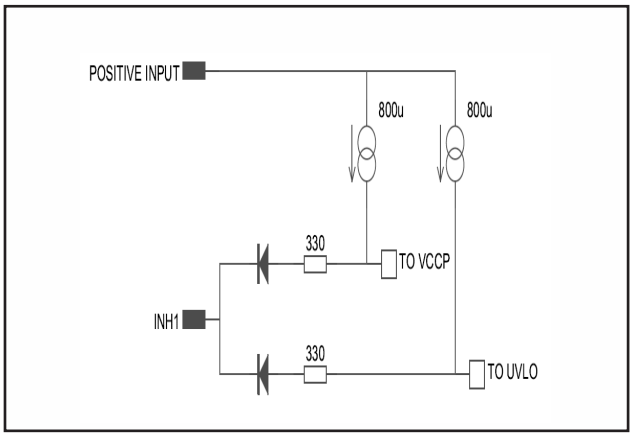

Figure 4: INH1

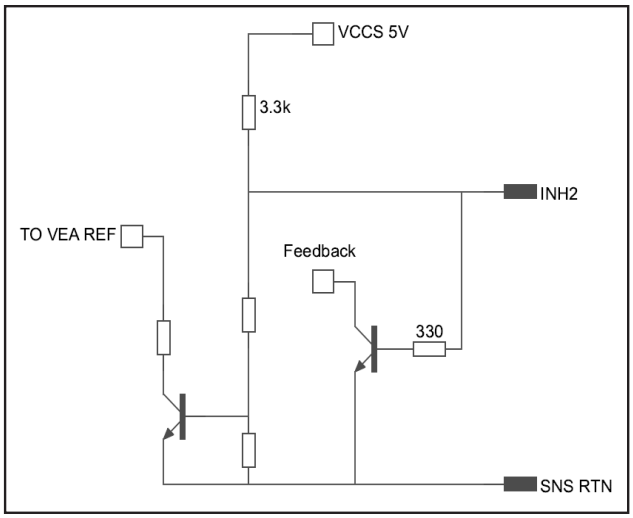

Figure 5: INH2

Undervoltage Lockout

Undervoltage lockout prevents the units from operating below approximately 14.5 volts input voltage to keep system current levels smooth, especially during initialization or re-start operations.

Sync In and Sync Out

The cMOR converters can be synchronized to the system clock by applying an active high sync signal to the Sync In pin. Sync Out can be used to synchronize other components to the cMOR converter's switching frequency.

The frequency range for external synchronization is 450 to 550 kHz. The requirements for an external signal are 20% to 50% duty cycle, 0 ≤ L ≤ 0.8 V and 3.5 ≤ H ≤ 7 V. Both Sync In and Sync Out are referenced to input common. Sync In should be left unconnected if not used.

OUTPUT OVERCURRENT PROTECTION

Output current is typically limited to 125% of maximum specified current under short circuit or load fault conditions. Single output models operate from no load to full load.

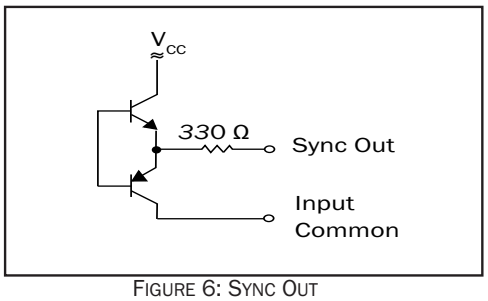

Figure 6: Sync Out

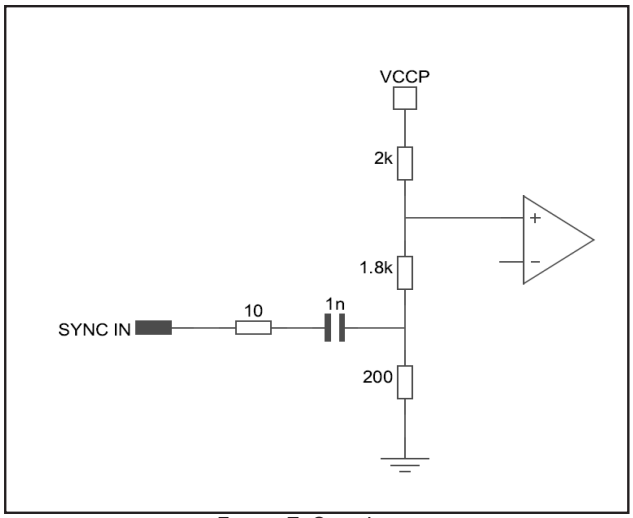

Figure 7: Sync In

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PARALLELING (SHARE PIN)

By using the Share pin, up to five single or dual converters may be paralleled for a total output power of up to 540 watts, depending on model. To calculate available power, multiply the number of converters (up to five) by their maximum output power. Multiply the result by 90% for total available power. The converters will share within 10% of each other at 25% to 90% for total available power.

Due to the synchronous rectification used in the cMOR, a series diode must be connected between the Positive Output pin and the load. The Positive Sense must be connected to the Postive Output on the anode side of the series diode. Voltage trim may be used to compensate for the voltage drop across the external diode. If the Positive Sense must be connected to the load, please see xMOR Current Sharing white paper or contact Application Engineering for additional instructions.

All Negative Outputs and Sense Returns should be connected to a common point. The Share pin is referenced to Sense Return. Leave the share pin floating (unconnected) if not used. Also see Figure 8.

PULSED CAPACITIVE LOAD

Care must be taken when applying a capacitive load on the cMOR as a load transient into no load may lead to negative current flow into the converter and cause damage to the device Figure 9.

In current share configurations, a capacitive load shall not be placed on the anode side of the series diode, but may be placed on the cathode side.

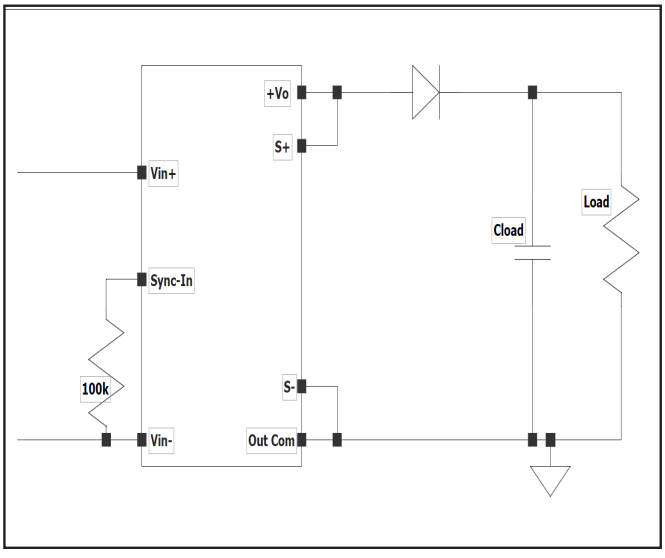

FIGURE 9: PULSED CAPACITIVE LOAD

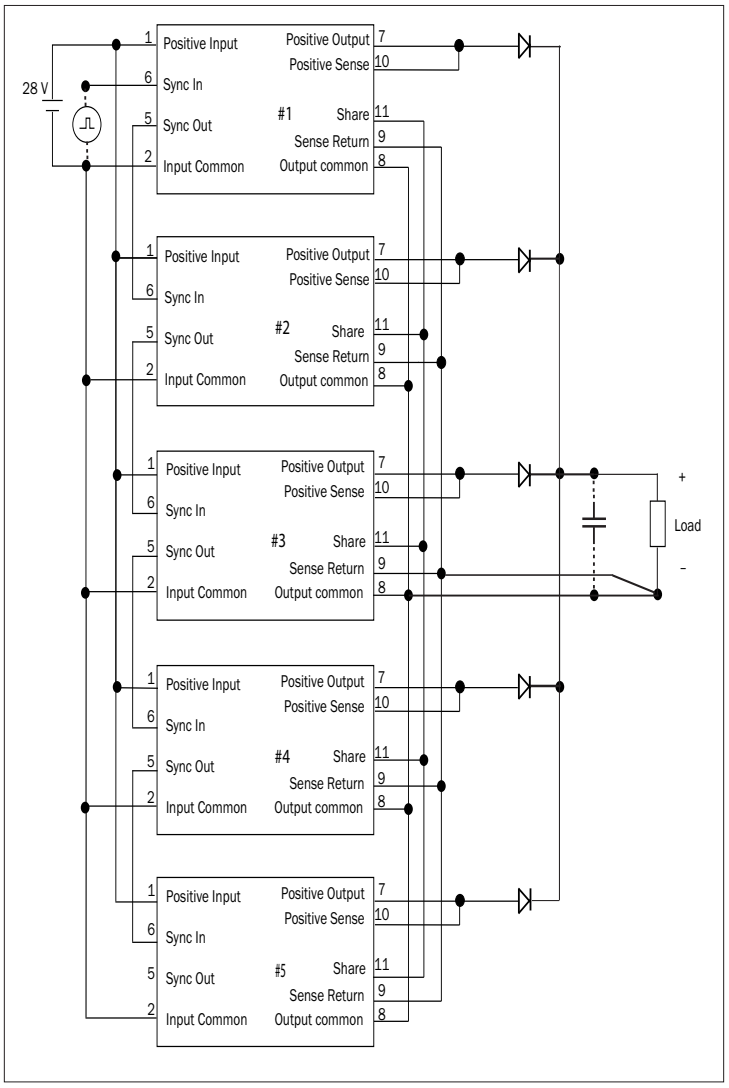

FIGURE 8: PARALLELING

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Positive Sense and Sense Return

A special remote sensing feature maintains the desired output voltage at the load. When this feature is not used, connect the sense lines to their respective output terminals. See Figure 10. Remote sensing is available on single output models only. Do not exceed 110% of Vout and do not exceed maximum output power.

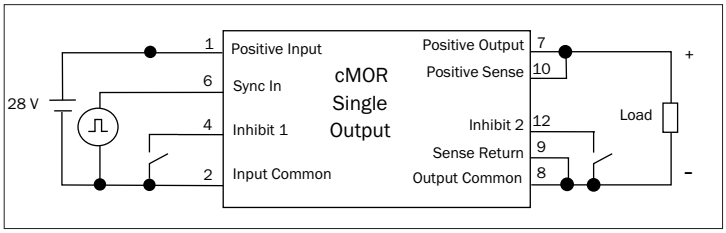

Figure 10: Typical Connections With Sense Pin

Increase Output Voltage by Spanning Outputs

Dual outputs may be spanned to increase the output voltage. cMOR duals can also be configured as a single output where the positive output is used as one rail and the negative output is used as the other rail. As an example the positive and negative 15 volt dual can be configured as a single 30 volt output. This can be used as a positive 30 volt output or a negative 30 volt output. See Figure 11. In all cases Output Common of the converter is not connected.

If the dual is configured as a positive 30 volt output the negative output would be used as system ground and the positive output would be used as the positive 30 volt output.

If the dual is configured as a negative 30 volt output the positive output would be used as system ground and the negative output would be used as the negative 30 volt output.

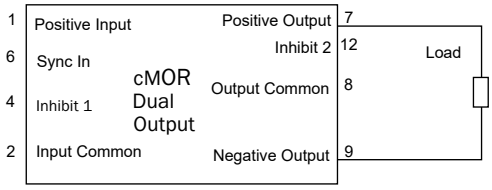

Figure 11: Span Output Connection - Dual Model

The maximum capacitance when using a span voltage on a dual is half the value specified for each output. Inhibit 2 cannot be referenced to system ground when spanning voltages. Leave Inhibit 2 floating if not in use. If Inhibit 2 is needed, please contact Applications Engineering at powerapps@craneae.com.

Output Overload and Short-Circuits Protection

The cMOR converters are protected against indefinite short circuits when the short circuit output voltage is less than 900 mV. Figure 12 below shows the overload characteristics of the output voltage versus output current. Between the current limit trip point and the short circuit is the output overload, where a continuous operation in this state may damage or substantially shorten the life of these converters. To prevent damage, the converters should not operate in overload for more than 100 ms.

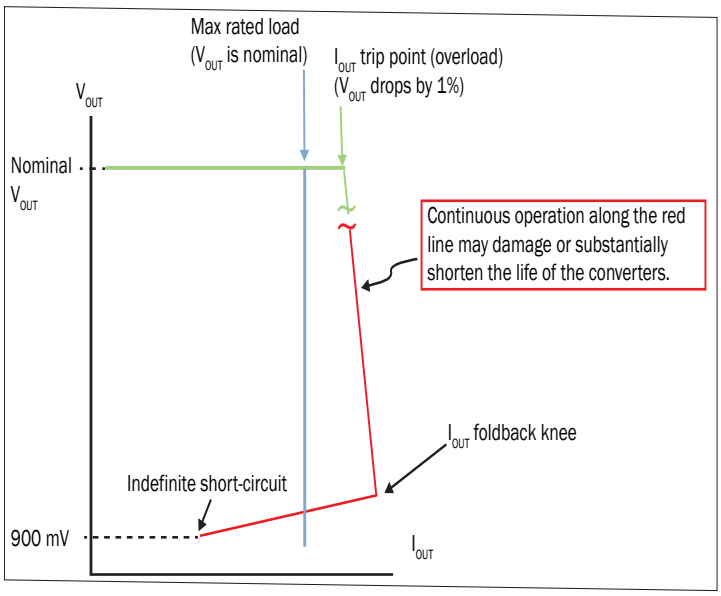

Figure 12: Output Overload and Short-Circuits Protection

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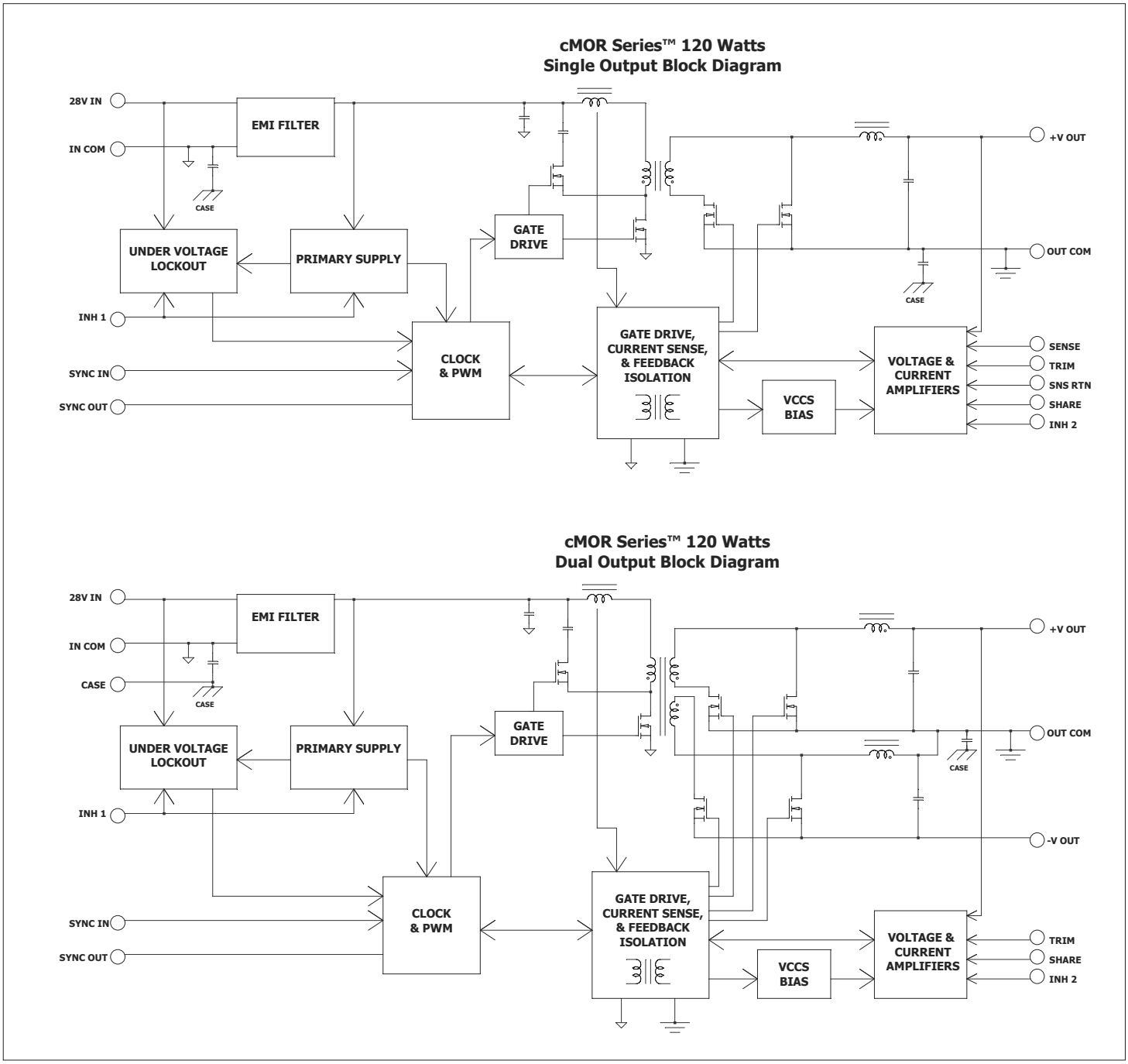

Figure 13: cMOR Single And Dual Output Block Diagram

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PIN OUT			
Pin	**Single Output**	**Dual Output**	**cMOR2828S ONLY**
1	Positive Input	Positive Input	Positive Input
2	Input Common	Input Common	Input Common
3	Trim	Case	Case
4	Inhibit 1 (INH1)	Inhibit 1 (INH1)	Inhibit 1 (INH1)
5	Sync Out	Sync Out	Sync Out
6	Sync In	Sync In	Sync In
7	Positive Output	Positive Output	Positive Output
8	Output Common	Output Common	Pin-8 Trim/INH 2 RTN. Use Pin-8 as return for Trim, Share, and/or Inh 2 functions only. Leave unconnected otherwise
9	Sense Return	Negative Output	Output Common
10	Positive Sense	Trim	Trim
11	Share	Share	Share
12	Inhibit 2 (INH2)	Inhibit 2 (INH2)	Inhibit 2 (INH2)

TABLE 1: PIN OUT

PINS NOT IN USE	
Case	User's discretion
Inhibit (INH1, INH2)	Leave unconnected
Sense Lines	Must be connected to the appropriate outputs
Sync In	Leave unconnected
Sync Out	Leave unconnected
Share	Leave unconnected
Trim	Leave unconnected

TABLE 2: PINS NOT IN USE

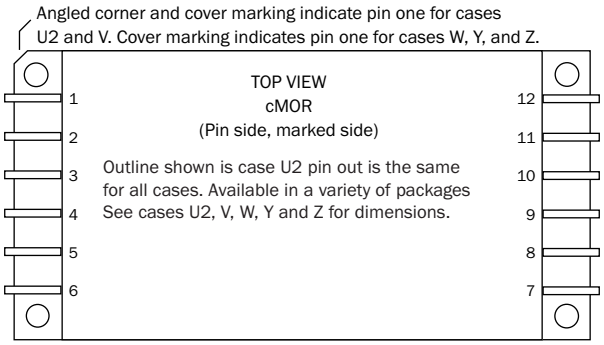

FIGURE 14: PIN OUT TOP VIEW

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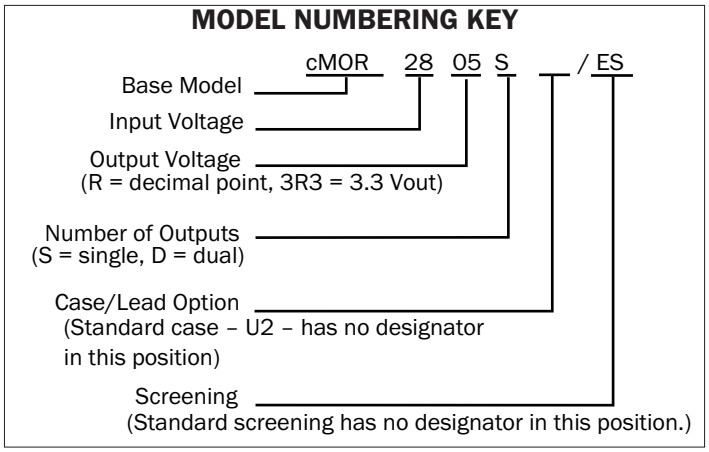

Figure 15: Model Numbering Key

MODEL NUMBER OPTIONS

TO DETERMINE THE MODEL NUMBER ENTER ONE OPTION FROM EACH CATEGORY IN THE FORM BELOW.

CATEGORY	Base Model and Input Voltage	Output Voltage [1]	Number of Outputs [2]	Case Options [3]	Screening	Lead Plating [4]
OPTIONS	cMOR28	3R3, 05, 6R3, 9R5, 12, 15, 28 05, 6R3, 9R5, 12, 15	S D	(U2, leave blank) V, W, Y, Z	(standard screening) ES	Au (leave blank) Solder dip (-Q)
FILL IN FOR MODEL # [8]	cMOR28	_____	_____	_____	/ _____	_____

Notes
1. Number of Outputs: S is a single output. D is for dual output.
2. Case Options: For the standard case, U2, leave the case option blank. For other case options, insert the letter that corresponds to the desired case. See figures 86 through 90 on pages 27 through 31 for case designators and dimensions.
3. Screening: For standard screening leave the screening option blank. For other screening options, insert the desired screening level. For more information see Table 10 on page 33.
4. If ordering by model number add suffix “-Q” to request solder dipped leads; example: (cMOR2805S/ES-Q).

TABLE 3: Model Number Options

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TABLE 4: Operating Conditions, All Models, 25°C case, 28 Vin, 100% load, unless otherwise specified

		ALL MODELS			
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
LEAD SOLDERING TEMPERATURE [1]	10 SECONDS MAX.	—	—	300	°C
STORAGE TEMPERATURE [1]		-55	—	+125	°C
CASE OPERATING TEMPERATURE	FULL POWER	-55	—	+105	°C
	ABSOLUTE [1]	-55	—	+115	
DERATING OUTPUT POWER/CURRENT [1]	LINEARLY	From 100% at 105°C to 0% at 115°C			
ISOLATION: INPUT TO OUTPUT, INPUT TO CASE, OUTPUT TO CASE [2]	@ 500 V AT 25°C	100	—	—	Megohms
UNDER VOLTAGE LOCKOUT		—	14.5	—	V
CURRENT LIMIT/POWER LIMIT [3]	% OF FULL LOAD	—	125	—	%
AUDIO REJECTION [1]		—	40	—	dB
SWITCHING FREQUENCY		450	500	550	kHz
SYNCHRONIZATION	INPUT FREQUENCY	450	—	550	kHz
	DUTY CYCLE	20	—	50	%
	ACTIVE LOW	—	—	0.8	V
	ACTIVE HIGH	3.5	—	7	
	SYNC IN REFERENCED TO	INPUT COMMON			
	SYNC OUT REFERENCED TO	INPUT COMMON			
INHIBIT ACTIVE LOW (OUTPUT DISABLED) Do not apply a voltage to the inhibit pin. [4]	INHIBIT 1 PIN PULLED LOW	—	—	0.8	V
	INHIBIT 1 PIN SOURCE CURRENT [1]	—	—	5	mA
	INHIBIT 1 REFERENCED TO	INPUT COMMON			
	INHIBIT 2 PIN PULLED LOW	—	—	0.2	V
	INHIBIT 2 PIN SOURCE CURRENT [1]	—	—	2	mA
	INHIBIT 2 SINGLES REFERENCED TO	SENSE RETURN			
	INHIBIT 2 DUALS REFERENCED TO	OUTPUT COMMON			
INHIBIT ACTIVE HIGH (OUTPUT ENABLED) Do not apply a voltage to the inhibit pin. [4]	INHIBIT PIN CONDITION INHIBIT 1 AND 2	OPEN COLLECTOR OR UNCONNECTED			
	OPEN INHIBIT 1 PIN VOLTAGE [1]	—	19	—	V
	OPEN INHIBIT 2 PIN VOLTAGE [1]		2.5	_	

For mean time between failures (MTBF) contact Applications Engineering at powerapps@craneae.com

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Isolation is tested with the all input pins (referenced to input common) tied together, and all output pins (referenced to output common) tied together. They are tested for isolation input to output, input to case and output to case. Discharge the pins after each test.
3. Current limit is defined as the point at which the output voltage drops by 1%
   Dual outputs: The over-current limit will trigger when the sum of the currents from both outputs reaches 125% (typical value) of the maximum rated "total" current of both outputs.
4. An external inhibit interface should be used to pull the inhibits low or leave them floating. The inhibit pins can be left unconnected if not used.

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TABLE 5: ELECTRICAL CHARACTERISTICS -55°C TO +105°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED

SINGLE OUTPUT MODELS		cMOR283R3S			cMOR2805S			cMOR286R3S			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		3.22	3.30	3.38	4.88	5.00	5.13	6.14	6.30	6.46	V
OUTPUT CURRENT	V_{IN} = 15 TO 50 V	–	–	24.3	–	–	24.0	–	–	19.0	A
OUTPUT POWER	V_{IN} = 15 TO 50 V	–	–	80	–	–	120	–	–	120	W
OUTPUT RIPPLE [8]	25°C	–	–	70	–	–	130	–	–	90	mV p-p
10 kHz - 20 MHz	-55°C TO +105°C	–	30	80	–	30	130	–	75	100	
LINE REGULATION	V_{IN} = 15 TO 50 V	–	–	17	–	0	20	–	–	32	mV
LOAD REGULATION	NO LOAD TO FULL	–	–	33	–	0	30	–	–	63	mV
INPUT VOLTAGE	CONTINUOUS	15	28	50	15	28	50	15	28	50	V
INPUT CURRENT	NO LOAD	–	70	150	–	90	150	–	70	150	mA
	INHIBITED–INH1	–	–	10	–	–	10	–	–	10	
	INHIBITED–INH2	–	–	100	–	–	100	–	–	100	
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	–	50	130	–	50	130	–	–	130	mA p-p
EFFICIENCY(28VIN/100% LOAD)	25°C	81	85	–	86	88	–	88	91	–	%
	-55°C TO +105°C	80	–	–	85	–	–	87	–	–	
EFFICIENCY(42VIN/100% LOAD)	25°C	81	85	–	86	88	–	87	91	–	%
	-55°C TO +105°C	80	–	–	85	–	–	87	–	–	
LOAD FAULT [2, 3]	SHORT CIRCUIT	–	–	35	–	–	35	–	–	35	W
POWER DISSIPATION	RECOVERY [1]	–	–	30	–	–	30	–	–	30	ms
STEP LOAD RESPONSE [3, 4]	TRANSIENT	–	–	±425	–	–	±450	–	–	±500	mV pk
50% - 100% - 50%	RECOVERY	–	–	300	–	–	300	–	–	300	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	–	±430	–	–	±400	–	–	±500	mV pk
V_{IN} = 15 - 50 - 15 V	RECOVERY	–	–	300	–	–	300	–	–	300	µs
START-UP [3, 6]	DELAY	–	–	30	–	–	30	–	–	30	ms
	OVERSHOOT [1]	–	–	50	–	0	50	–	–	50	mV pk
CAPACITIVE LOAD [1, 7]	T_C = 25°C	–	–	3500	–	–	3500	–	–	3500	µF

Notes

1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Short circuit is measured with a 10 milliohm (±10%) resistive load.
3. Recovery and start-up times are measured from application of the transient or change in condition to the point at which V_{OUT} is within 1% of final value.
4. Step load test is performed at 10 microseconds typical.
5. Step line test is performed at 100 microseconds ± 20 microseconds.
6. Tested on release from inhibit.
7. No effect on dc performance.
8. Measurement taken on ATE using an R-C damping network (0.25Ω /1µF) across Vout to minimize excessive ringing due to test station parasitics.

cMOR Single and Dual DC-DC Converters

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TABLE 6: ELECTRICAL CHARACTERISTICS -55°C TO +105°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED

SINGLE OUTPUT MODELS		cMOR289R5S			cMOR2812S			cMOR2815S			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		9.31	9.50	9.69	11.76	12.00	12.24	14.70	15.00	15.30	V
OUTPUT CURRENT	V_{IN} = 15 TO 50 V	–	–	12.6	–	–	10.0	–	–	8.0	A
OUTPUT POWER	V_{IN} = 15 TO 50 V	–	–	120	–	–	120	–	–	120	W
OUTPUT RIPPLE [8]	25°C	–	–	100	–	–	100	–	–	120	mV p-p
10 kHz - 20 MHz	-55°C TO +105°C	–	30	120	–	30	120	–	100	150	
LINE REGULATION	V_{IN} = 15 TO 50 V	–	–	48	–	0	60	–	–	75	mV
LOAD REGULATION	NO LOAD TO FULL	–	–	95	–	0	120	–	–	150	mV
INPUT VOLTAGE	CONTINUOUS	15	28	50	15	28	50	15	28	50	V
INPUT CURRENT	NO LOAD	–	70	150	–	90	150	–	70	180	mA
	INHIBITED–INH1	–	–	10	–	–	10	–	–	10	
	INHIBITED–INH2	–	–	100	–	–	100	–	–	100	
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	–	50	130	–	50	130	–	50	130	mA p-p
EFFICIENCY(28VIN/100% LOAD)	25°C	90	92	–	90	93	–	90	92	–	%
	-55°C TO +105°C	89	–	–	90	–	–	90	–	–	
EFFICIENCY(42VIN/100% LOAD)	25°C	87	–	–	87	–	–	87	–	–	%
	-55°C TO +105°C	87	–	–	87	–	–	87	–	–	
LOAD FAULT [2, 3]	SHORT CIRCUIT	–	–	35	–	–	35	–	–	35	W
POWER DISSIPATION	RECOVERY [1]	–	–	30	–	–	30	–	–	30	ms
STEP LOAD RESPONSE [3, 4]	TRANSIENT	–	–	±800	–	–	±750	–	–	±700	mV pk
50% - 100% - 50%	RECOVERY	–	–	300	–	–	300	–	–	300	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	–	±570	–	–	±600	–	–	±600	mV pk
V_{IN} = 15 - 50 - 15 V	RECOVERY	–	–	100	–	–	100	–	–	100	µs
START-UP [3, 6]	DELAY	–	–	30	–	–	30	–	–	30	ms
	OVERSHOOT [1]	–	–	50	–	0	50	–	–	50	mV pk
CAPACITIVE LOAD [1, 7]	T_C = 25°C	–	–	3500	–	–	3500	–	–	3500	µF

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Short circuit is measured with a 10 milliohm (±10%) resistive load.
3. Recovery and start-up times are measured from application of the transient or change in condition to the point at which V_{OUT} is within 1% of final value.
4. Step load test is performed at 10 microseconds typical.
5. Step line test is performed at 100 microseconds ± 20 microseconds.
6. Tested on release from inhibit.
7. No effect on dc performance.
8. Measurement taken on ATE using an R-C damping network (0.25Ω /1µF) across Vout to minimize excessive ringing due to test station parasitics.

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

TABLE 7: ELECTRICAL CHARACTERISTICS -55°C TO +105°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED

SINGLE OUTPUT MODELS		cMOR2828S			
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		27.30	28	28.70	V
OUTPUT CURRENT	V_{IN} = 15 TO 50 V	—	—	4.3	A
OUTPUT POWER	V_{IN} = 15 TO 50 V	—	—	120	W
OUTPUT RIPPLE [8]	25°C	—	—	300	mV p-p
10 kHz - 20 MHz	-55°C TO +105°C	—	—	300	
LINE REGULATION	V_{IN} = 15 TO 50 V	—	—	140	mV
LOAD REGULATION	NO LOAD TO FULL	—	—	280	mV
INPUT VOLTAGE	CONTINUOUS	15	28	50	V
INPUT CURRENT	NO LOAD	—	—	250	mA
	INHIBITED–INH1	—	—	10	
	INHIBITED–INH2	—	—	100	
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	—	—	130	mA p-p
EFFICIENCY(28VIN/100% LOAD)	25°C	90	92	—	%
	-55°C TO +105°C	90	—	—	
EFFICIENCY(42VIN/100% LOAD)	25°C	87	91	—	%
	-55°C TO +105°C	87	—	—	
LOAD FAULT [2, 3]	SHORT CIRCUIT	—	—	35	W
POWER DISSIPATION	RECOVERY [1]	—	—	30	ms
STEP LOAD RESPONSE [3, 4]	TRANSIENT	—	—	±1400	mV pk
50% - 100% - 50%	RECOVERY	—	—	300	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	—	—	±1400	mV pk
V_{IN} = 15 - 50 - 15 V	RECOVERY	—	—	300	µs
START-UP [3, 6]	DELAY	—	—	30	ms
	OVERSHOOT [1]	—	—	50	mV pk
CAPACITIVE LOAD [1, 7]	T_C = 25°C	—	—	875	µF

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Short circuit is measured with a 10 milliohm (±10%) resistive load.
3. Recovery and start-up times are measured from application of the transient or change in condition to the point at which V_{OUT} is within 1% of final value.
4. Step load test is performed at 10 microseconds typical.
5. Step line test is performed at 100 microseconds ± 20 microseconds.
6. Tested on release from inhibit.
7. No effect on dc performance.
8. Measurement taken on ATE using an R-C damping network (0.25Ω /1µF) across Vout to minimize excessive ringing due to test station parasitics.

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

TABLE 8: ELECTRICAL CHARACTERISTICS -55°C TO +105°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED

DUAL OUTPUT MODELS		cMOR2805D			cMOR286R3D			cMOR289R5D			UNITS
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
OUTPUT VOLTAGE [2]	$+V_{OUT}$	4.88	5.00	5.13	6.17	6.30	6.43	9.40	9.50	9.60	V
	$-V_{OUT}$	-5.18	-5.00	-4.82	-6.48	-6.30	-6.12	-9.65	-9.50	9.35	
OUTPUT CURRENT [3]	EITHER OUTPUT	—	±12	16.8	—	±9.5	13.3	—	±6.3	8.8	A
V_{IN} = 15 TO 50 V	TOTAL	—	—	24.0	—	—	19.0	—	—	12.6	
OUTPUT POWER [3]	EITHER OUTPUT	—	±60	84	—	±60	84	—	±60	84	W
V_{IN} = 15 TO 50 V	TOTAL	—	—	120	—	—	120	—	—	120	
OUTPUT RIPPLE [11]	25°C	—	—	75	—	—	90	—	—	100	mV p-p
$\pm V_{OUT}$ 10 KHZ - 20 MHZ	-55°C TO +105°C	—	100	130	—	50	100	—	75	120	
LINE REGULATION	$+V_{OUT}$	—	—	50	—	—	32	—	—	48	mV
V_{IN} = 15 TO 50 V	$-V_{OUT}$	—	—	100	—	—	63	—	—	95	
LOAD REGULATION	$+V_{OUT}$	—	—	50	—	—	63	—	—	95	mV
	$-V_{OUT}$	—	—	250	—	—	252	—	—	285	
CROSS REGULATION [4]	$-V_{OUT}$	—	5	8	—	5	8	—	4	7	%
INPUT VOLTAGE	CONTINUOUS	15	28	50	15	28	50	15	28	50	V
INPUT CURRENT	NO LOAD	—	—	250	—	—	250	—	70	250	mA
	INHIBITED–INH1	—	—	10	—	—	10	—	—	10	
	INHIBITED–INH2	—	—	100	—	—	100	—	—	100	
INPUT RIPPLE CURRENT	10 KHZ - 20 MHZ	—	60	130	—	—	130	—	60	130	mA p-p
EFFICIENCY(28VIN/100% LOAD)	25°C	87	89	—	89	90	—	90	91	—	%
	-55°C TO +105°C	86	—	—	88	—	—	90	—	—	
EFFICIENCY(42VIN/100% LOAD)	25°C	86	89	—	87	90	—	87	91	—	%
	-55°C TO +105°C	85	—	—	87	—	—	87	—	—	
LOAD FAULT [5, 6]	SHORT CIRCUIT	—	—	35	—	—	35	—	—	35	W
POWER DISSIPATION	RECOVERY [1]	—	—	30	—	—	30	—	—	30	ms
STEP LOAD RESPONSE [6, 7]	TRANSIENT	—	—	±450	—	—	±600	—	—	±800	mV pk
$\pm V_{OUT}$ 50% - 100% - 50%	RECOVERY	—	—	300	—	—	300	—	—	300	µs
STEP LINE RESPONSE [1, 6, 8]	TRANSIENT	—	—	±400	—	—	±500	—	—	±570	mV pk
V_{IN} = 15 - 50 - 15 V	RECOVERY	—	—	300	—	—	300	—	—	300	µs
START-UP [6, 9]	DELAY	—	—	30	—	—	30	—	—	30	ms
	OVERSHOOT [1]	—	—	50	—	—	50	—	—	50	mV pk
CAPACITIVE LOAD [1, 10]	T_C = 25°C	—	—	1750	—	—	1750	—	—	1750	µF

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Output voltage for dual output models is measured with balanced loads.
3. The "Total" specification is the maximum combined current/power of both outputs. Up to 70% of that total is available from either output provided the other output maintains a minimum of 30% of the total power used. The 15% minimum maintains regulation.
4. Effect on negative Vout from 50%/50% loads to 70%/30% or 30%/70% loads.
5. Short circuit is measured with a 10 milliohm (±10%) resistive load. Both outputs shorted simultaneously.
6. Recovery and start-up times are measured from application of the transient or change in condition to the point at which V_{OUT} is within 1% of final value.
7. Step load test is performed at 10 microseconds typical.
8. Step line test is performed at 100 microseconds ± 20 microseconds.
9. Tested on release from inhibit.
10. Each output. No effect on dc performance.
11. Measurement taken on ATE using an R-C damping network (0.25Ω /1µF) across Vout to minimize excessive ringing due to test station parasitics.

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

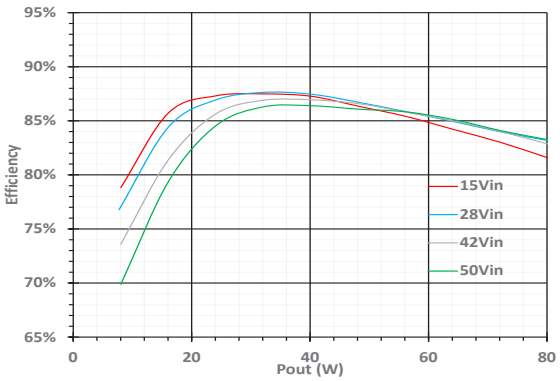
TABLE 9: ELECTRICAL CHARACTERISTICS -55°C TO +105°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED

DUAL OUTPUT MODELS		cMOR2812D			cMOR2815D			UNITS
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	
OUTPUT VOLTAGE [2]	$+V_{OUT}$	11.75	12.00	12.24	14.70	15.00	15.30	V
	$-V_{OUT}$	-12.30	12	-11.70	-15.38	-15.00	-14.62	
OUTPUT CURRENT [3]	EITHER OUTPUT	—	±5	7	—	±4.0	5.6	A
V_{IN} = 15 TO 50 V	TOTAL	—	—	10	—	—	8.0	
OUTPUT POWER [3]	EITHER OUTPUT	—	±60	84	—	±60	84	W
V_{IN} = 15 TO 50 V	TOTAL	—	—	120	—	—	120	
OUTPUT RIPPLE [11]	25°C	—	—	75	—	—	90	mV p-p
$\pm V_{OUT}$ 10 KHZ - 20 MHZ	-55°C TO +105°C	—	75	120	—	75	150	
LINE REGULATION	$+V_{OUT}$	—		60	—		75	mV
V_{IN} = 15 TO 50 V	$-V_{OUT}$	—		120	—		150	
LOAD REGULATION	$+V_{OUT}$	—		120	—		150	mV
	$-V_{OUT}$	—		240	—		300	
CROSS REGULATION [4]	$-V_{OUT}$	—	3	5	—	2	4	%
INPUT VOLTAGE	CONTINUOUS	15	28	50	15	28	50	V
INPUT CURRENT	NO LOAD	—	70	250	—	70	250	mA
	INHIBITED–INH1	—	—	10	—	—	10	
	INHIBITED–INH2	—	—	100	—	—	100	
INPUT RIPPLE CURRENT	10 KHZ - 20 MHZ	—		130	—	—	130	mA p-p
EFFICIENCY(28VIN/100% LOAD)	25°C	90	93	—	90	92	—	%
	-55°C TO +105°C	90	—	—	90	—	—	
EFFICIENCY(42VIN/100% LOAD)	25°C	87	92	—	87	91	—	%
	-55°C TO +105°C	87	—	—	87	—	—	
LOAD FAULT [5, 6]	SHORT CIRCUIT	—	—	35	—	—	35	W
POWER DISSIPATION	RECOVERY [1]	—	—	30	—	—	30	ms
STEP LOAD RESPONSE [6, 7]	TRANSIENT	—	—	±750	—	—	±700	mV pk
$\pm V_{OUT}$ 50% - 100% - 50%	RECOVERY	—	—	300	—	—	300	µs
STEP LINE RESPONSE [1, 6, 8]	TRANSIENT	—	—	±600	—	—	±600	mV pk
V_{IN} = 15 - 50 - 15 V	RECOVERY	—	—	300	—	—	300	µs
START-UP [6, 9]	DELAY	—	—	30	—	—	30	ms
	OVERSHOOT [1]	—	—	50	—	—	50	mV pk
CAPACITIVE LOAD [1, 10]	T_C = 25°C	—	—	1750	—	—	1750	µF

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Output voltage for dual output models is measured with balanced loads.
3. The "Total" specification is the maximum combined current/power of both outputs. Up to 70% of that total is available from either output provided the other output maintains a minimum of 30% of the total power used. The 15% minimum maintains regulation.
4. Effect on negative Vout from 50%/50% loads to 70%/30&% or 30%/70% loads.
5. Short circuit is measured with a 10 milliohm (±10%) resistive load. Both outputs shorted simultaneously.
6. Recovery and start-up times are measured from application of the transient or change in condition to the point at which V_{OUT} is within 1% of final value.
7. Step load test is performed at 10 microseconds typical.
8. Step line test is performed at 100 microseconds ± 20 microseconds.
9. Tested on release from inhibit.
10. Each output. No effect on dc performance.
11. V_{OUT} ripple is measurement taken on ATE using an R-C damping network (0.25Ω /1µF) across Vout to minimize excessive ringing due to test station parasitics.

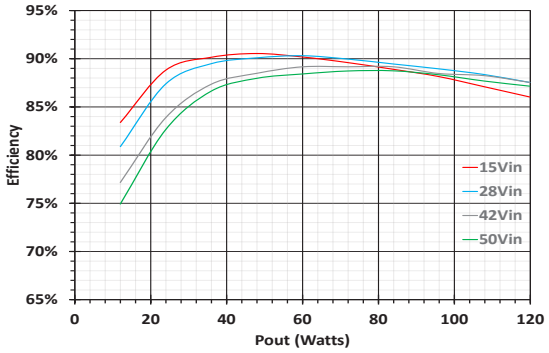
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



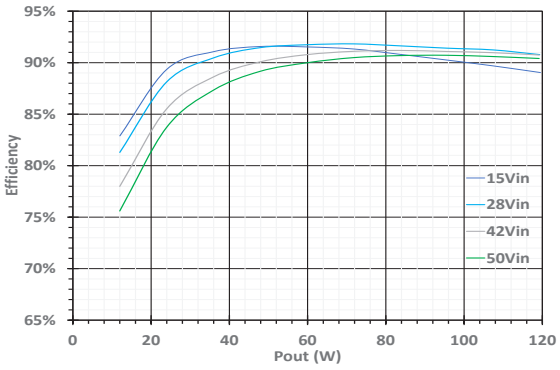
cMOR283R3S Typical Efficiency 25°C

FIGURE 16



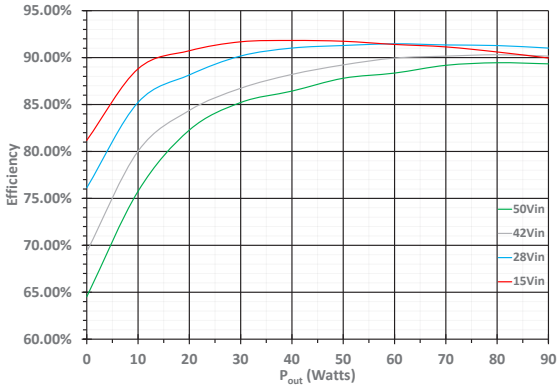
cMOR2805S Typical Efficiency 25°C

FIGURE 17



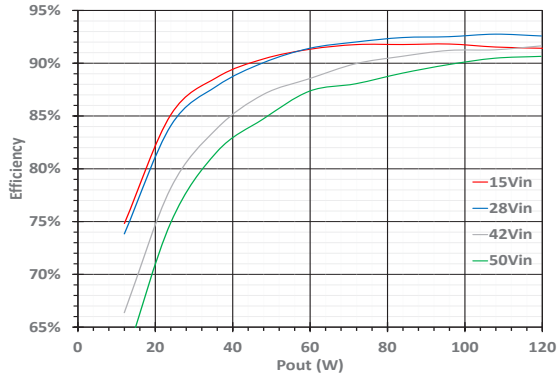
cMOR286R3S Typical Efficiency 25°C

FIGURE 18



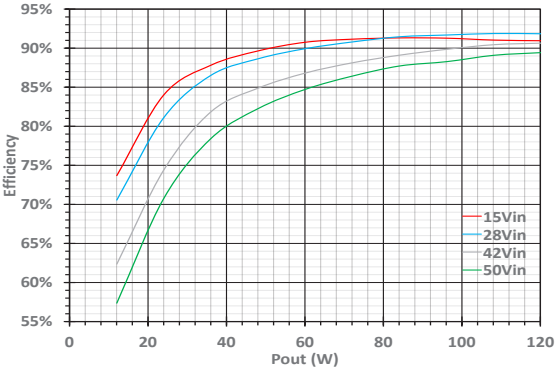
cMOR289R5S Typical Efficiency 25°C

FIGURE 19



cMOR2812S Typical Efficiency 25°C

FIGURE

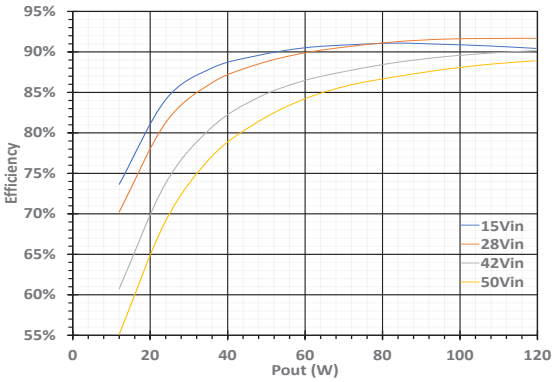


cMOR2815S Typical Efficiency 25°C

FIGURE 21

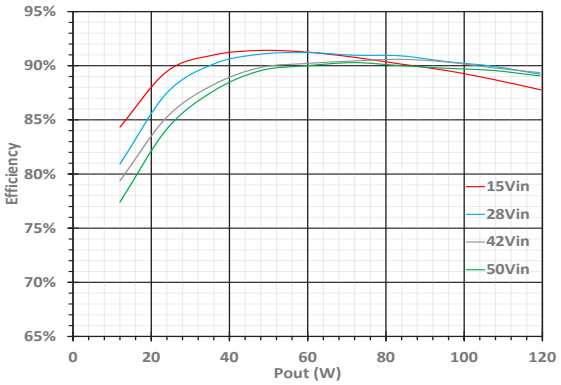
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



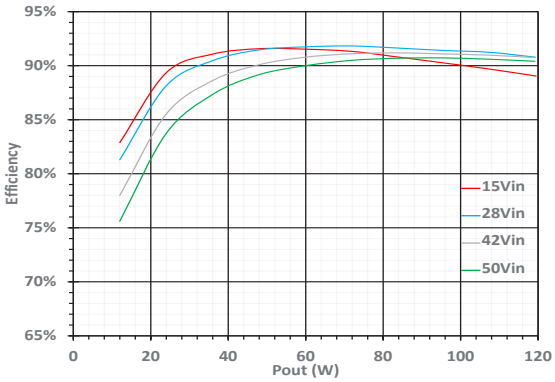
cMOR2828S Typical Efficiency 25°C

FIGURE 22



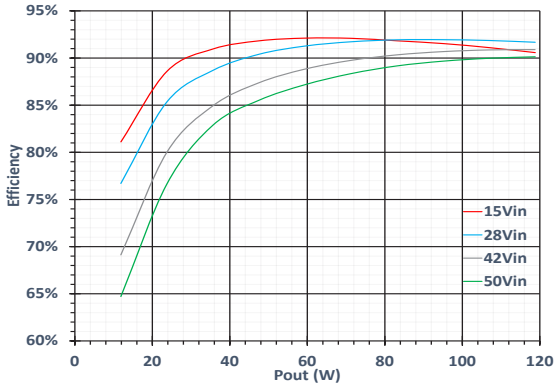
cMOR2805D Typical Efficiency 25°C

FIGURE 23



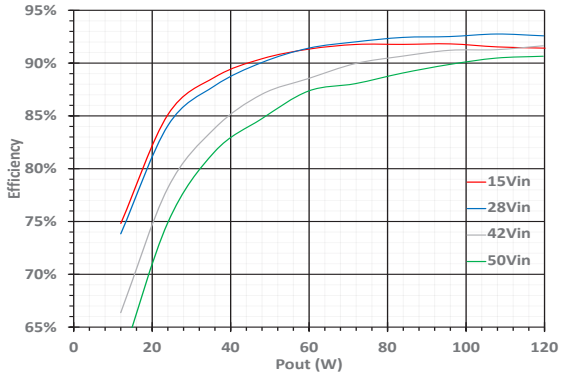
cMOR286R3D Typical Efficiency 25°C

FIGURE 24



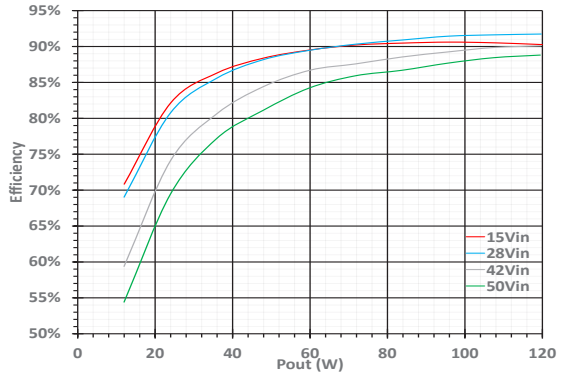
cMOR289R5D Typical Efficiency 25°C

FIGURE 25



cMOR2812D Typical Efficiency 25°C

FIGURE 26

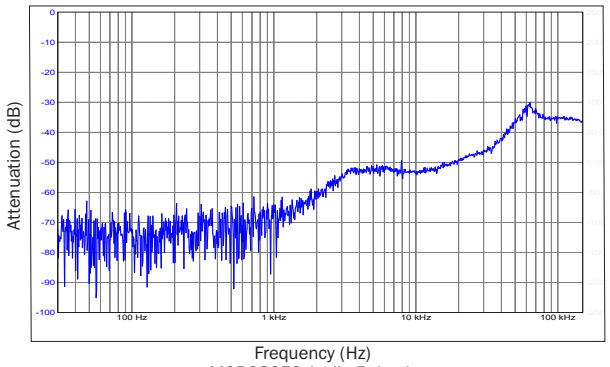


cMOR2815D Typical Efficiency 25°C

FIGURE 27

cMOR Single and Dual DC-DC Converters

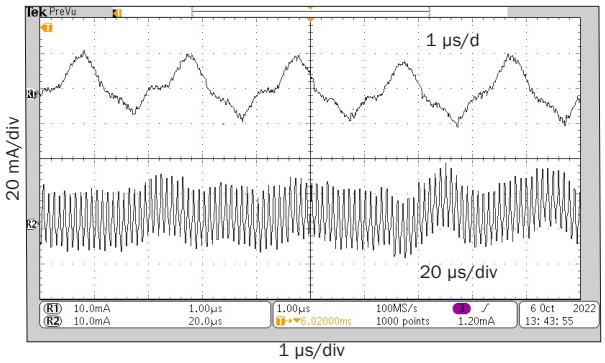
15 TO 50 VOLT INPUT - UP TO 120 WATT



cMOR2805S Audio Rejection

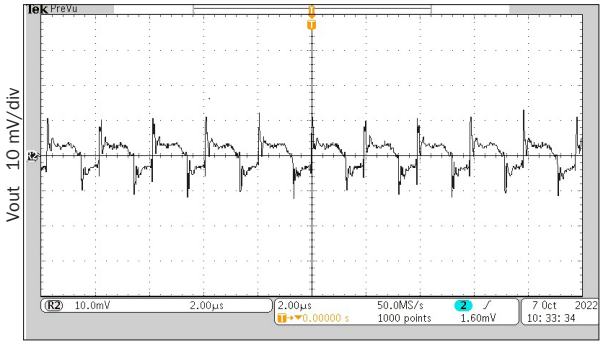
Frequency (Hz)

FIGURE 28



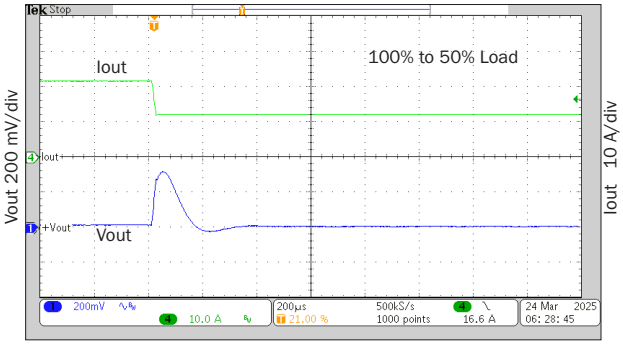
cMOR2805S Input Ripple Current (Iin)

FIGURE 29



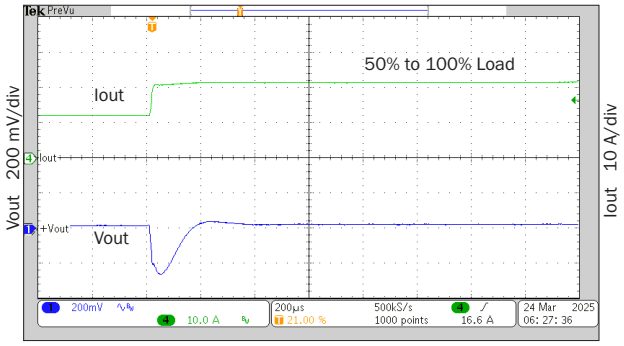
cMOR2805S Output Ripple (Vout)

FIGURE 30



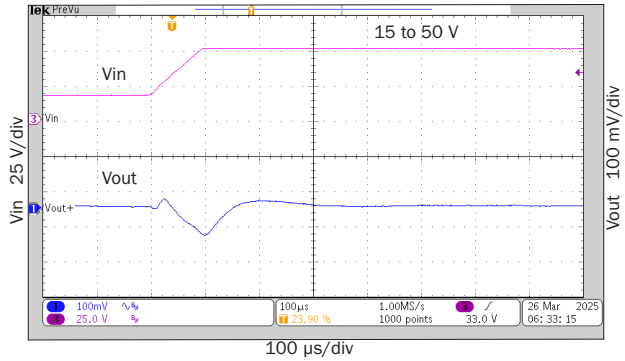
cMOR2805S step load response

FIGURE 31



cMOR2805S step load response

FIGURE 32

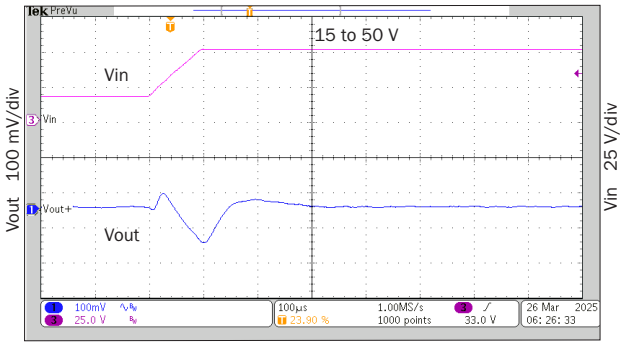


cMOR2805S step line response

FIGURE 33

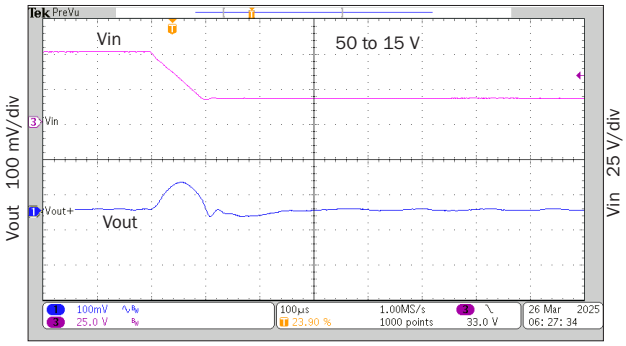
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



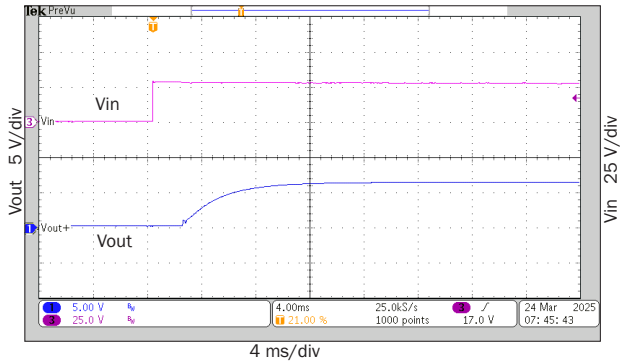
100 µs/div
cMOR286R3S step line response

FIGURE 34



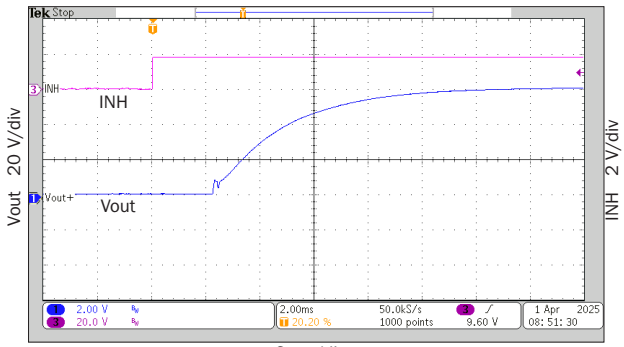
100 µs/div
cMOR286R3S step line response

FIGURE 35



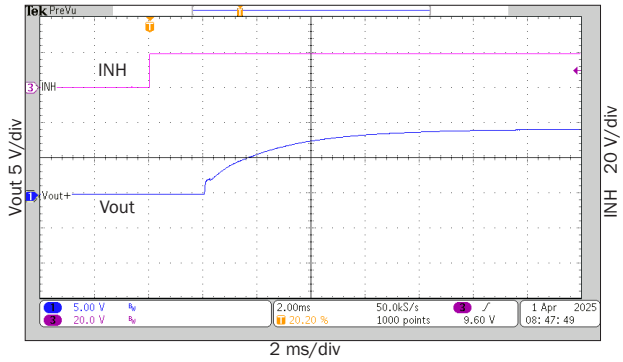
4 ms/div
cMOR286R3S start-up response

FIGURE 36



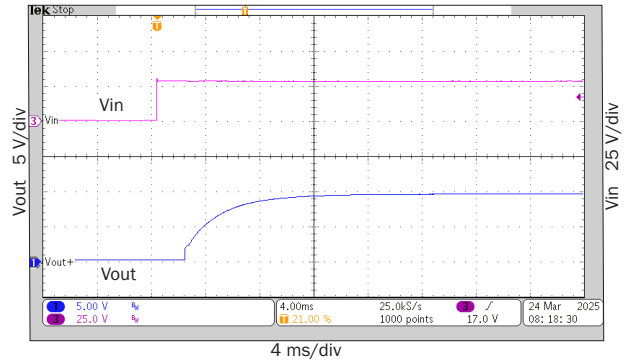
2 ms/div
cMOR286R3S inhibit release

FIGURE 37



2 ms/div
cMOR289R5S inhibit release

FIGURE 38

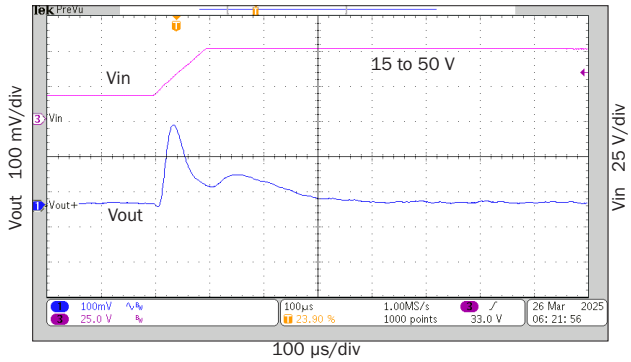


4 ms/div
cMOR289R5S start-up response

FIGURE 39

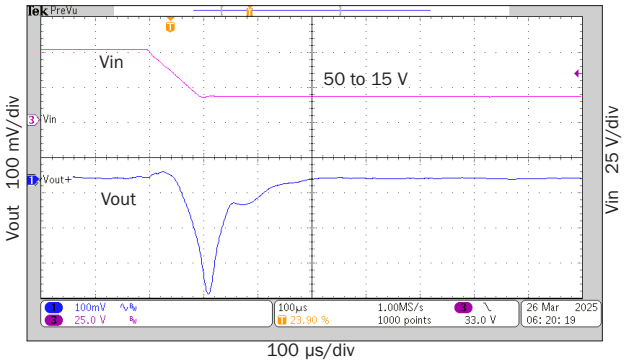
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



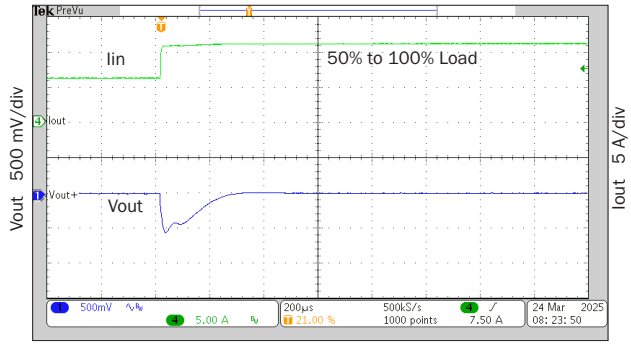
100 µs/div
cMOR289R5S step line response

FIGURE 40



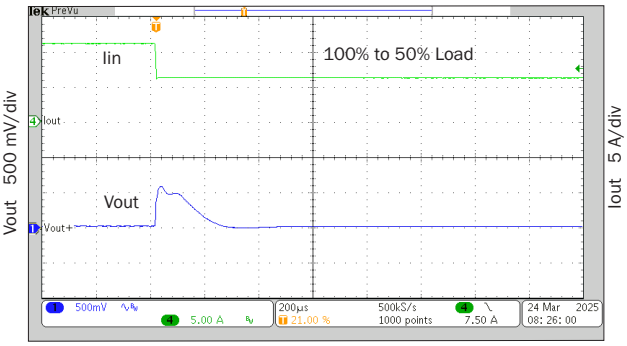
100 µs/div
cMOR289R5S step line response

FIGURE 41



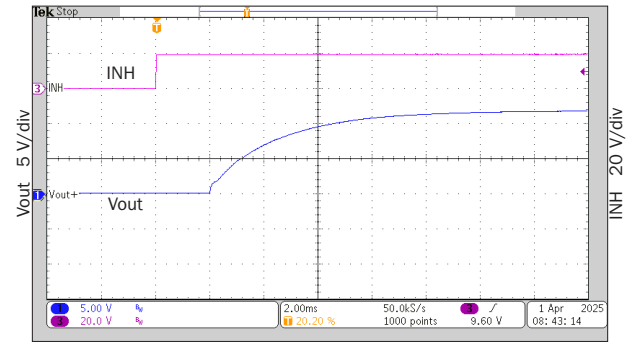
200 µs/div
cMOR289R5S step load response

FIGURE 42



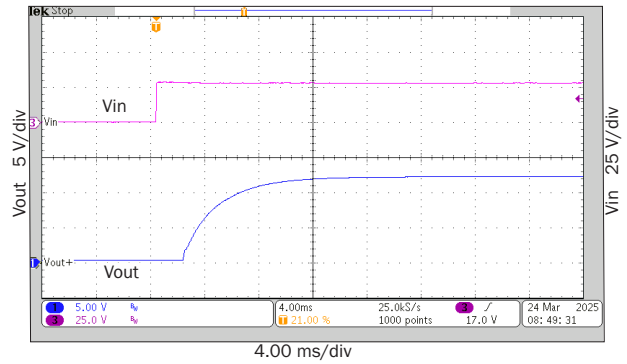
200 µs/div
cMOR289R5S step load response

FIGURE 43



2.00 ms/div
cMOR2812S inhibit release

FIGURE 44

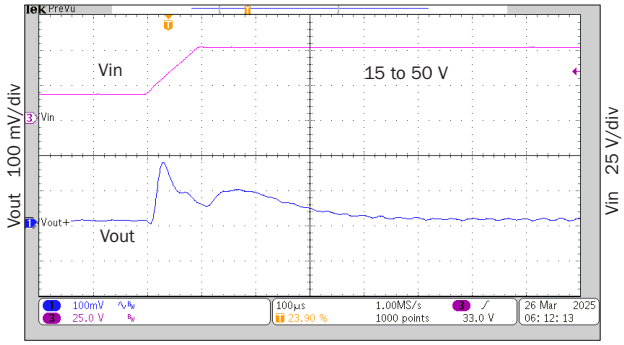


4.00 ms/div
cMOR2812S start-up response

FIGURE 45

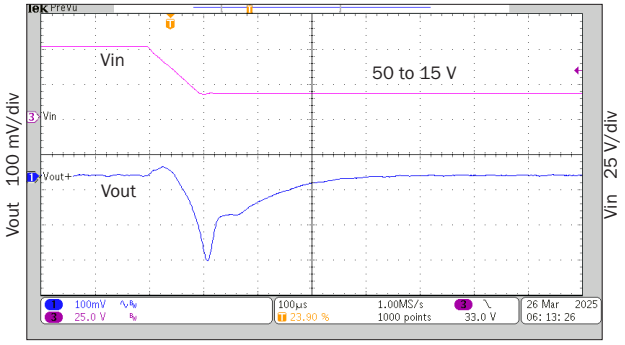
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



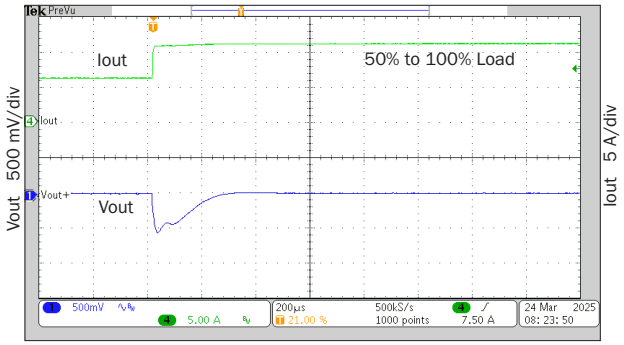
100 µs/div
cMOR2812S step line response

FIGURE 46



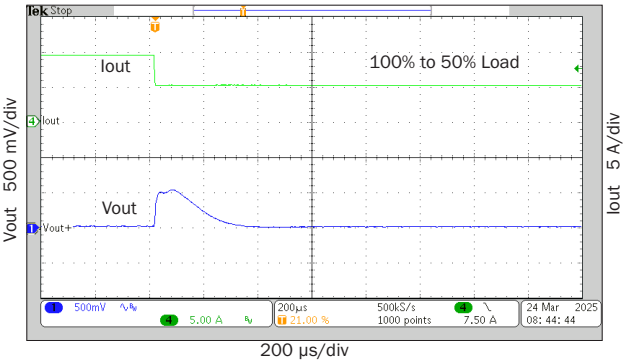
100 µs/div
cMOR2812S step line response

FIGURE 47



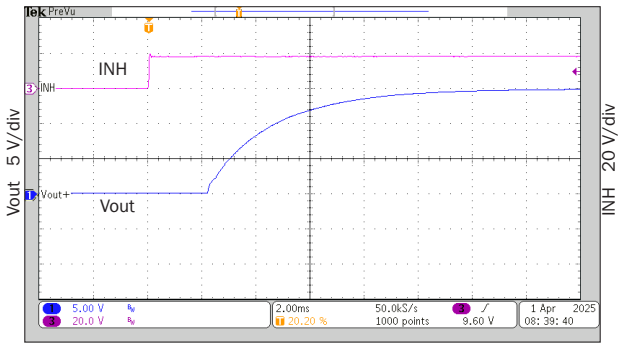
200 µs/div
cMOR2812S step load response

FIGURE 48



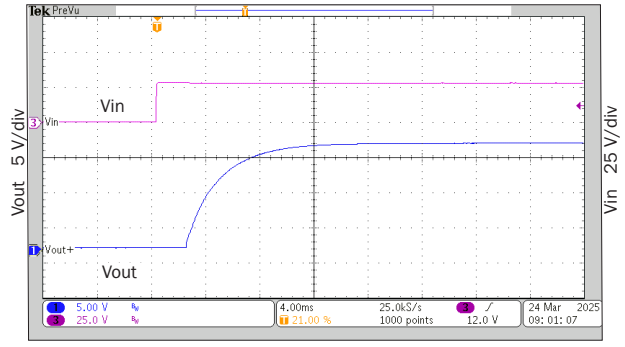
200 µs/div
cMOR2812S step load response

FIGURE 49



2.00 ms/div
cMOR2815S inhibit release

FIGURE 50

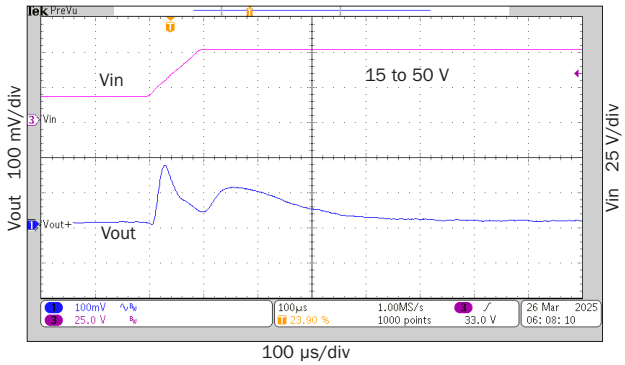


4.00 ms/div
cMOR2815S start-up response

FIGURE 51

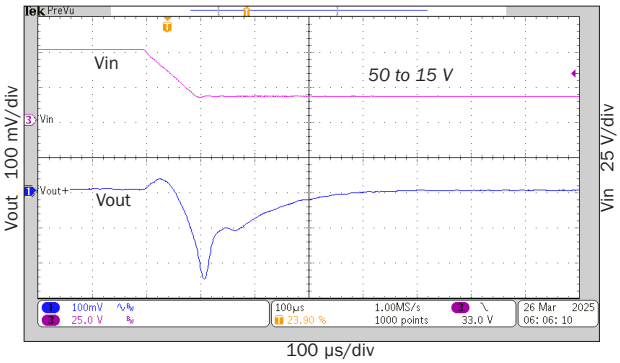
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



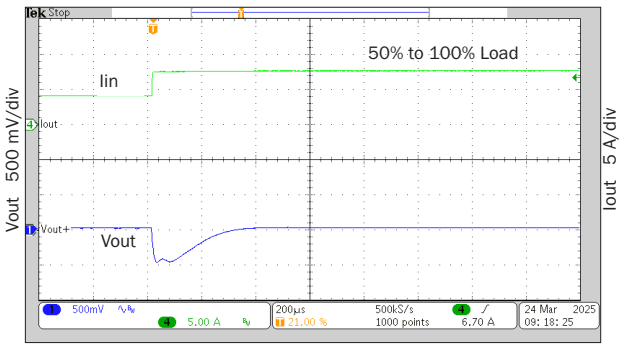
100 µs/div
cMOR2815S step line response

FIGURE 52



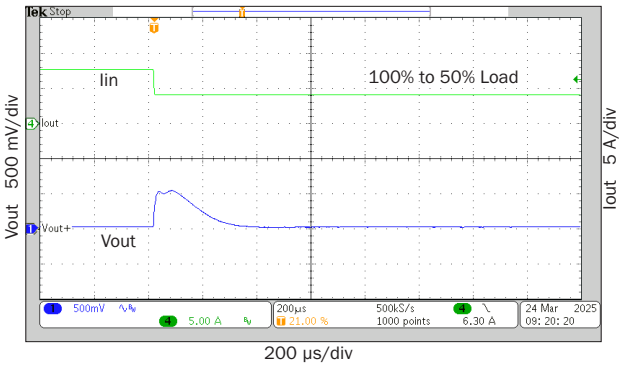
100 µs/div
cMOR2815S step line response

FIGURE 53



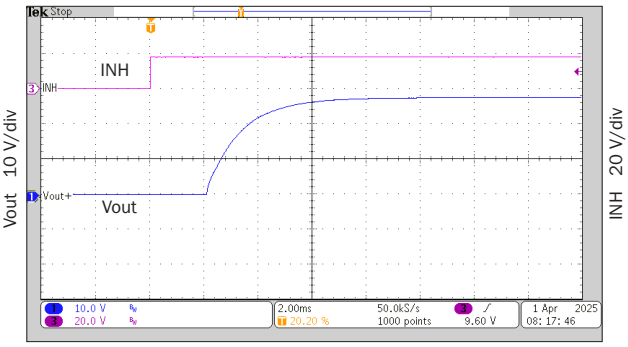
200 µs/div
cMOR2815S step load response

FIGURE 54



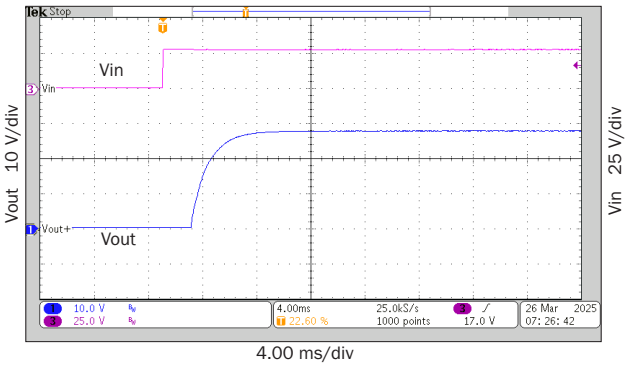
200 µs/div
cMOR2815S step load response

FIGURE 55



2.00 ms/div
cMOR2828S inhibit release

FIGURE 56

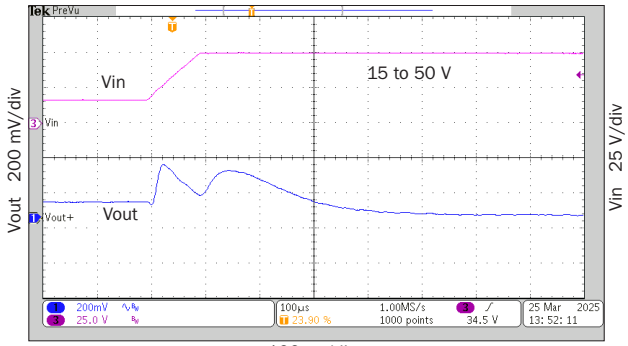


4.00 ms/div
cMOR2828S start-up response

FIGURE 57

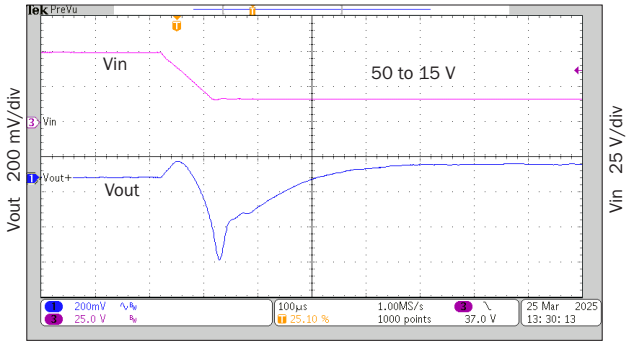
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



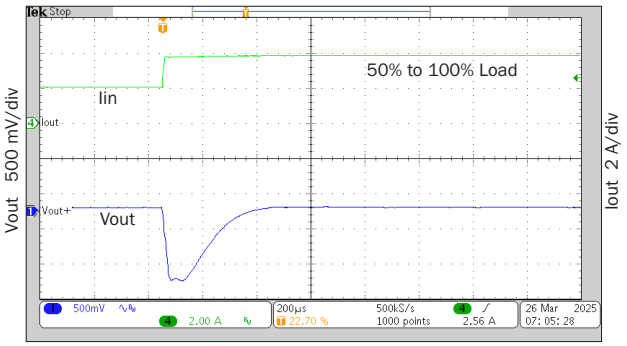
100 µs/div
cMOR2828S step line response

FIGURE 58



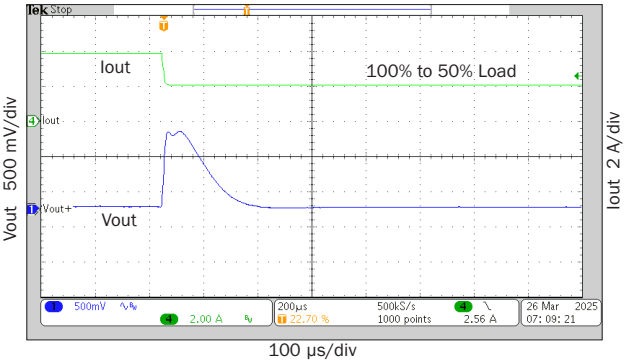
100 µs/div
cMOR2828S step line response

FIGURE 59



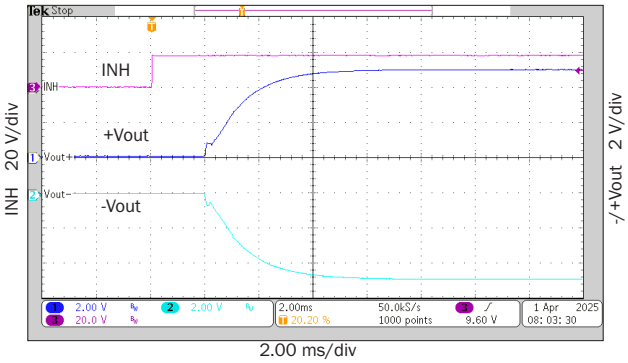
100 µs/div
cMOR2828S step load response

FIGURE 60



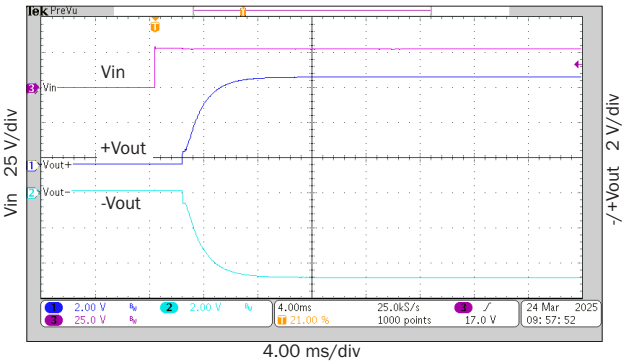
100 µs/div
cMOR2828S step load response

FIGURE 61



2.00 ms/div
cMOR2805D inhibit release

FIGURE 62

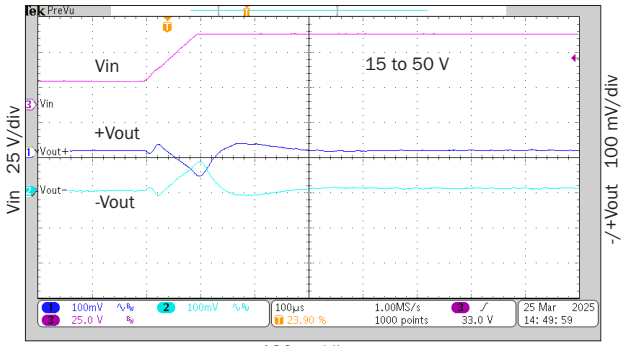


4.00 ms/div
cMOR2805D start-up response

FIGURE 63

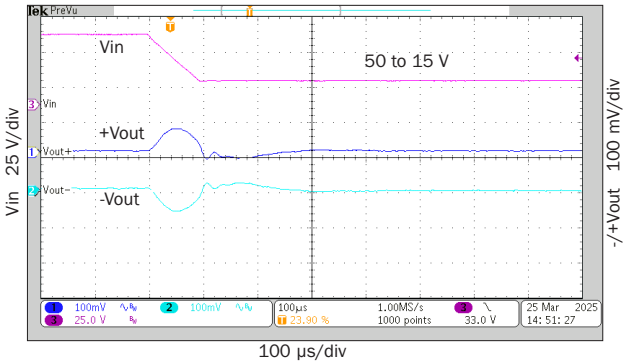
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



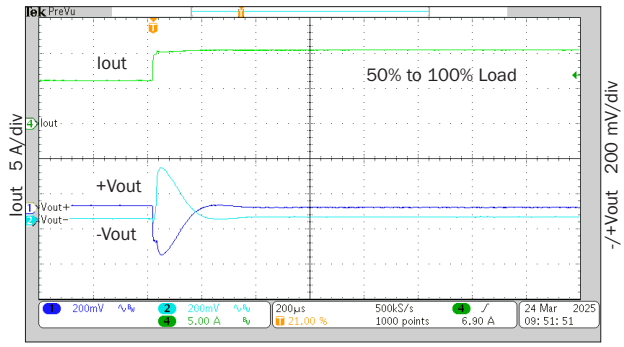
100 µs/div
cMOR2805D step line response

FIGURE 64



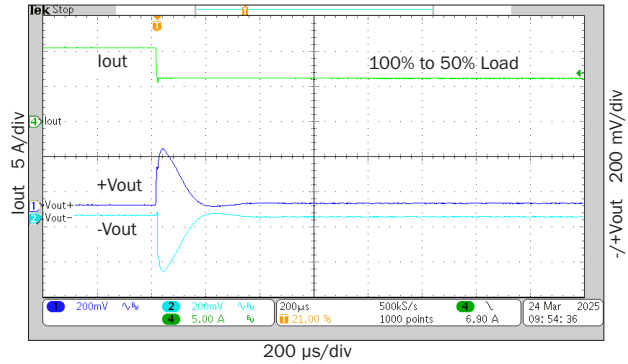
100 µs/div
cMOR2805D step line response

FIGURE 65



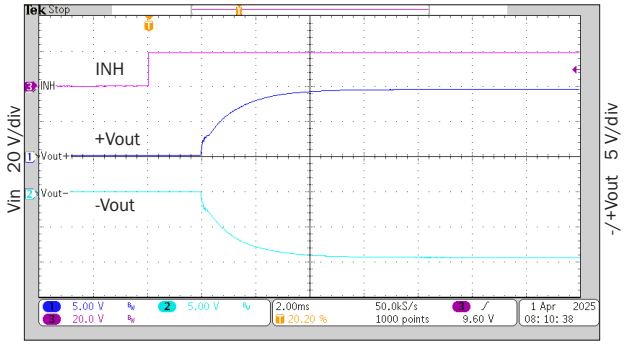
200 µs/div
cMOR2805D step load response

FIGURE 66



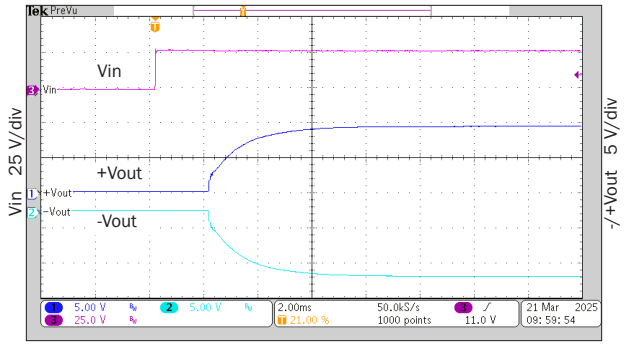
200 µs/div
cMOR2805D step load response

FIGURE 67



2.00 ms/div
cMOR289R5D inhibit release

FIGURE 68

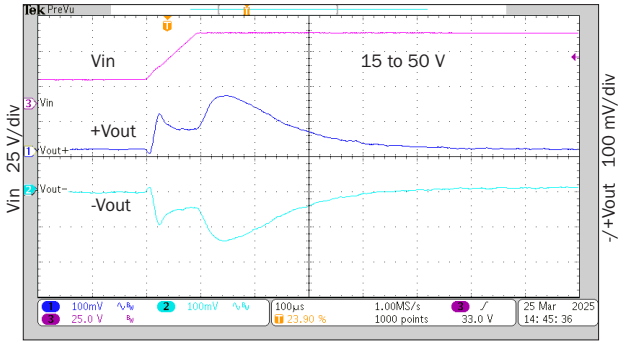


2.00 ms/div
cMOR289R5D start-up response

FIGURE 69

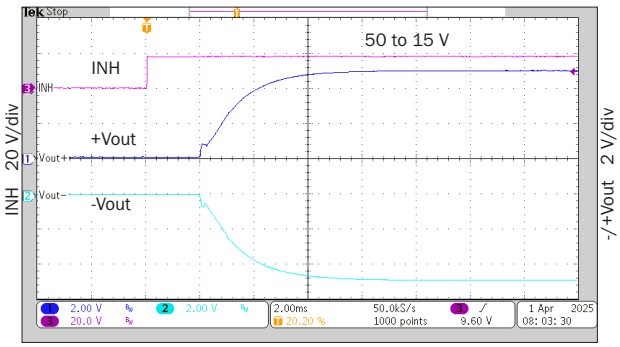
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



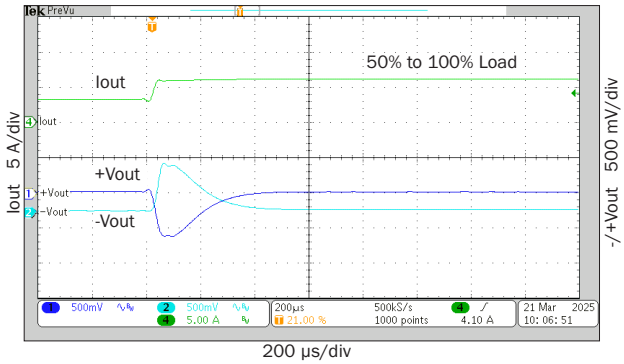
100 µs/div
cMOR289R5D step line response

FIGURE 70



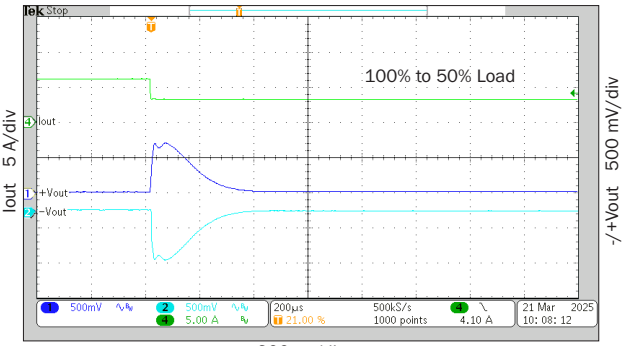
2.00 ms/div
cMOR289R5D step line response

FIGURE 71



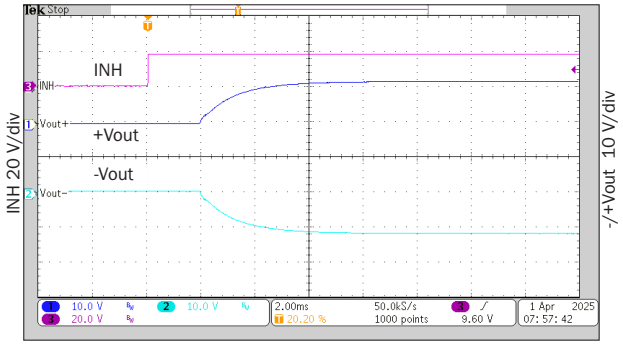
200 µs/div
cMOR289R5D step load response

FIGURE 72



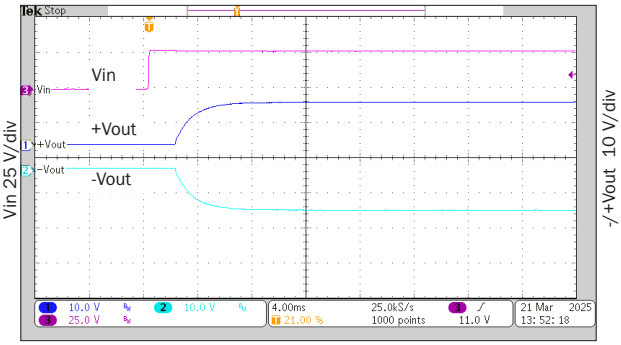
200 µs/div
cMOR289R5D step load response

FIGURE 73



2.00 ms/div
cMOR2812D inhibit release

FIGURE 74

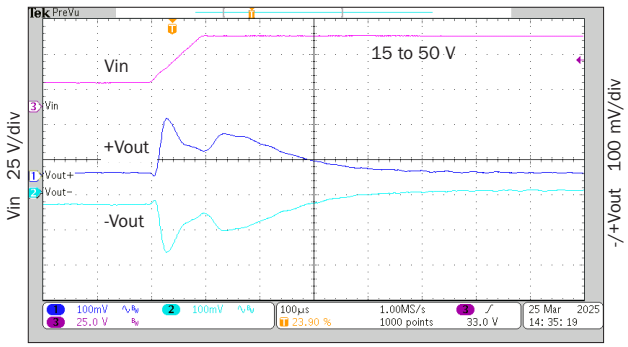


4.00 ms/div
cMOR2812D start-up response

FIGURE 75

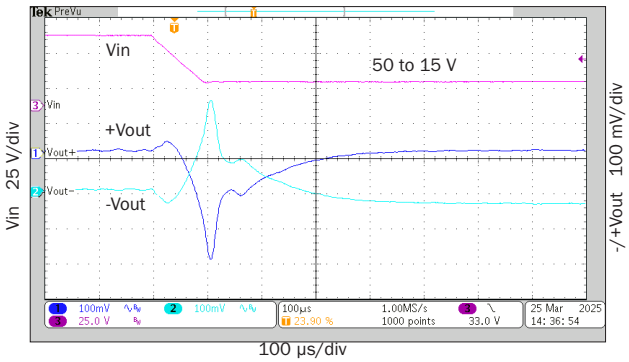
cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT



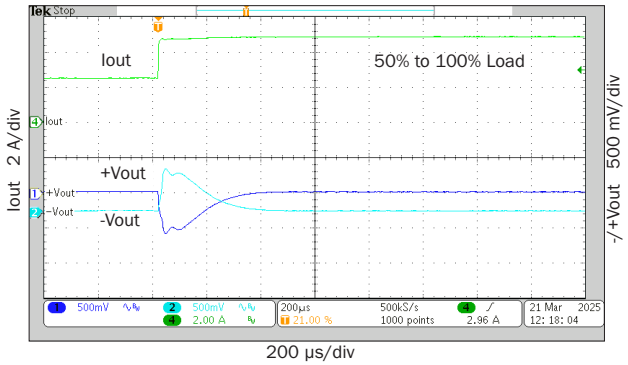
100 µs/div
cMOR2812D step line response

FIGURE 76



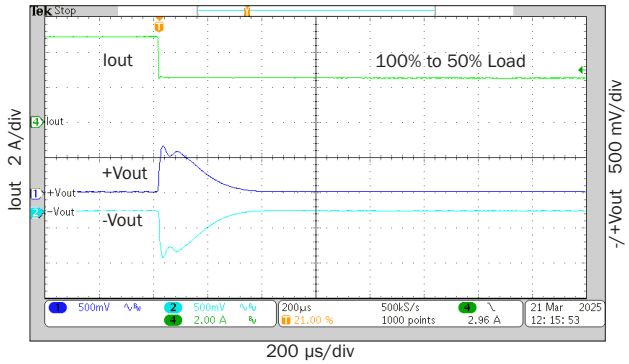
100 µs/div
cMOR2812D step line response

FIGURE 77



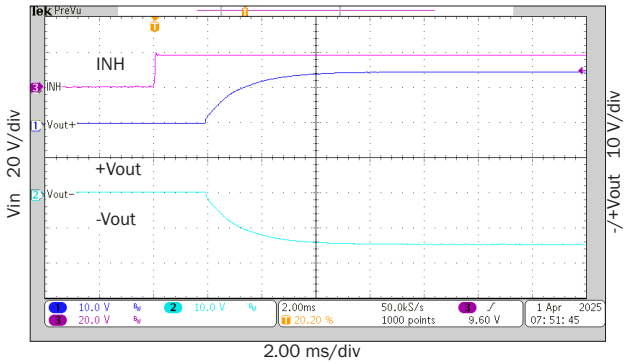
200 µs/div
cMOR2812D step load response

FIGURE 78



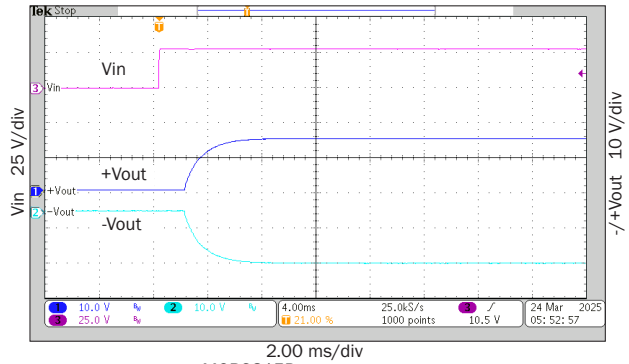
200 µs/div
cMOR2812D step load response

FIGURE 79



2.00 ms/div
cMOR2815D inhibit release

FIGURE 80

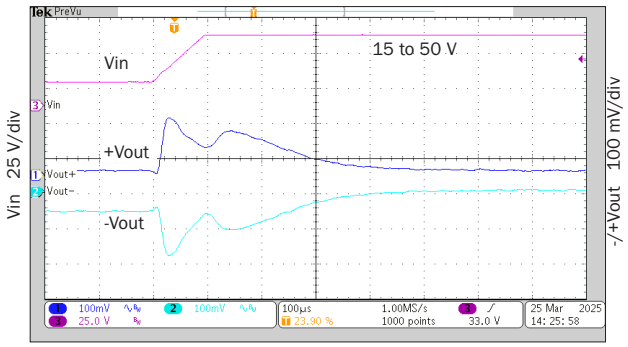


2.00 ms/div
cMOR2815D start-up response

FIGURE 81

cMOR Single and Dual DC-DC Converters

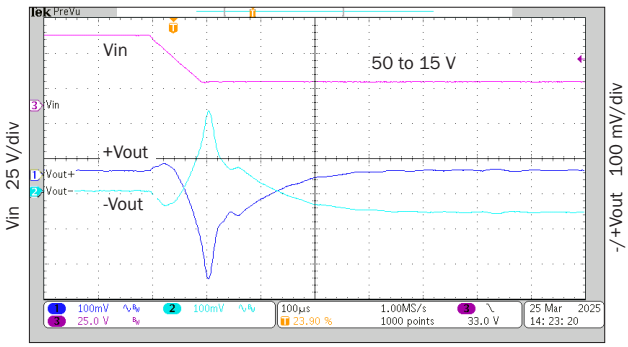
15 TO 50 VOLT INPUT - UP TO 120 WATT



100 µs/div

cMOR2815D step line response

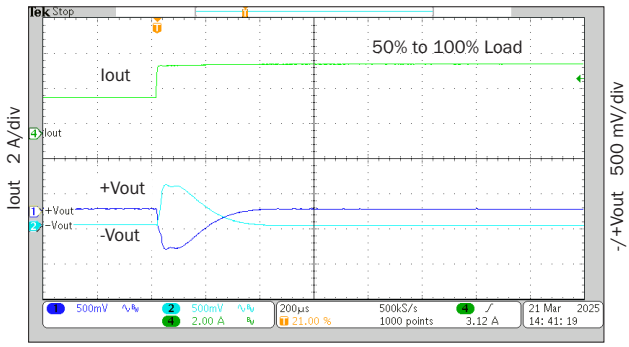
FIGURE 82



100 µs/div

cMOR2815D step line response

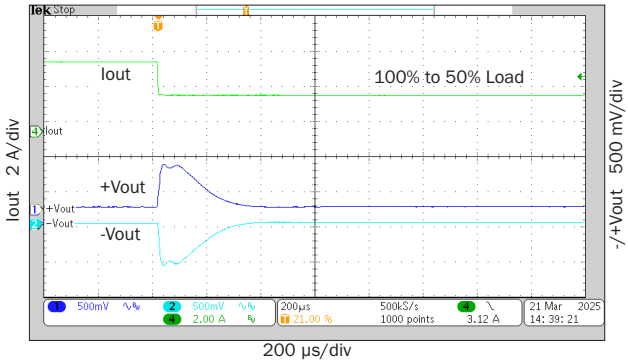
FIGURE 83



200 µs/div

cMOR2815D step load response

FIGURE 84



200 µs/div

cMOR2815D step load response

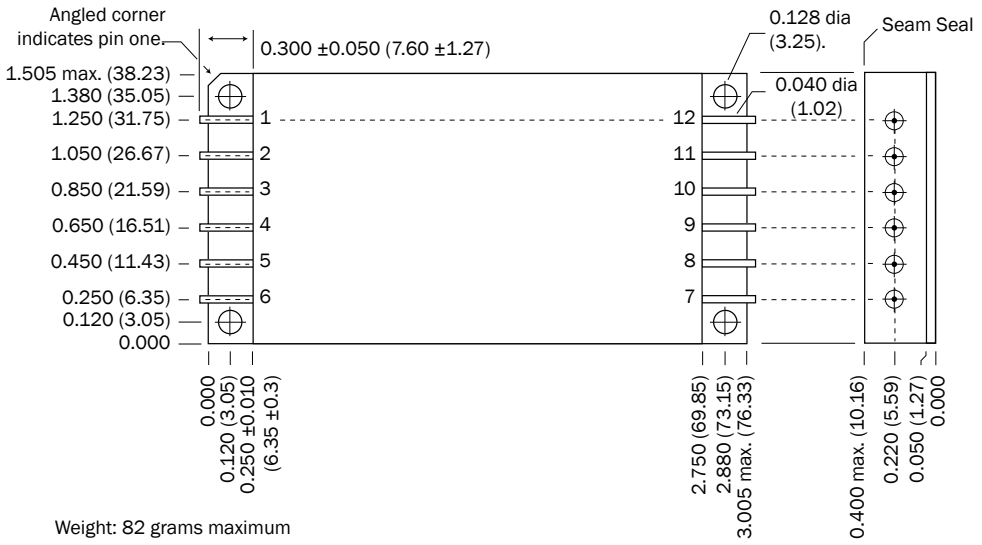
FIGURE 85

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

TOP VIEW CASE U2
Flanged case, short leads

Case “U2” does not require a designator in the Case Option position of the model number for the cMOR family



Weight: 82 grams maximum

Case dimensions in inches (mm)
Tolerance ±0.005 (0.13) for three decimal places
±0.01 (0.3) for two decimal places
unless otherwise specified

CAUTION
Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Materials

Header	Cold Rolled Steel/Nickel Nickel plating of 200 microinches MIN
Cover	Kovar/Nickel
Pins	Alloy 52/Copper, compression glass seal Gold plating of 50 microinches MIN over Nickel plating of 200 microinches MIN Seal Hole: 0.098 ±0.002 (2.49 ±0.05)

Please refer to the numerical dimensions for accuracy.

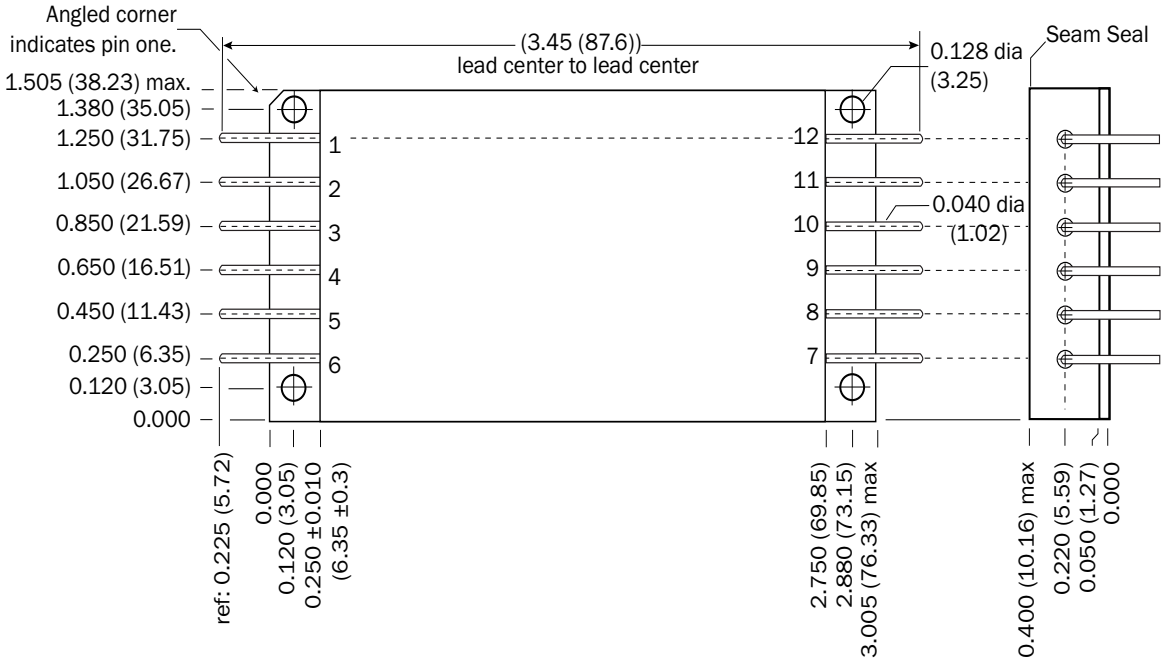
Figure 86: Case U2

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

TOP VIEW CASE V
Flanged case, down leaded

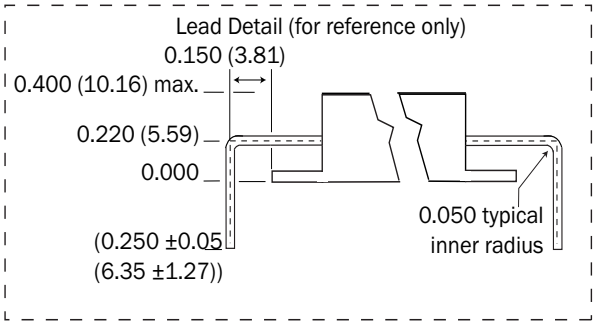
Case “V” requires a “V” in the Case Option position of the model number.



Weight: 84 grams maximum

Case dimensions in inches (mm)
Tolerance ±0.005 (0.13) for three decimal places
±0.01 (0.3) for two decimal places
unless otherwise specified

CAUTION
Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300 °C for 10 seconds per pin.



Materials

Header	Cold Rolled Steel/Nickel Nickel plating of 200 microinches MIN
Cover	Kovar/Nickel
Pins	Alloy 52/Copper, compression glass seal Gold plating of 50 microinches MIN over Nickel plating of 200 microinches MIN Seal Hole: 0.098 ±0.002 (2.49 ±0.05)

Please refer to the numerical dimensions for accuracy.

Case V, Rev M, 60312-001, 2021.04.05 font size

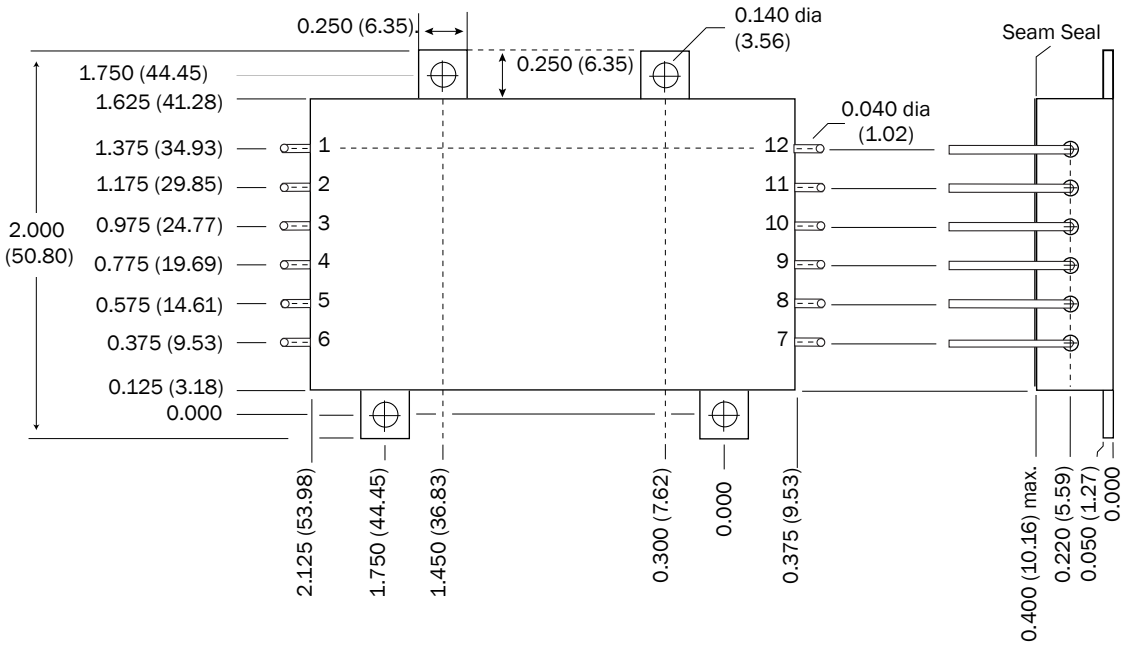
FIGURE 87: CASE V

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

TOP VIEW CASE W
Tabbed case, up-leaded

Case “W” requires a “W” in the Case Option position of the model number.



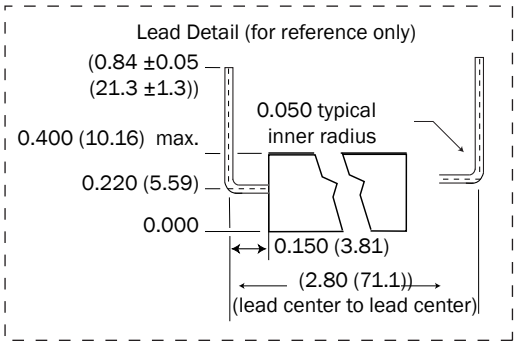
Weight: 79 grams maximum

Case dimensions in inches (mm)
Tolerance ±0.005 (0.13) for three decimal places
±0.01 (0.3) for two decimal places
unless otherwise specified

CAUTION
Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Materials

Header	Cold Rolled Steel/Nickel
	Nickel plating of 200 microinches MIN
Cover	Kovar/Nickel
Pins	Alloy 52/Copper, compression glass seal
	Gold plating of 50 microinches MIN over
	Nickel plating of 200 microinches MIN
	Seal Hole: 0.098 ±0.002 (2.49 ±0.05)



Please refer to the numerical dimensions for accuracy.

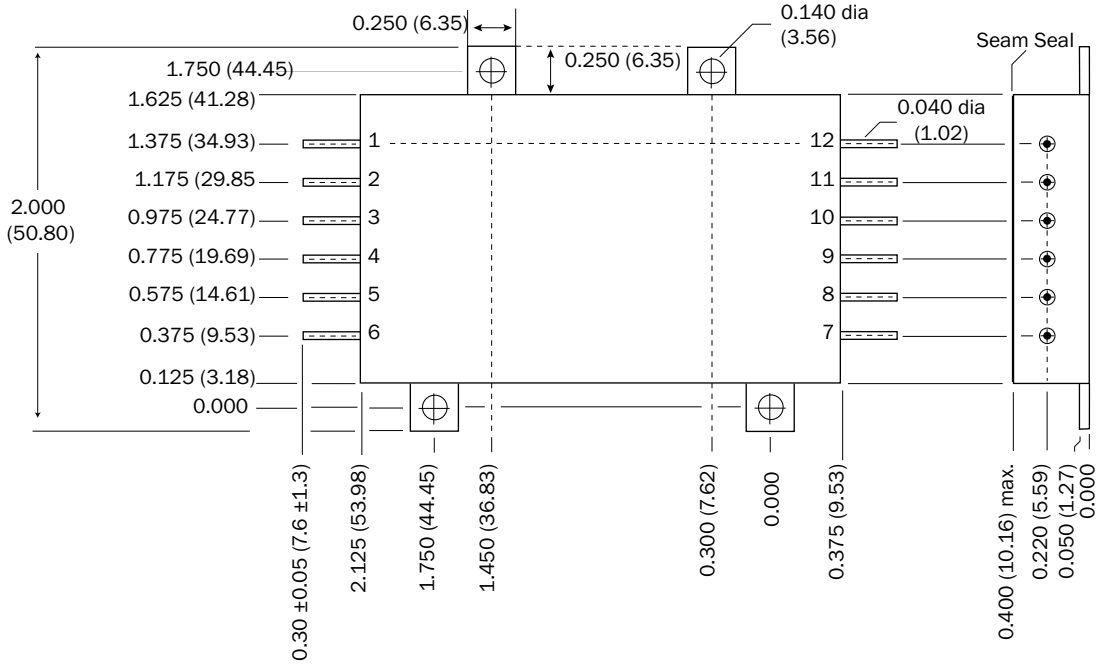
FIGURE 88: CASE W

cMOR Single and Dual DC-DC Converters

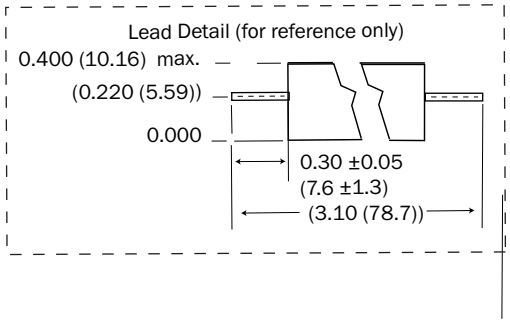
15 TO 50 VOLT INPUT - UP TO 120 WATT

TOP VIEW CASE Y
Tabbed case, straight-leaded

Case “Y” requires a “Y” in the Case Option position of the model number.



Weight: 79 grams maximum



Case dimensions in inches (mm)
Tolerance ±0.005 (0.13) for three decimal places
±0.01 (0.3) for two decimal places
unless otherwise specified

CAUTION
Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Materials

Header	Cold Rolled Steel/Nickel/ Nickel plating of 200 microinches MIN
Cover	Kovar/Nickel
Pins	Alloy 52/Copper, compression glass seal Gold plating of 50 microinches MIN over Nickel plating of 200 microinches MIN Seal Hole: 0.98 ±0.002 (2.49 ±0.05)

Please refer to the numerical dimensions for accuracy.

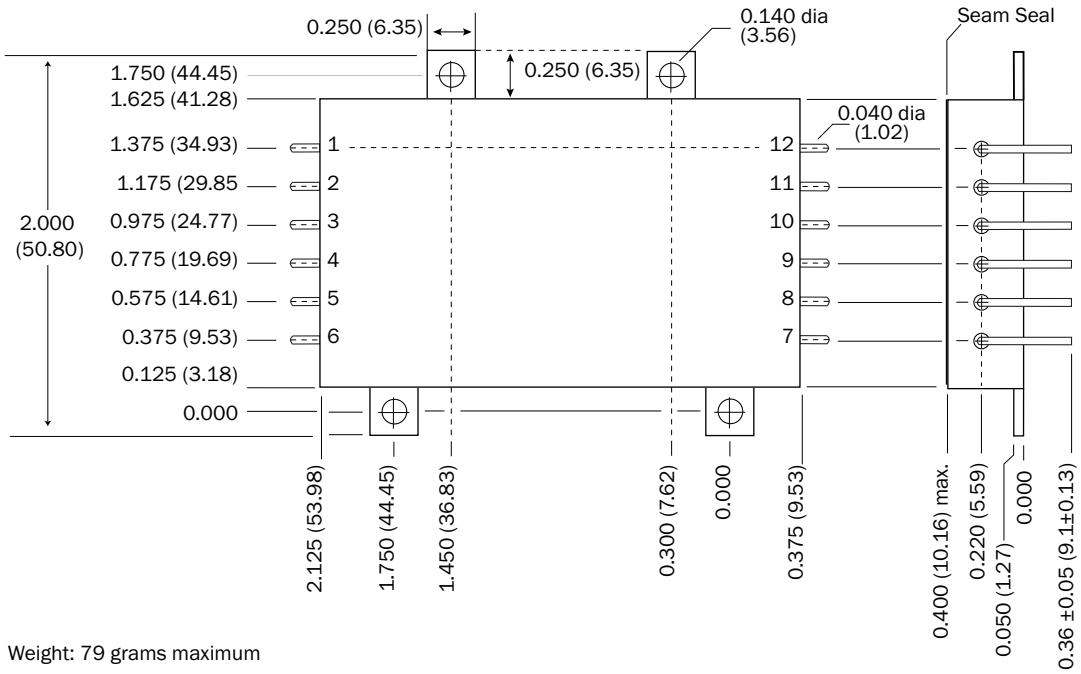
FIGURE 89: CASE Y

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

TOP VIEW CASE Z
Tabbed case, down-leaded

Case “Z” requires a “Z” in the Case Option position of the model number.



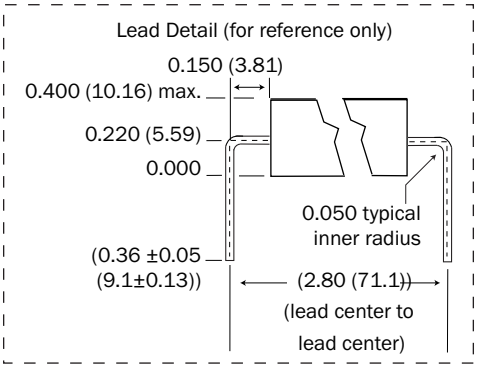
Weight: 79 grams maximum

Case dimensions in inches (mm)
Tolerance ±0.005 (0.13) for three decimal places
±0.01 (0.3) for two decimal places
unless otherwise specified

CAUTION
Heat from reflow or wave soldering may damage the device.
Solder pins individually with heat application not exceeding 300 °C for 10 seconds per pin.

Materials

Header	Cold Rolled Steel/Nickel Nickel plating of 200 microinches MIN
Cover	Kovar/Nickel
Pins	Alloy 52/Copper, compression glass seal Gold plating of 50 microinches MIN over Nickel plating of 200 microinches MIN Seal Hole: 0.098 ±0.002 (2.49 ±0.05)



Please refer to the numerical dimensions for accuracy.

FIGURE 90: CASE Z

cMOR Single and Dual DC-DC Converters

15 TO 50 VOLT INPUT - UP TO 120 WATT

CMOR PRODUCT QUALIFICATION REQUIREMENTS				
Subgroup	**Test**	**883 Test Method**	**Conditions**	**QTY (Accept Number)**
1	Resistance to Soldering Heat	2036		5(0)
	External Visual	2009		
	Temperature Cycling	1010	B, 20 cycles	
	Mechanical Shock	2002	A, Y1 direction	
	Constant Acceleration	2001	500g	
	Random Vibration	2026	F	
	Seal (Gross Leak, Dip)	1014		
	End-point-Electrical	Per ATP		
2	Steady-state life test	1005	1000 hours at +105C	22(0)
	End-point Electrical	Per ATP		
4	Internal Visual	IPC-A-610 Class 3		5(0)
5	ESD	3015		3(0)
	End-point Electrical	Per ATP		

TABLE 9: PRODUCT QUALIFICATION REQUIREMENTS

Environmental Screening COTS, Standard and /ES

Test Performed	Standard	/ES
Pre-cap Inspection, IPC - 610 Class-3	■	■
Temperature Cycle (10 times)		
MIL-STD-883, Method 1010, Cond. B, -55°C to +105°C, ambient		■
Constant Acceleration		
Method 2001, 300 g		■
Burn-in Method 1015, +105°C case, typical		
24 hours	■	
96 hours		■
Final Electrical Test		
-55°C, +25°C, +105°C case	■	■
Hermeticity Test, Method 1014		
Gross Leak, Cond. C_1, fluorocarbon		■
Gross Leak, Dip	■	
Final visual inspection, Method 2009	■	■

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

Notes
1. Standard and /ES products may not meet all of the requirements of MIL-PRF-38534.
2. Burn-in temperature designed to bring the case temperature to the maximum case temperature of the product. Refer to the specific product information for the maximum case temperature. Burn-in is a powered test.

TABLE 10: ENVIRONMENTAL SCREENING HIGH RELIABILITY COTS DC-DC CONVERTERS COTS, STANDARD AND /ES