

Maximum Flexible Power (MFP) Single Output Point of Load

MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

MAXIMUM FLEXIBLE POWER (MFP) IN A SINGLE 7 AMP POINT OF LOAD.
A USE-ANYWHERE POWER SOLUTION FOR DIGITAL AND NON-DIGITAL SYSTEMS.

FEATURES

- Radiation hardened space DC-DC converter
 - Single event effects (SEE) LET performance to 86 MeV cm²/mg
 - Total ionizing dose (TID) guaranteed per method 1019 of MIL-STD-883, radiation hardness assurance (RHA) P = 30 krad(Si),
 - 50 - 300 rad(Si)/sec dose rate
 - LDR tested to 30 krad at 10 mrad(Si)/sec
- No external components required
- Up to 92% typical efficiency, flat down to 30% load
- Vin 3.0 to 6.0 volts dc, transients up to 15 volts for 1 sec.
- Inhibit and sync functions
- Current monitoring
- Current sharing pin for parallel operation
- Four pin-selectable, preset voltages, 0.8, 1.6, 2.5 and 3.3
- Output voltage adjustable from 0.8 to 3.5 volts
- Indefinite output short circuit protection
- Adjustable start-up sequencing
- Remote sense and voltage margining
- Internal solid state power switch provides many benefits including inrush current limiting

DESCRIPTION

The Interpoint® MFP Series™ of DC-DC converters offers up to 16.5 watts of power in a radiation hardened design. (See Table 9 on page 11.) The low profile MFP converters are manufactured in our certified and qualified MIL-PRF-38534 Class K production facility and our Class H production facility. They packaged in hermetically sealed steel cases and are ideal for use in programs requiring high reliability, small size, and high levels of radiation hardness assurance. The MFP Series is a high-efficiency point of load converter with a 3.3 or 5 volt input, an undervoltage shutdown below 3.0 volts and an overvoltage shutdown above 6.0 volts.

The non-isolated, feature-rich MFP uses a buck converter design with synchronous rectification. The design allows the unit to operate synchronously to no output load, ensuring high efficiency at the lightest loads without switching off the synchronous devices. Important features include a solid state switch, inrush current limiting, synchronization with an external system clock and the ability to current share allowing multiple devices to supply a common load.

The MFP includes an internal house keeping supply that is active at inputs as low as 2 volts and provides a boosted and regulated voltage supply for internal use. This internal supply is one of the reasons that this product can provide full power at very high efficiency at input voltages as low as 3 volts. No external power source or external bias is required.

The MFP converters are designed for the large, fast transient load currents typical to digital loads. See Figure 3 on page 10. The MFP Series is intended to be powered by a fully regulated power source.

HISTORY OF PROVEN PERFORMANCE

Crane Aerospace & Electronics, Power Solutions was issued its first standard microcircuit drawing (SMD) in 1992 for an Interpoint® Class H hybrid. Our first Class K hybrid SMD was issued in 1997 and we were one of the first companies to certify manufacturing to Class K. Our facility has a Defense Logistics Agency (DLA) approved Radiation Hardness Assurance (RHA) plan.

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ELECTRICAL CHARACTERISTICS:

-55 to +125 °C T_C , 5 V_{IN} , 3.3 V_{OUT} , nominal frequency, unless otherwise specified.

TABLE 1: ABSOLUTE MAXIMUM RATINGS

PARAMETER	CONDITION	FIGURE	SYMBOL	MIN	TYP	MAX	UNITS
OPERATING TEMPERATURE	All	—	T_C	-55	—	+125	°C
STORAGE TEMPERATURE	All	—	T_{STG}	-65	—	+150	°C
DERATING OUTPUT POWER/CURRENT	Linearly	From 100% at 125 °C to 0% at 135 °C					
MAXIMUM WEIGHT - MFP0507S/BP-MOD	—	Figure 1	—	—	—	26	grams
ESD RATING ¹ MIL-PRF-38534, 3.9.5.8.2, MIL-STD-883	METHOD 3015	—	—	Class 1B 500 - 999 ²			V
LEAD SOLDERING TEMPERATURE ²	10 seconds max	—	—	—	—	300	°C

Table 1 Notes

1. Passed 500 volts.

2. Caution: Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300 °C for 10 seconds per pin.

TABLE 2: INPUT SPECIFICATIONS

PARAMETER	STATE	CONDITION	FIGURE	SYMBOL	MIN	TYP	MAX	UNITS
Input Voltage Range See Note 1	Continuous		Figure 5	V_{IN}	0	5.0	6.0	V
	Operating ^{2, 3}	$V_{OUT} = V_1, V_2$	Figure 5	V_{IN}	3.0	3.3 or 5.0	6.0	V
		$V_{OUT} = V_1, V_2, V_3$	Figure 5		3.3	3.3 or 5.0	6.0	
		$V_{OUT} = V_1, V_2, V_3, V_4$	Figure 5		4.5	5.0	6.0	
Transient		Figure 5	$V_{IN\ TRAN}$	—	—	15	V	
Input Current	No Load	$V_{IN} 3.3\ V, V_{OUT} 0.8\ V$	—	I_{IN}	—	210	330	mA
		$V_{IN} 5.0\ V, V_{OUT} 3.3\ V$	—		—	135	180	
	Disabled ⁴	$V_{IN} 3.3\ V$	—		—	105	145	
		$V_{IN} 5.0\ V$	—		—	50	90	
Enable/Disable Input	Open Circuit Voltage	Pin 1 Open $V_{IN} 3.0\ to\ 6.0\ V$	Figure 4	$V_{PIN\ 1}$	1.6	2.0	2.6	V
	Threshold Unit Enabled	$V_{IN} 3.0\ to\ 6.0\ V$	Figure 4	$V_{PIN\ 1}$	2.3	—	—	
	Threshold Unit Disabled				—	—	1.4	
	ENABLE Pin Current Unit Disabled				$I_{PIN\ 1}$	—	—	2
External Synchronization	Standard Sync Range	$I_{OUT} 5\ A$	Figure 4	—	270	—	600	kHz
	Amplitude		—	—	3.0	5.0	6.0	V
	Duty Cycle ⁵		—	—	40	50	60	%
	Frequency Source Impedance		—	—	—	—	—	50

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TABLE 2: INPUT SPECIFICATIONS (CONTINUED)

PARAMETER	STATE	CONDITION	FIGURE	SYMBOL	MIN	TYP	MAX	UNITS
Switching Frequency	-55 to +125 °C	I _{OUT} 5 A	Figure 17	—	270	—	400	kHz
Power Dissipation	No Load		—	—	—	—	0.9	W
	Fault Power, Output Short ⁶		—	—	—	2.8	4.25	
Input Ripple Current 20 Hz - 20 MHz	V ₁ (0.8 V)	V _{IN} 3.3, I _{OUT} 5 A	—	—	—	200	290	mA p-p
	V ₄ (3.3 V)	V _{IN} 5.0, I _{OUT} 5 A	—	—	—	200	310	
	I _{RMS}	—	—	—	—	60	—	mA _{RMS}

Table 2 - Notes

- When powering from 0 volts to the minimum specified input voltage, it is recommended to use a voltage ramp time of 1 millisecond or less. The maximum voltage ramp time does not apply when releasing the ENABLE pin while power is applied.
- 50 microsecond minimum transition time. Transient duration less than 1 second.
- V_{OUT}: V₁ = 0.8 V, V₂ = 1.6 V, V₃ = 2.5 V, V₄ = 3.3 V
- T_{RISE}/T_{FALL} must be less than 50 ns.

TABLE 3: OUTPUT SPECIFICATIONS

PARAMETER	STATE	CONDITION	FIGURE	SYMBOL	MIN	TYP	MAX	UNITS				
Operating Voltage Accuracy ¹	V ₁ 0.8, 25 °C	2.5 A Load 20Krad post-rad⁴	Figure 3	V ₁	0.789	0.800	0.813	V _{dc}				
	V ₁ 0.8, -55 to +125 °C				0.787		0.813					
	V ₂ 1.6, 25 °C	2.5 A Load	Figure 3	V ₂	1.578	1.600	1.632					
	V ₂ 1.6, -55 to +125 °C				1.538		1.644					
	V ₃ 2.5, 25 °C				Figure 3		V ₃		2.447	2.500	2.531	
	V ₃ 2.5, -55 to +125 °C								2.381		2.546	
	V ₄ 3.3, 25 °C								Figure 3		V ₄	3.236
V ₄ 3.3, -55 to +125 °C	3.184							3.364				
Load Regulation ¹ V _{OUT} V ₁ , V ₂ , V ₃ , V ₄	25 °C	50% Full Load, Operating	—	V _{R LOAD}	—	—	6	mV				
	-55 to +125 °C				—		6					
Line Regulation ¹ V _{OUT} V ₁ , V ₄	25 °C	V ₁ V _{IN} 3-6 V, I _{OUT} 7 A	—	V _{R LINE}	—	—	20	mV				
	-55 to +125 °C	V ₄ V _{IN} 4.5-6 V, I _{OUT} 5 A			—		20					
Output Ripple and Noise 20 Hz to 20 MHz	-55 to +125 °C	V _{IN} 3.3 V, V _{OUT} 0.8 V, I _{OUT} 5 A	Figure 12	V _{OUT-RIP}	—	25	70	mV p-p				
	-55 to +125 °C	V _{IN} 5.0 V, V _{OUT} 3.3 V, I _{OUT} 5 A	Figure 13		—		40		80			
Output Current ¹	V ₁ 0.8, -55 to +125 °C	V _{IN} 3.0 to 6.0 V	Figure 7	I _{OUT}	0	—	7.0	A				
	V ₂ 1.6, -55 to +125 °C				0		6.4					
	V ₃ 2.5, -55 to +125 °C				0		5.0					
	V ₄ 3.3, -55 to +125 °C				0		5.0					

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-55 to +125°C T_C, 5 V_{IN}, 3.3 V_{OUT}, nominal frequency, unless otherwise specified.

TABLE 3: OUTPUT SPECIFICATIONS (CONTINUED)

PARAMETER	STATE	CONDITION	FIGURE	SYMBOL	MIN	TYP	MAX	UNITS
Output Power ¹	V ₁ 0.8, -55 to +125°C	V _{IN} 3.0 to 6.0 V	Figure 7	P _{OUT}	0	—	5.6	W
	V ₂ 1.6, -55 to +125°C				0	—	10.2	
	V ₃ 2.5, -55 to +125°C	V _{IN} 3.0 to 6.0 V			0	—	12.5	
	V ₄ 3.3, -55 to +125°C	V _{IN} 4.5 to 6.0 V			0	—	16.5	
External Load Capacitance ²	—	—	—	—	—	—	5000	μF
Efficiency ¹	V ₁ 0.8, 25°C	I _{OUT} 5 A	Figure 9	EFF ₁	70.0	73	—	%
	V ₁ 0.8, -55 to +125°C				67.4	—	—	
	V ₂ 1.6, 25°C		Figure 9	EFF ₂	81.8	84	—	%
	V ₂ 1.6, -55 to +125°C				80.0	—	—	
	V ₃ 2.5, 25°C		Figure 9	EFF ₃	87.3	89	—	%
	V ₃ 2.5, -55 to +125°C				85.8	—	—	
	V ₄ 3.3, 25°C		Figure 9	EFF ₄	90.1	92	—	%
V ₄ 3.3, -55 to +125°C	88.8	—			—			
Turn On Peak Deviation, V _{OUT} , V ₄ ^{1, 3}	Release of Enable	I _{OUT} No Load to Full	—	—	—	—	50	mV pk
	V _{IN} Step Start	V _{IN} 0 to 5.0 V	—	—	—	—	50	
Turn On Settling Time to 2%, V _{OUT} , V ₄ ^{1, 3}	Release of Enable	I _{OUT} No Load to Full	—	—	—	3.2	5	ms
	V _{IN} Step Start	V _{IN} 0 to 5.0 V	—	—	—	3.2	5	
Output Load Transient Response	V _{IN} 3.3 V, V _{OUT} 0.8 V	I _O 2.5 to 5 A @ 1 A / μs settle to 2%	Figure 14 Figure 15	—	—	350	400	μs
	V _{IN} 5.0 V, V _{OUT} 3.3 V			—	—	140	200	
Load Transient Peak Deviation	V _{IN} 3.3 V, V _{OUT} 0.8 V	I _O 2.5 to 5 A @ 1 A/μs settle to 2%	Figure 14 Figure 15	—	—	210	244	mV pk
	V _{IN} 5.0 V, V _{OUT} 3.3 V			—	—	150	220	
Output Voltage Trim ¹ V ₁ , V ₂ , V ₃ , V ₄	—	See Table 7 on page 15						
Sequence Time Delay	See Table 6 on page 11							
MTBF	MFP0507S/BP-MOD	AIF @ 55°C	—	—	—	6722	—	kHrs

Table 3 – Notes

- V_{OUT} is defined by the subscript after the V: V₁ = 0.8 V, V₂ = 1.6 V, V₃ = 2.5 V, V₄ = 3.3 V, **unless otherwise noted.**
- Guaranteed stable up to maximum capacitance.
- Test condition at V_{OUT} at 3.3 V based on worst case setpoint condition.
- Radiation performance is not tested or measured during production. Radiation performance is guaranteed through product characterization. Post 20Krad limits are 0.785 min to 0.815 max.**

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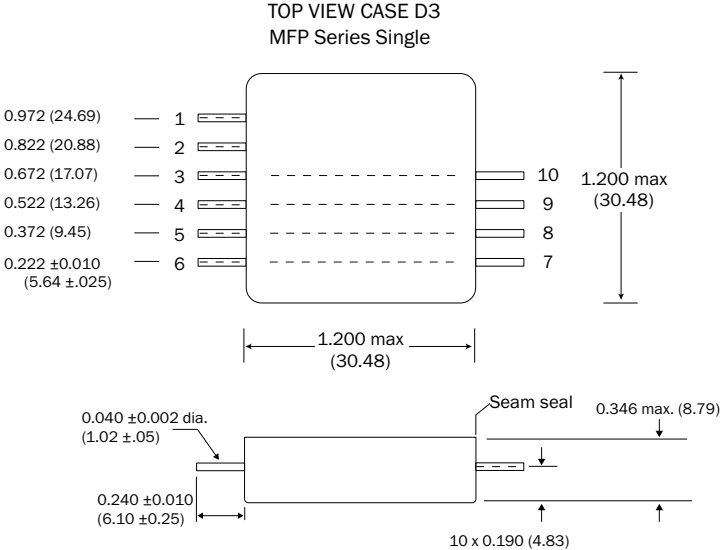
PIN OUT			
Pin Number	Designation	Function	If Pin is not Used
1	ENABLE	Enable, provides remote turn on and off	Leave open
2	+V IN	Positive Input	Always used
3	V IN COM	Input Common	Always used
4	SYNC	Synchronization	Leave open
5	SENSE	Sense, voltage drop compensation	Connect to + V _{OUT} pin 10
6	SHARE	Current Share, parallel operation, or current monitor	Leave open
7	TRIM A	Preset Output Voltage and Trim	See Figure 3 on page 10 and Table 7 on page 15
8	TRIM B	Preset Output Voltage and Trim	See Figure 3 on page 10 and Table 7 on page 15
9	V OUT COM	Output Common (also SENSE Return)	Always used
10	+V OUT	Positive Output	Always used

TABLE 4: MFP0507S/BP-MOD PIN OUT

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MECHANICAL INFORMATION



Weight: 26 grams maximum

Case dimensions in inches (mm)
 Tolerances, unless otherwise specified,
 X.XXX (X.XX) = ±0.005 (0.13)
 X.XX (X.X) = ±0.01 (0.30)
 Pin and hole placement ±0.0035 (±0.089)

CAUTION
 Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300 °C for 10 seconds per pin.

Materials
 Header - Cold Rolled Steel/Nickel
 Cover - Kovar/Nickel
 Pins - 3:1 Cu Cored alloy 52/Gold over Nickel, compression glass seal
 Gold plating of 50 - 150 microinches included in pin diameter
 Seal hole: 0.091 ±0.003 (2.31 ±0.08)

Please refer to the numerical dimensions for accuracy.

FIGURE 1: MFP0507S/BP-MOD CASE DIMENSIONS

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THERMAL AND MOUNTING CONSIDERATIONS

THERMAL CONSIDERATIONS

The MFP is designed to be mounted close to the point-of-use which, in many cases, may be on a printed circuit board. The high efficiency of the MFP reduces the issues normally associated with the converter's internal dissipation. The maximum internal dissipation occurs when the product is configured as a 3.3 volt output at full load. This condition will result in a dissipation of not more than 1.78 watts. This dissipation is nearly uniformly distributed over the base area of 1.4 square inches. Full load power loss is largely independent of output voltage, for instance at 0.8 volts and full load the internal power loss maximum is again 1.78 watts.

In order to determine the cooling or heat sinking requirements in the application, the maximum product power dissipation should be calculated from the product efficiency and output power. Graphs and tabled values in the specification table can be used to find the efficiency given the input voltage, selected output voltage and output load. The internal dissipation, difference between output and input power, can be calculated from the equation below. where:

P_{OUT} = output power

ϵ = efficiency

$$P_{DISS} = P_{OUT} (1 - \epsilon) / \epsilon$$

ϕ = thermal resistance of converter attachment to board

$$\Delta T = P_{DISS} \times \phi$$

$$T_{CASE} = T_{BASE} + \Delta T$$

Many applications will not require special efforts at cooling, however, this depends on ambient temperatures, adjacent components, and other factors.

If product cooling is required for safe operation convection and/or conduction can be used. Thermal considerations require that the base of the MFP be maintained at a safe temperature of less than the maximum rating. All components internal to the MFP are bonded to the metal base of the package. The base is the surface that is important if conduction cooling is used. It is a good practice to bond the device to the PCB or mounting surface with a thermally conductive pad. Such pads provide some degree of conduction cooling to the mounting surface depending on the amount of voiding at the interface. In the case of the side leaded MFP package, this thermal pad will firmly locate the device to the surface so that the lead connections only manage the electrical requirements and not the mechanical requirements.

In Figure 2 below, the thermal rise internal to the MFP can be seen to be only 6°C. This low thermal rise gives the end user more flexibility in board design options to meet applicable derating guidelines.

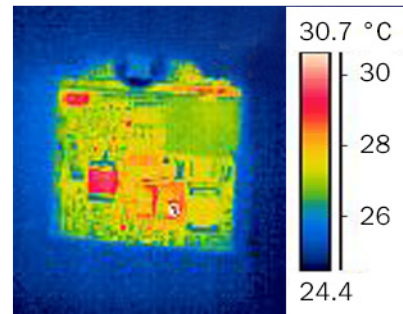


FIGURE 2: INFRARED IMAGE MFP AT FULL LOAD WITH 6 V_{IN}, 3.3 V_{OUT}

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THERMAL AND MOUNTING CONSIDERATIONS (CONTINUED)

MOUNTING CONSIDERATIONS

The recommended mounting material is Chomerics double-sided adhesive materials for attachment of the MFP to a circuit board or metal surface.

Because of the MFP's efficiency the thermal characteristics of the Chomerics materials are not required even though the Chomerics material provides good thermal conductivity.

The following information refers to products attached using Chomerics double-sided adhesive.

Vibration Testing

The MFP was tested in Random vibration using both the T1680 and T404 to mount the units to an aluminum vibration fixture. Testing was performed to the most severe level in MIL-STD-883 Method 2026; Condition 2, Letter K, overall G_{RMS} 51.1, for 15 minutes per axis, 3 axes and passed. No mounting detachment occurred.

Application

Recommended size for the adhesive tapes is 1.18 x 1.18 inches. Application of the tapes is a matter of peeling the release liners and attaching to the MFP and circuit board respectively. See Chomerics data sheets and application notes for details. The T404 material does require higher application pressure. The T1680 material is specifically made for low pressure attachment of hybrids, ceramic and flat packages.

Removal

Refer to Chomerics application notes for Thermattach Tape.

Specifications

Refer to Table 5: Chomerics Material Specifications for thermal conductivity, temperature range and out-gassing.

TABLE 5: CHOMERIC MATERIAL SPECIFICATIONS

MATERIAL	THERMAL CONDUCTIVITY	TEMPERATURE RANGE	OUTGASSING DATA		MOUNTING APPLICATION
	W/M-K	°C	% TVM	% CVCM	
CHO-THERM 1671 (Note 1)	2.6	-60 to +200	0.76	0.07	Rougher surfaces
CHO-THERM T1680 (Note 2)	0.65	-60 to +200	1.27	0.23	Smooth surfaces
THERMATTACH T404 (Note 2)	0.4	-30 to +125	0.53	0.02	Smooth surfaces

Table 5 - Notes

- 1. Chomerics Cho-Therm 1671 is a good choice for mounting on rougher surfaces. This material has a fiberglass barrier with PSA on one side. It can be obtained with PSA on both sides if needed.
- 2. Chomerics Thermattach Tape T404 and Cho-Therm T1680 are two excellent choices for circuit board mounting. Both have a Kapton insulating barrier with pressure sensitive adhesive (PSA) on both sides.

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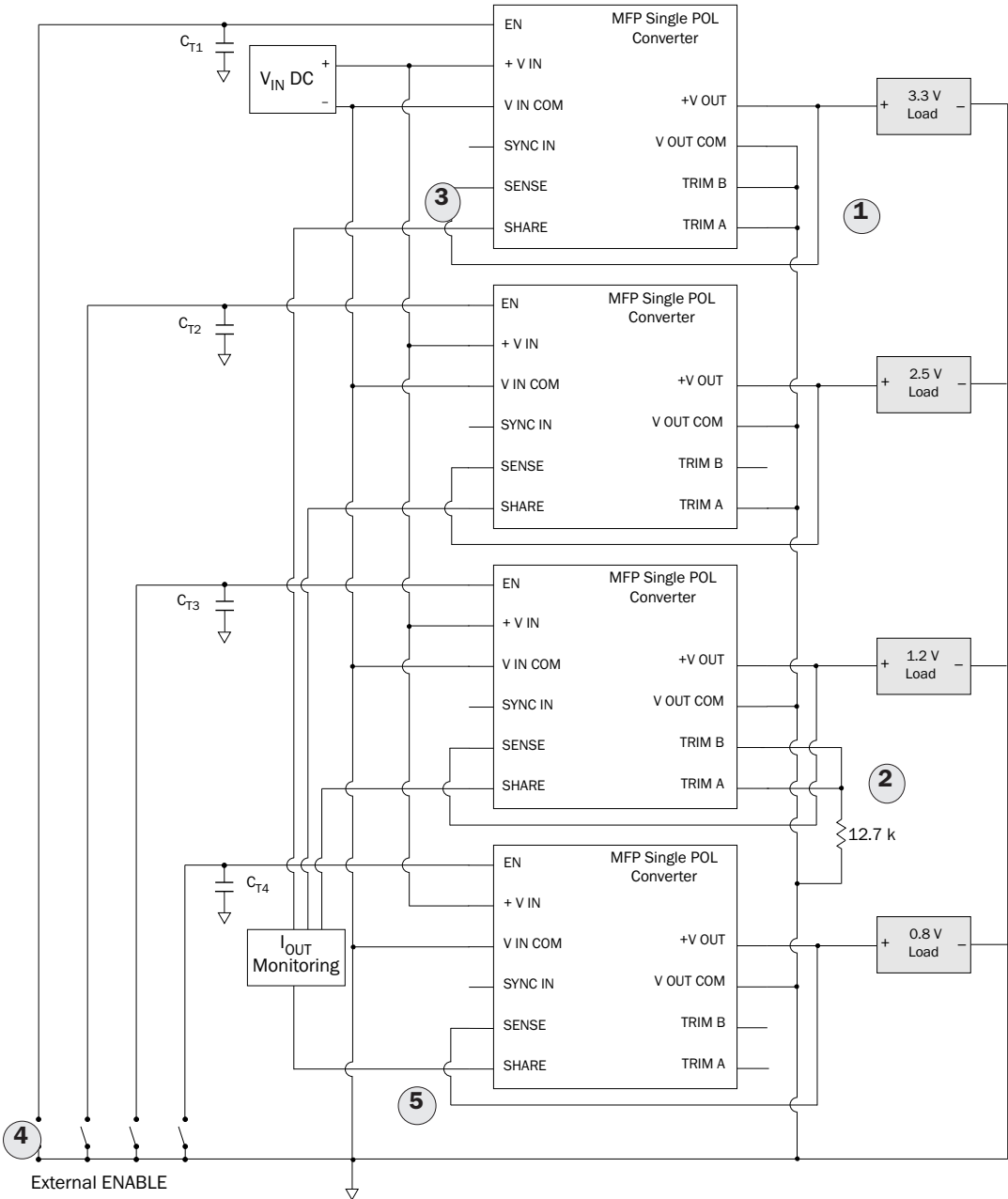


FIGURE 3: TYPICAL CONNECTION DIAGRAM
ENABLE, TRIM, SENSE, AND I_{OUT} MONITORING

For more information:

- ① ② TRIM section
- ③ Remote SENSE section
- ④ ENABLE section, sequencing. Table 6 on page 11 lists C_T values.
- ⑤ SHARE section

DESIGN RECOMMENDATIONS

The MFP is designed to be used with a low impedance power source. If the inductance to the MFP input is not negligible, it is recommended that the inductance is kept to less than 1 uH per MFP converter. For additional information please contact Applications Engineering application support at powerapps@craneae.com.

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ENABLE

The MFP provides an enable pin that will allow normal power conversion to occur if left open or unconnected. The ENABLE pin allows remote turn-on and turn-off control of the MFP. Connecting this pin to ground will disable power conversion, resulting in no output voltage and greatly reduced current consumption. The MFP ENABLE function will work with an open collector device connected to the pin or with a logic high voltage from a digital device as long as the logic high voltage is greater than the minimum voltage listed in the specification for enabled operation.

The enable pin is active high at ≥ 2.3 volts or with a floating input.

Sequencing: The start-up of the MFP can be delayed with the addition of an external capacitor connected to the ENABLE pin as shown in Figure 6. This feature is useful in sequencing the start-up of multiple point of load converters in a system requiring a specific startup sequence for various low-voltage loads. The startup delay is roughly equal to 1 millisecond per microfarad of capacitance. More precise external capacitance values can be found in Table 6 where it can be seen that there is a variation in startup delay time as the input voltage varies. The listed delay is from the beginning of application of power to the beginning of internal power conversion. There will be an additional delay as the power converter begins a normal start-up sequence and ramps to final output voltage.

SYNC

The MFP includes a synchronization feature, a key capability in low noise system design. The internal conversion oscillator can be synchronized with a system clock or with a bus voltage source. The MFP is designed to synchronize with a 300 kHz system but can be synchronized with sources up to 600 kHz, a frequency range used by many DC-DC converters. A synchronized system prevents the generation of low frequency sub harmonics in the audio range. The synchronization input amplitude can range from 3 to 6 volts. Figure 16 on page 17 illustrates the relationship between operating frequency, temperature and input voltage.

The external synchronization timing cycle can be varied cycle to cycle for systems employing spread-spectrum clocking or for slave sharing clock interleaving. The dc level of the sync pin can be used to detect the state of the input voltage protection switch.

FIGURE 4: ENABLE AND SYNC EQUIVALENT CIRCUIT

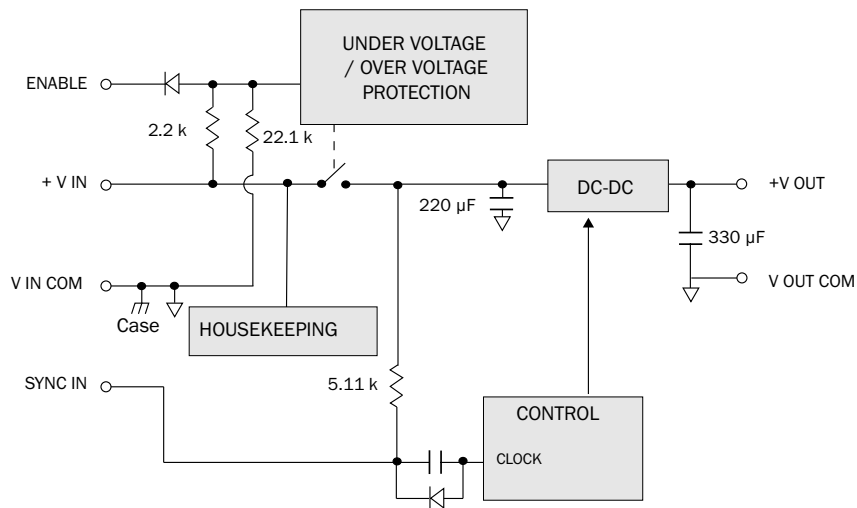


TABLE 6: ENABLE CAPACITANCE VALUES FOR START-UP DELAY

ENABLE CAPACITANCE: DELAY FROM ENABLE RELEASE TO START OF OUTPUT RISE (25 °C)												UNITS
CAPACITANCE (C _T)	0.22	0.33	0.47	0.68	1.0	1.5	2.2	3.3	4.7	6.8	10	μF
V _{IN} 3.3 V	0.8	1.1	1.6	2.2	3.1	4.6	6.7	10.0	14.1	20.2	29.7	ms
V _{IN} 5.0 V	0.4	0.5	0.7	0.9	1.3	1.8	2.6	3.9	5.5	7.8	11.4	ms

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$+V_{IN}$ AND V_{IN} COMMON

Input Voltage

The input voltage range for normal operating conditions is 3.0 to 6.0 volts (Figure 5). For input ripple current see Figure 10 and Figure 11 on page 16.

The V_{IN} Common pin is connected to V_{OUT} Common and case ground. The input and output should share the same ground plane in the power system design.

ADDITIONAL INPUT BLOCK FEATURES

Input Under and Over Voltage Protection

The MFP includes a solid state switch on the input section. This switch opens for fault conditions including input voltages below the minimum and transient voltages above the maximum. The safe operating range includes ground and extends to 7.0 volts indefinitely and up to 15 volts as a time limited transient. The switch will only close when certain internal conditions are met, including the proper operation of the internal housekeeping supply and a safe input voltage range.

No Single Point Failure

The solid state switch (SSS) can be used to provide one additional level of reliability: “no single point failure” will result in a connection from input voltages to output loads. The SSS can be opened by grounding of the ENABLE Pin. The status of the SSS can be determined by detecting the voltage on the SYNC pin. A logic low on this pin indicates that the SSS is open.

No External Bias Required

An internal housekeeping supply that is active at inputs as low as 2 volts provides a boosted and regulated voltage supply for internal use. This internal supply is one of the reasons that this product can provide full power at very high efficiency at input voltages as low as 3 volts. No external power source or external bias is required.

Input Reflected Noise and Inrush Current Limit

Substantial input capacitance is included and the input solid state switch previously described is designed to provide associated inrush current limiting. The substantial input capacitance and high SSS provide a “pi” filter configuration that results in very low reflected ripple current. The very low input noise and inrush limiter make the MFP unique among point of load converters.

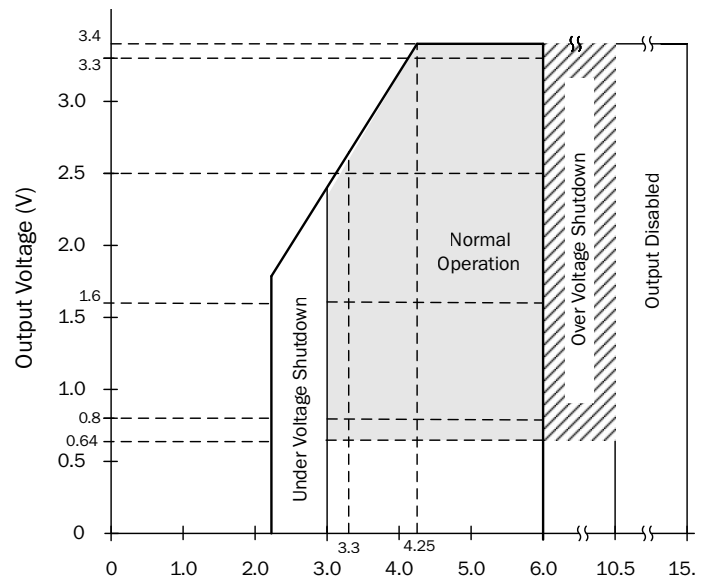


FIGURE 5: INPUT VOLTAGE VS MAXIMUM OUTPUT VOLTAGE

Maximum Flexible Power (MFP) Single Output Point of Load

MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

SENSE

The MFP includes a positive remote sense. The SENSE pin is intended to be used to maintain the desired preset voltage at the point-of-use by connecting the remote sense to the +Vout supply in close proximity to the load. Up to 0.27 volts of power line drop can be accommodated. If the SENSE pin lead is not connected to the output positive power pin, the output will rise a total of 0.27 volts.

The output voltage can be margined upward from the preset value as much as 0.2 volts by the addition of a resistor between the positive SENSE pin and the output power pin. The amount of increase in the output voltage by margining will reduce the available remote sense range by the same amount. The sum of margined voltage and voltage sense drop must be less than 0.2 volts.

Sense margining can be used to adjust V_{OUT} from 3.3 to 3.5. Connections must be made as close as possible to Common and to R_x . This method uses the SENSE pin's voltage compensation function to raise the output voltage. Therefore, there will not be an option to compensate for voltage drop at the load.

If connections have no voltage drop, the formula for the resistor is

$$R_x = \frac{1000 \text{ in ohms}}{\left[\frac{(0.2697)}{(V_{OUT} - 3.3)^{-1}} \right]}$$

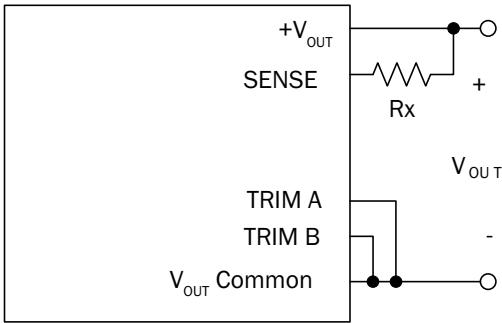


FIGURE 6: SENSE PIN VOLTAGE MARGINING
 V_{OUT} FROM 3.3 TO 3.5 ^{1, 2, 3}

- Figure 6 - Notes
- 1. For external connections see Figure 3 on page 10.
 - 2. See Table 7 on page 15 for output voltages from 0.64 to 3.3.
 - 3. Not tested.

Maximum Flexible Power (MFP) Single Output Point of Load

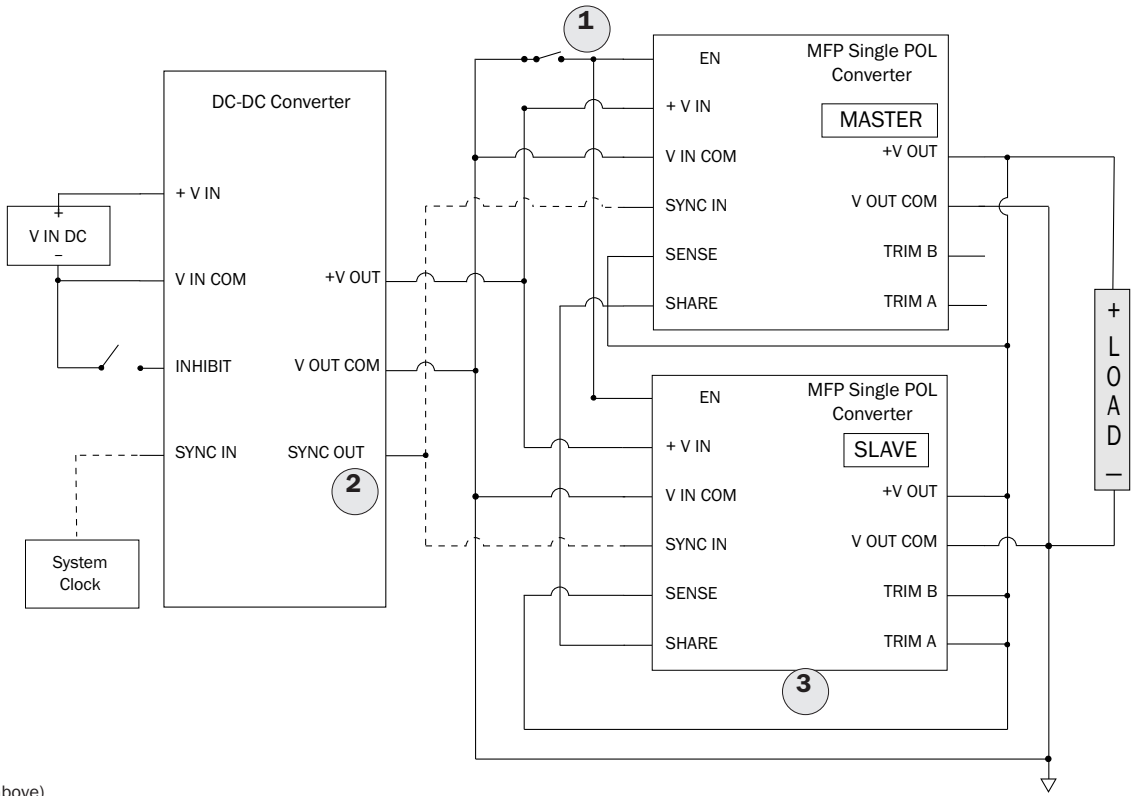
MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

SHARE

The MFP includes a current share feature that allows multiple units to operate as a single supply capable of providing a total current that is the sum of the maximum from each of the units that are operated in parallel. In connecting units in parallel, the SHARE pin is connected between units and all but one unit, the master, will have TRIM A and TRIM B pins tied to the positive SENSE pin. The master will have the TRIM pins configured for the desired output voltage while the other units in parallel will match the current and voltage of the master unit.

Two connections are critical to sharing between two units. The SHARE pins of the two (or more) units must be tied together and the TRIM A and TRIM B outputs must be tied together and shorted to +Vout and SENSE for each unit that is not the Master. The master unit will be the one with the highest pre-set output voltage. In the case of Figure 7, callout 3, the master is configured with both TRIM pins open for a 0.8 volt output.

The SHARE pin can be used as an output current monitor because the voltage on this pin is proportional to unit current. See Figure 3 on page 10, callout 5. Output currents corresponding to SHARE pin voltages are shown in Figure 16 on page 17. Connections for current monitoring are also shown in Figure 3 on page 10.



- For more information:
- 1 ENABLE section
 - 2 SYNC section
 - 3 SHARE section (above)

FIGURE 7: TYPICAL SHARE CONNECTION WITH OPTIONAL SYNC

Maximum Flexible Power (MFP) Single Output Point of Load

MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

TRIM A AND TRIM B

Output Voltage Set and Adjustment

The MFP0507S, single output model has the flexibility to be set for any voltage from 0.64 to 3.3 volts. The MFP includes five precision set-points that can be accomplished with pin connections alone and no trim resistor. An open circuit on both TRIM pins results in a 0.80 volts output, grounding one or the other or both pins results in precise output voltages of 1.6, 2.5 or 3.3 volts. One other preset voltage is possible using the SENSE pin. Connecting both trim pins to the positive SENSE pin results in 0.64 volts. Output values of 0.8 to 3.5 volts can be set with the use of external trim resistors in series with the trim pins to ground.

Any voltage intermediate to the pre-set voltages is available by adding a trim resistor between Common and both TRIM pins. Table 7 lists available pin-configurable and adjust/trim output voltages. See Figure 8 for output current under specific operating conditions.

OUTPUT VOLTAGE USING PIN CONFIGURATIONS OR TRIM RESISTORS			
Desired Voltage		Pin Configurable	TRIM Resistor (R_T) ¹ from ground to TRIM A and TRIM B
0.64 ²	Fixed SENSE pin V_s	Both TRIM A and TRIM B connected to SENSE	—
0.8	Fixed V_1	Both TRIM A and TRIM B open	—
0.9	Adjust	—	57.6 k
1.0	Adjust	—	27.4 k
1.2	Adjust	—	12.7 k
1.5	Adjust	—	6.19 k
1.6	Fixed V_2	TRIM A open TRIM B grounded	—
1.8	Adjust	—	3.57 k
2.0	Adjust	—	2.61 k
2.5	Fixed V_3	TRIM A grounded. TRIM B open	—
3.3	Fixed V_4	Both TRIM A and TRIM B grounded	—

TABLE 7: USER CONFIGURABLE OUTPUT VOLTAGES ^{1, 3}

Table 7 – Notes

1. Formula for R_T in Table 7 for V_{OUT} below 3.3 and above 0.8 V:

$$R_T = \frac{6.031}{V_{OUT} - 0.804} - 2.4 \text{ in kOhms}$$

- 2. The output voltage of 0.64 is available for use but performance is not electrically specified.
- 3. See page Figure 6 on page 13.

+ V_{OUT} AND V_{OUT} COMMON

Due to the Buck topology, the required output voltage of the MFP must always be at least 0.8 volt lower than the input. Precise values of achievable output voltages and currents as a function of V_{IN} are shown in Figure 7 on page 14.

See page Figure 10 and Figure 11 on page 16 for typical output ripple plots.

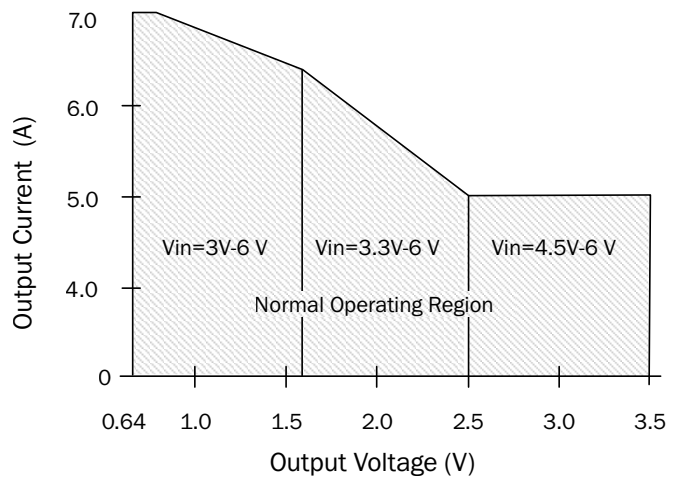


FIGURE 8: MAXIMUM RATED OUTPUT CURRENT

Maximum Flexible Power (MFP) Single Output Point of Load

MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

TYPICAL PERFORMANCE PLOTS: 25 °C CASE, 5 VIN, 100% LOAD, FREE RUN, UNLESS OTHERWISE SPECIFIED.
These are examples for reference only and are not guaranteed specifications.

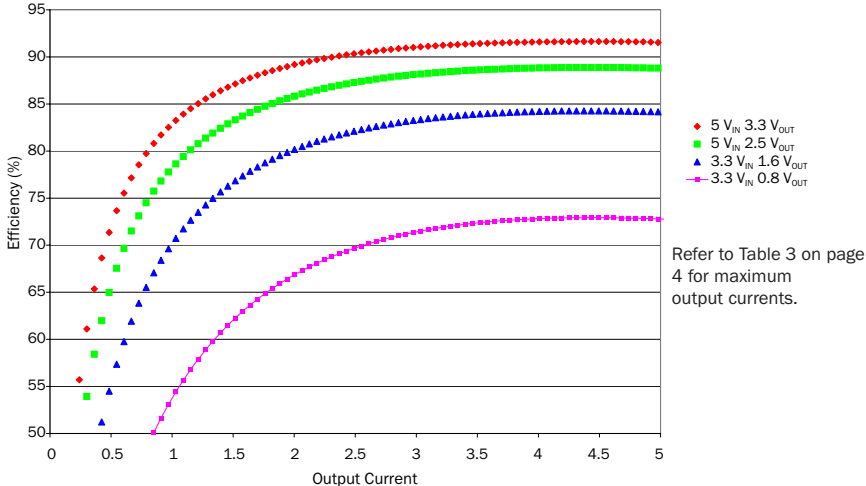
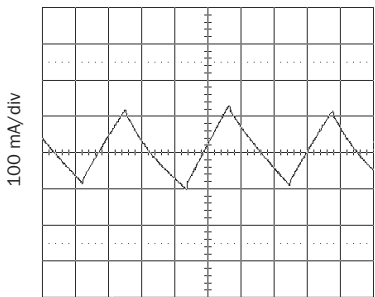
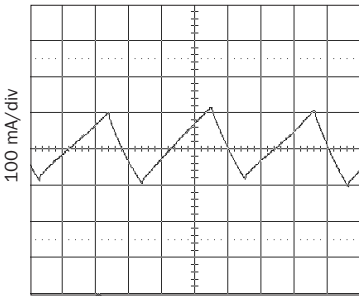


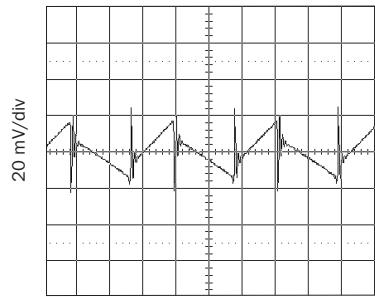
FIGURE 9: TYPICAL EFFICIENCIES



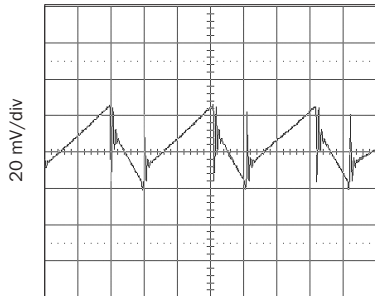
100 mA/div
1 μs/div
3.3 VIN, 1.2 VOUT, 5 A load
MFP0507S Input Ripple (I_{IN})
FIGURE 10



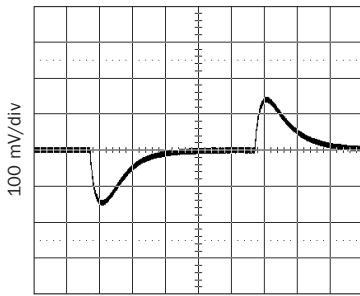
100 mA/div
1 μs/div
5 VIN, 3.3 VOUT, 5 A load
MFP0507S Input Ripple (I_{IN})
FIGURE 11



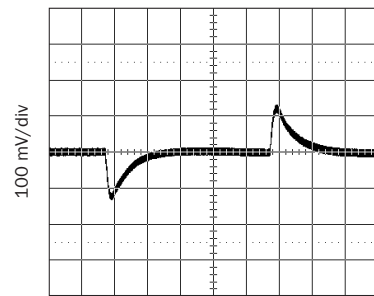
20 mV/div
1 μs/div
3.3 VIN, 1.2 VOUT, 5 A load
MFP0507S Output Ripple
FIGURE 12



20 mV/div
1 μs/div
5 VIN, 3.3 VOUT, 5 A load
MFP0507S Output Ripple
FIGURE 13



100 mV/div
200 μs/div
3.3 VIN, 1.2 VOUT, 5 A load
MFP0507S Load Transient
FIGURE 14



100 mV/div
200 μs/div
5 VIN, 3.3 VOUT, 5 A load
MFP0507S Load Transient
FIGURE 15

Maximum Flexible Power (MFP) Single Output Point of Load

MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

TYPICAL PERFORMANCE PLOTS: 25 °C CASE, 5 VIN, 100% LOAD, FREE RUN, UNLESS OTHERWISE SPECIFIED.
These are examples for reference only and are not guaranteed specifications.

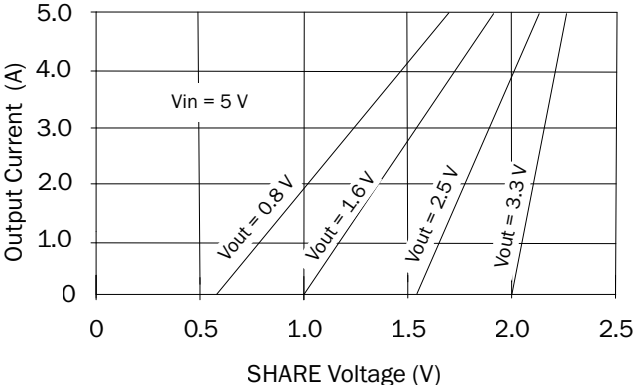


FIGURE 16: SHARE AS MONITOR FOR OUTPUT CURRENT

Note on SHARE: Because there is a predictable relationship between SHARE pin voltage and load current, the SHARE pin can be monitored to indicate load current when the share function is not being used. Due to initial tolerance related variations in the exact SHARE voltage vs. load current relationship, it is recommended that the user perform a calibration by measuring the SHARE pin voltage at two or more load currents and calculating the V-SHARE vs. load slope and intercept. The graph in Figure 16 is indicative of what should be expected when generating such a relationship.

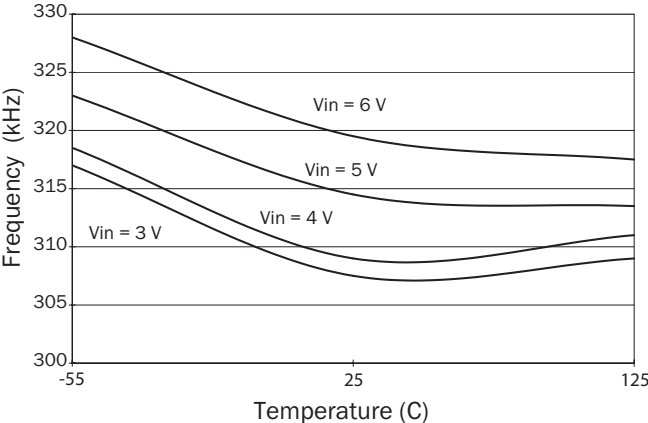


FIGURE 17: TYPICAL OPERATING FREQUENCY VS TEMPERATURE AND V_{IN}

This graph illustrates the performance of proprietary technology

Maximum Flexible Power (MFP) Single Output Point of Load

MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

ELEMENT EVALUATION TABLES FOR QML PRODUCTS ARE IN "APP-009 QUALITY AND CERTIFICATION", APPENDIX A, IN COMPLIANCE WITH MIL-PRF-38534 REVISION L.
(LINK [HTTPS://WWW.CRANEEAE.COM/QUALITY-ASSURANCE-MODULAR-POWER](https://www.craneeae.com/quality-assurance-modular-power))

ENVIRONMENTAL SCREENING SPACE DC-DC CONVERTERS

TEST PERFORMED	CLASS BP ¹
	/K
Non-destruct wire bond pull, Method 2023	■
Pre-cap Inspection, Method 2017, 2032	■
Temperature Cycle (10 times)	
Method 1010, Cond. C, -65 °C to +150 °C, ambient	■
Constant Acceleration	
Method 2001, 3000 g	■
PIND, Test Method 2020, Cond. A	■
Pre burn-in test, Group A, Subgroups 1 and 4	■
Burn-in Method 1015, +125 °C case, typical ³	
96 hours	
160 hours	
2 x 160 hours (includes mid-BI test)	■
Final Electrical Test, MIL-PRF-38534, Group A,	
Subgroups 1 and 4: +25 °C case	
Subgroups 1 through 6, -55 °C, +25 °C, +125 °C case	■
Hermeticity Test, Method 1014	
Gross Leak, Cond. B ₂ , Kr85	■
Gross Leak, Cond. C ₁ , fluorocarbon	
Gross Leak, Dip	
Fine Leak, Cond. B ₁ , Kr85	■
Fine Leak, Cond. A ₂ , helium	
Radiography, Method 2012	■
Post Radiography Electrical Test, +25 °C case	■ ²
Final visual inspection, Method 2009	
Method 2009 of MIL-STD-883	■
Magnification 1X	

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

Notes

- All processes are QML qualified and performed by certified operators.
- Not required by DLA but performed to assure product quality.
- Burn-in temperature designed to bring the case temperature to +125 °C minimum.
Burn-in is a powered test.

TABLE 8: ENVIRONMENTAL SCREENING

Maximum Flexible Power (MFP) Single Output Point of Load

MFP0507S, 3 TO 6 VOLT INPUT, 7 AMP, DC-DC CONVERTER FOR BP-MOD ONLY

SPACE RADIATION HARDNESS ASSURANCE DC-DC CONVERTERS RHA ¹ P

	CLASS K
QUALIFICATION PER MIL-STD	/BP
RHA P: 30 krad(Si) total dose ¹	■
SEE, LET 86 MeV cm²/mg ²	■

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

Notes

1. Radiation sensitive components internal to the devices are procured with radiation guarantees or undergo radiation lot acceptance testing (RLAT) performed per condition A, method 1019 of MIL-STD-883.

2. Single event testing was performed on a converter to 86 MeV-cm²/mg using 15 MeV/ nucleon gold ions with no latch-up, burn-out, functional interrupts, or gate ruptures exhibited. Single event upsets (output voltage transients) may be present up to 86 MeV-cm²/mg.

TABLE 9: SPACE RADIATION HARDNESS ASSURANCE DC-DC CONVERTERS RHA P

