### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

#### **FEATURES**

- · No cross-regulation error in triple output models
- Operating temperature -55° to +125°C
- Input voltage range 16 to 40 volts
- · Transient protection 50 Vin for 50 ms
- · Fully isolated, magnetic feedback
- · Fixed high frequency switching
- · Inhibit and synchronization function
- · Indefinite short circuit and overload protection
- Soft-start function limits inrush current during start-up

ALSO SEE OUR IMPROVED MTR (50) 16 - 50 Vin, 80 V transient per MIL-STD-704A datasheet



MODELS									
OUTPUT VOLTAGE (V)									
SINGLE	DUAL	TRIPLE							
3.3	±5	+5 & ±12							
5	±12	+5 & ±15							
8.5	±15								
12									
15									
18									

### **DESCRIPTION**

The Interpoint® MTR (40) Series™ of DC-DC converters offers up to 30 watts of power in single, dual, or triple output configurations packaged in a low profile case. The MTR (40)converters are manufactured in our fully certified and qualified MIL-PRF-38534 Class H production facility and packaged in hermetically sealed steel cases. They are ideal for use in programs requiring high reliability, small size, and high efficiency.

MTR (40) models have an input voltage range of 16 to 40 and transient protection up to 50 volts dc in for up to 50 milliseconds. They operate over the full military temperature range with up to 84% efficiency. MTR (40) converters are packaged in hermetically sealed metal cases, making them ideal for use in military, aerospace and other high reliability applications. The converters are offered with standard screening, "ES" screening, or fully compliant to "883" MIL-PRF-38534 Class H screening. See Table 13 on page 27 for more information. Standard microcircuit drawings (SMD) are available. See Table 3 on page 8.

### CONVERTER DESIGN

The MTR converters are constant frequency, pulse-width modulated switching regulators which use a quasi-square wave, single ended, forward converter design. Tight load regulation is maintained via wide bandwidth magnetic feedback and, on single output models, through use of remote sense. On dual output models, the positive output is independently regulated and the negative output is cross regulated through the use of tightly coupled magnetics. The MTR Series triple output DC-DC converter's design includes individual regulators on the auxiliary outputs which provide for no cross regulation error when a minimum 300 mA load is maintained on the main (+5) output.

All models include a soft-start function to prevent large current draw and minimize overshoot.

Indefinite short circuit protection and overload protection are provided by a constant current-limit feature. This protective system senses current in the converter's secondary stage and limits it to approximately 125% of the maximum rated output current.

MTR converters are provided with internal filtering capacitors that help reduce the need for external components in normal operation. Use our FMCE-0328 $^{\text{TM}}$ , FMCE-0528 $^{\text{TM}}$  or FMCE-0828 $^{\text{TM}}$  EMI filter to meet the requirements of MIL-STD-461C CE03 and CS01 and/or MIL-STD-461D, E and F CE102 and CS101 levels of conducted emissions. Or use the FM-704A for transient suppression and to meet MIL-STD-461C CE03.

### COVER MARKING

The cover marking for the MTR 40 is "MTR DC-DC CONVERTER" under the model number. See Figure 7, Figure 8 and Figure 9 on page 7.

### SYNCHRONIZATION

Synchronizing the converter with the system clock allows the designer to confine switching noise to clock transitions, minimizing interference and reducing the need for filtering. In sync mode, the converter will run at any frequency between 500 kHz and 675 kHz for singles and duals and between 500 and 700 for triples. The sync control operates with a duty cycle between 40% and 60%. The sync pin must be connected to input common pin when not in use.



### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

### WIDE INPUT VOLTAGE RANGE

MTR converters are designed to provide full power over a full 16 to 40 volts input voltage range. Operation below 16 volts, including MIL-STD-704A emergency power conditions is possible with derated power.

#### DYNAMIC RESPONSE

The MTR Series feed-forward compensation system provides excellent dynamic response and audio rejection. Audio rejection is typically 40 dB for singles and duals and 50 dB for triples.

### INHIBIT FUNCTION

MTR converters provide an inhibit terminal that can be used to disable internal switching, resulting in no output voltage and very low quiescent input current. The converter is inhibited when the inhibit pin is pulled below 0.8 V and enabled when its inhibit pin is left floating. An external inhibit interface should be capable of pulling the converter's inhibit pin below 0.8 V while sinking the maximum inhibit current and also allowing the inhibit pin to float high to enable the converter. A voltage should not be applied to the inhibit pin. The open circuit voltage present on the inhibit pin is 9 to 11 V.

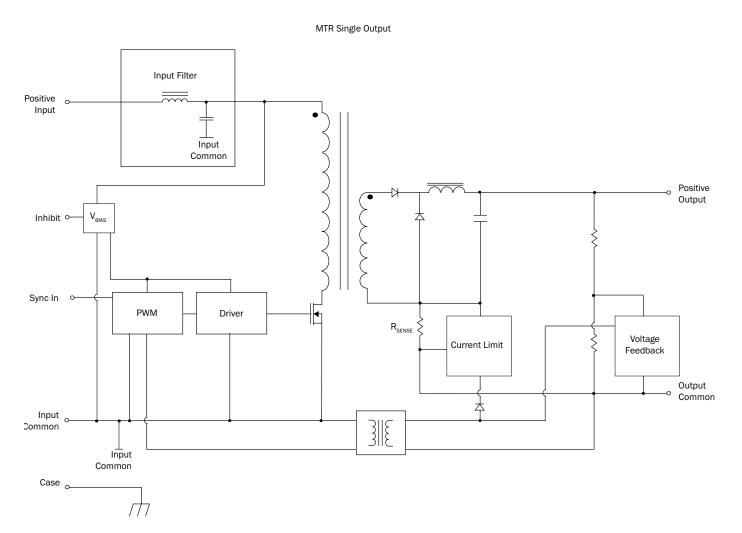


FIGURE 1: MTR SINGLE BLOCK DIAGRAM

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

MTR Series Dual Output /883

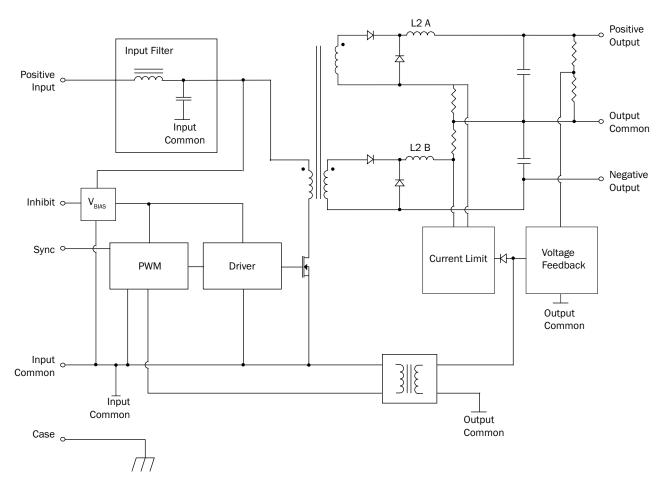


FIGURE 2: MTR DUAL /883 BLOCK DIAGRAM

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

MTR Series Dual Output non-883

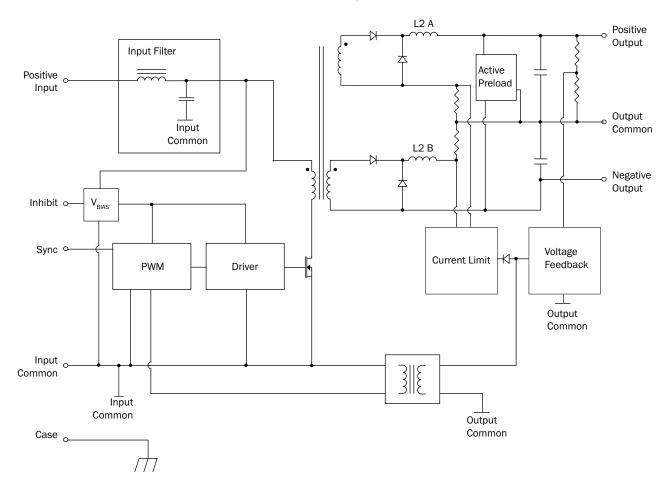


FIGURE 3: MTR DUAL NON-883 BLOCK DIAGRAM

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

MTR Series Triple Output

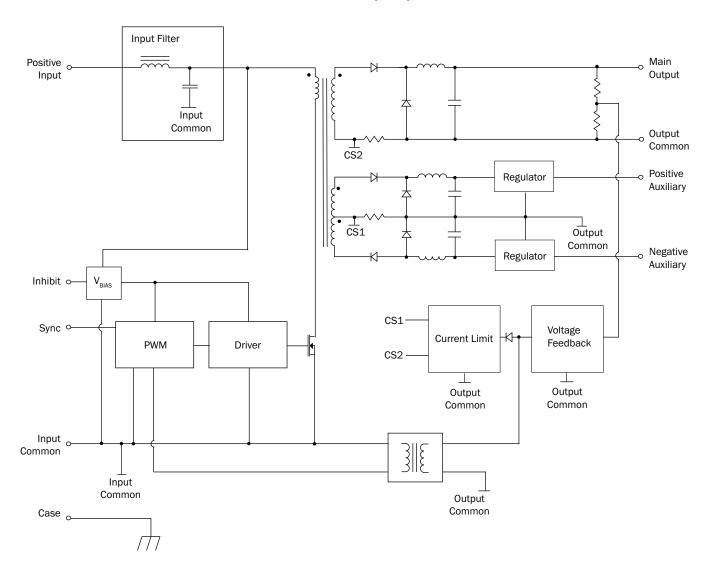


FIGURE 4: MTR TRIPLE BLOCK DIAGRAM

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

TRIM AND REMOTE SENSE (AVAILABLE ON SINGLE 3.3, 5, 8.5, 12 AND 15 OUTPUT MODELS ONLY)

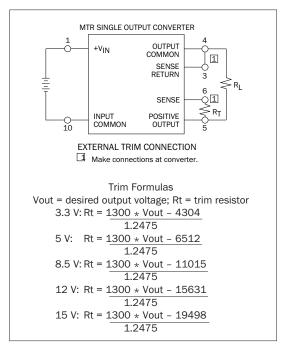


FIGURE 5: TRIM CONNECTION 1, 2, 3

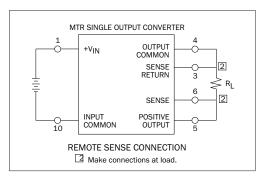
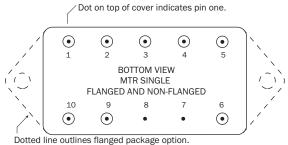


FIGURE 6: REMOTE SENSE CONNECTION <sup>4</sup>

Notes for Remote Sense and Trim

- 1. When trimming output voltage and/or using remote sense, the total output voltage increase must be less than 0.6 volts at the converters pins. Do not exceed the maximum power.
- 2. If neither voltage trim nor remote sense will be used, connect pin 3 to pin 4 and pin 5 to pin 6.
- 3. CAUTION: The converter will be permanently damaged if the remote sense (pin 6) is shorted to ground. Damage may also result if the output common or positive output is disconnected from the load with the remote sense leads connected to the load.
- 4. When using remote sense for voltage compensation or when using remote sense for trim, the output will drift over temperature. Contact Applications Engineering for more information at powerapps@craneae.com

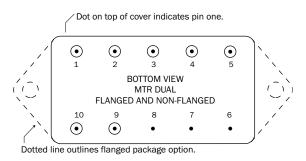
### 16 TO 40 VOLT INPUT - 20 TO 30 WATT



See Figure 34 on page 22 and Figure 37 on page 25 for dimensions.

MTR	MTR280 DC-DC CC	NVERTER	Δ
SN		IDC	
		sc	
•	TWN	CAGE 50	821

FIGURE 7: PIN OUT AND MARKING SINGLE OUTPUT MODELS



See Figure 34 on page 22, Figure 35 on page 23, Figure 37 on page 25 and Figure 38 on page 26 for dimensions.



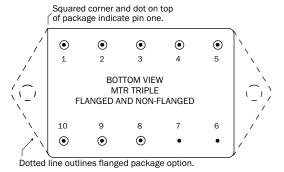
FIGURE 8: PIN OUT AND MARKING FOR DUAL OUTPUT MODELS

	PIN OUT									
Pin	Single Output	Dual Output	Triple Output							
1	Positive Input	Positive Input	Positive Input							
2	Inhibit	Inhibit	Main (+5) Output							
3	Sense Return	Positive Output	Output Common							
4	Output Common	Output Common	Neg. Aux. Output							
5	Positive Output	Negative Output	Pos. Aux. Output							
6	Positive Sense	Case Ground	Case Ground							
7	Case Ground	Case Ground	Case Ground							
8	Case Ground	Case Ground	Inhibit							
9	Sync	Sync	Sync							
10	Input Common	Input Common	Input Common							

TABLE 1: PIN OUT

PII	NS NOT IN USE
Inhibit	Leave unconnected
Sync In	Connect to input common
Sense Lines	Must be connected to appropriate outputs

TABLE 2: PINS NOT IN USE



See Figure 33 on page 21 and Figure 36 on page 24 for dimensions.

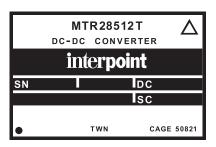


FIGURE 9: PIN OUT AND MARKING TRIPLE OUTPUT MODELS

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

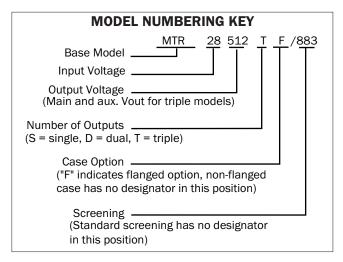


FIGURE 10: MODEL NUMBERING KEY

SMD NUMBERS								
STANDARD MICROCIRCUIT DRAWING (SMD)	MTR SIMILAR PART							
5962-0150101HXC	MTR283R3S/883							
5962-9306801HXC	MTR2805S/883							
5962-9306901HXC	MTR2812S/883							
5962-9307001HXC	MTR2815S/883							
5962-9320201HXC	MTR2818S/883							
5962-9320501HXC	MTR2805D/883							
5962-9307101HXC	MTR2812D/883							
5962-9307201HXC	MTR2815D/883							
5962-9307301HXC	MTR28512T/883							
5962-9307401HXC	MTR28515T/883							

SMD numbers shown are for screening level Class H, standard case (X), standard pin seal and non-solder dipped pins (C). For other options please refer to the SMD for the SMD number and the vendor similar number. All SMD numbers are listed on the SMD in the "Bulletin" which is the last page of the SMD. For exact specifications for an SMD product, refer to the SMD. SMDs can be downloaded from https://landandmaritimeapps. dla.mil/programs/smcr

TABLE 3: SMD NUMBER CROSS REFERENCE

#### **MODEL NUMBER OPTIONS** TO DETERMINE THE MODEL NUMBER ENTER ONE OPTION FROM EACH CATEGORY IN THE FORM BELOW. **Base Model and** Output Voltage 1 Case Options <sup>3</sup> Screening <sup>4</sup> **Number of CATEGORY Input Voltage** Outputs 2 3R3, 05, 8R5, 12, 15, 18 (non-flanged, leave blank) (standard, leave blank) **OPTIONS** MTR28 05, 12, 15 D F (flanged) ES 512, 515 Τ 883 **FILL IN FOR** MTR28 MODEL # 5

- 1. Output Voltage: An R indicates a decimal point. 3R3 is 3.3 volts out. The value of 3.3, 8.5 and 18 are only available in single output models. The 512 and 515 triple output converters are +5 volt main and ±12 or ±15 volt auxiliaries.
- 2. Number of Outputs: S is a single output, D is a dual output, and T is a triple output
- 3. Case Options: For the standard case (Figure 34 on page 22, Figure 35 on page 23 and Figure 33 on page 21) leave the case option blank. For the flanged case option (Figure 37 on page 25, Figure 38 on page 26 and Figure 36 on page 24), insert the letter F in the Case Option position.
- 4. Screening: For standard screening leave the screening option blank. For other screening options, insert the desired screening level. For more information see Table 13 on page 27.
- 5. If ordering by model number add suffix "-Q" to request solder dipped leads (MTR2805S/883-Q).

TABLE 4: MODEL NUMBER OPTIONS

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

Table 5: Operating Conditions, All Models: 25 °C case, 28 Vin, 100% load, unless otherwise specified.

		Al				
PARAMETER (Operating Conditions Table continued on next page)	CONDITIONS	MIN	TYP	MAX	UNITS	
LEAD SOLDERING TEMPERATURE <sup>1</sup>	10 seconds max.	_	_	300	°C	
STORAGE TEMPERATURE <sup>1</sup>		-65	_	+150	°C	
CASE OPERATING	FULL POWER	-55	_	+125	°C	
TEMPERATURE	ABSOLUTE <sup>1</sup>	-55	_	+135		
DERATING OUTPUT POWER/CURRENT <sup>1</sup>	LINEARLY	at 135°C				
ESD RATING <sup>1</sup>	MIL-STD-883, METHOD 3015					
MIL-PRF-38534, 3.9.5.8.2	CLASS 1 SINGLES AND TRIPLES	0 - 1999			V	
	CLASS 2 DUALS	2				
ISOLATION: INPUT TO OUTPUT, INPUT TO CASE, OUTPUT TO CASE $^2$	@ 500 VDC AT 25°C	100	_	_	Megohms	
INPUT TO OUTPUT CAPACITANCE <sup>1</sup>	SINGLES AND DUALS	_	50	_	_	
	TRIPLES	_	100	_	pF	
CURRENT LIMIT <sup>3</sup>	% OF FULL LOAD	_	125	_	%	
AUDIO REJECTION <sup>1</sup>	SINGLES AND DUALS	_	40	_	40	
	TRIPLES	_	50	_	dB	

- 1. Guaranteed by characterization test and/or analysis. Not a production test.
- 2. When testing isolation, input pins are tied together and output pins are tied together. They are tested against each other and against case. Discharge the pins before and after testing.
- 3. Dual and triple outputs: The over-current limit will trigger when the sum of the currents from both dual outputs or both auxiliary outputs (triple)reaches 125% (typical value) of the maximum rated "total" current of both outputs.

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

Table 6: Operating Conditions, All Models: 25 °C case, 28 Vin, 100% load, unless otherwise specified.

		A	ALL MODELS					
PARAMETER (Operating Conditions Table continued)	CONDITIONS	MIN	TYP	MAX	UNITS			
SWITCHING FREQUENCY	SINGLES AND DUALS	550	_	650	kHz			
-55° TO +125°C	TRIPLES	525	_	650	КПZ			
SYNCHRONIZATION	INPUT FREQUENCY							
	SINGLES AND DUALS	500	_	675	kHz			
	TRIPLES	500	_	700	КПZ			
	DUTY CYCLE <sup>1</sup>	40	_	60	%			
	ACTIVE LOW	_	_	0.8	V			
	ACTIVE HIGH <sup>1</sup>	4.5	_	5.0				
	REFERENCED TO		INPUT	COMMON	N			
	IF NOT USED	СО	CONNECT TO INPUT COMMON					
INHIBIT ACTIVE LOW (OUTPUT DISABLED)	INHIBIT PIN PULLED LOW	_	_	0.8	V			
Do not apply a voltage to the inhibit pin. $^{\mathrm{2}}$	INHIBIT PIN SOURCE CURRENT <sup>1</sup>							
	SINGLES AND DUALS	_	_	6	mA			
	TRIPLES	_	_	8	IIIA			
	REFERENCED TO		INPUT	COMMON	N			
INHIBIT ACTIVE HIGH (OUTPUT ENABLED)	INHIBIT PIN CONDITION		OPEN CO	LLECTOR	OR			
Do not apply a voltage to the inhibit pin. $^{\mathrm{2}}$			UNCC	NNECTED	)			
	OPEN INHIBIT PIN VOLTAGE <sup>1</sup>	9	_	11	V			

For mean time between failures (MTBF) contact Applications Engineering at powerapps@craneae.com

- 1. Guaranteed by characterization test and/or analysis. Not a production test.
- 2. An external inhibit interface should be used to pull the inhibit low or leave it floating. The inhibit pin can be left unconnected if not used.

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

Table 7: Electrical Characteristics -55  $^{\circ}$ C to +125  $^{\circ}$ C case, 28 Vin, 100% load, unless otherwise specified.

SINGLE OUTPUT MODELS		М	MTR283R3S			MTR2805S			MTR288R5S		
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		3.201	3.30	3.399	4.85	5.00	5.15	8.23	8.5	8.77	V
OUTPUT CURRENT		0	_	6.06	0	_	5.0	0	_	2.95	А
OUTPUT POWER	V <sub>IN</sub> = 16 TO 40	0	_	20	0	_	25	0	_	25	W
OUTPUT RIPPLE	T <sub>C</sub> = 25 °C	_	15	40	_	35	50	_	35	60	
10 kHz - 2 MHz	T <sub>C</sub> = -55°C TO +125°C	_	_	50	_	50	90	_	50	90	mV p-p
LINE REGULATION <sup>2</sup>	V <sub>IN</sub> = 16 TO 40	_	_	10	_	15	50	_	15	50	mV
LOAD REGULATION	NO LOAD TO FULL	_	_	10	_	15	50	_	15	50	mV
INPUT VOLTAGE	CONTINUOUS	16	28	40	16	28	40	16	28	40	V
NO LOAD TO FULL	TRANSIENT 50 ms <sup>1</sup>	_	_	50	_	_	50	_	_	50	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
INPUT CURRENT	NO LOAD	_	30	75	_	35	75	_	35	75	
	INHIBITED	_	7	8	_	3	8	_	3	8	mA
INPUT RIPPLE CURRENT 3	10 kHz - 10 MHz	_	25	50	_	20	50	_	20	65	mA p-p
EFFICIENCY	T <sub>C</sub> = 25 °C	74	76	_	76	78	_	78	80	_	%
	$T_C = -55^{\circ}C TO + 125^{\circ}C$	71	_	_	73	_	_	78	_	_	%
LOAD FAULT <sup>4, 5</sup>	POWER DISSIPATION	_	_	12	_	_	12	_	_	12	W
SHORT CIRCUIT	RECOVERY <sup>1</sup>	_	1.4	6	_	1.4	5	_	1.4	5	ms
STEP LOAD RESPONSE 5, 6	TRANSIENT	_	±125	±250	_	±200	±300	_	±300	±400	mV pk
50% - 100% - 50%	RECOVERY <sup>1</sup>	_	_	200	_	60	200	_	60	200	μs
STEP LINE RESPONSE 1, 5, 7	TRANSIENT	_	_	±300	_	±200	±300	_	±300	±400	mV pk
V <sub>IN</sub> = 16 - 40 -16	RECOVERY	_	_	300	_	_	300	_	_	300	μs
START-UP <sup>5, 8</sup>	DELAY	-	1.4	5	_	1.4	5	_	1.4	5	m sec
FULL LOAD	OVERSHOOT <sup>1</sup>	_	0	50	_	0	50	_	0	50	mV pk
CAPACITIVE LOAD <sup>1</sup>	NO EFFECT ON DC			300			300			300	μF
$T_C = 25 \degree C$	PERFORMANCE		_	300	_	_	300		_	300	μr 

- 1. Guaranteed by characterization test and/or analysis. Not a production test.
- 2. Operation is limited below 16 volts.
- 3. Tested with 6800 pF ceramic bypass capacitor connected externally from input common to case.
- 4. Indefinite short circuit protection not guaranteed above 125  $^{\circ}\text{C}$  case.
- 5. Recovery time is measured from application of the transient to point at which  $V_{OUT}$  is within 1% of final value.
- 6. Step load transition test is performed at 10 microseconds typical.
- 7. Step line characterization test is performed at 100 microseconds  $\pm$  20 microseconds
- 8. Tested on release from inhibit.

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

Table 8: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, unless otherwise specified.

SINGLE OUTPUT MODELS	SINGLE OUTPUT MODELS		/TR2812	?S	N	/TR2815	S	N	1TR2818	S	LINITS
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		11.64	12.00	12.36	14.70	15.00	15.30	17.46	18.00	18.54	V
OUTPUT CURRENT		0	_	2.5	0	_	2.0	0	_	1.67	А
OUTPUT POWER	V <sub>IN</sub> = 16 TO 40	0	_	30	0	_	30	0	_	30	W
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	_	25	40	_	25	40	_	_	40	
10 kHz - 2 MHz	T <sub>C</sub> = -55°C TO +125°C	_	40	90	_	40	90	_	_	90	mV p-p
LINE REGULATION <sup>2</sup>	V <sub>IN</sub> = 16 TO 40	_	15	50	_	15	50	_	_	50	mV
LOAD REGULATION	NO LOAD TO FULL	_	15	50	_	15	50	_	_	50	mV
INPUT VOLTAGE	CONTINUOUS	16	28	40	16	28	40	16	28	40	.,
NO LOAD TO FULL	TRANSIENT 50 ms <sup>1</sup>	_	_	50	_	_	50	_	_	50	V
INPUT CURRENT	NO LOAD	_	35	75	_	35	75	_	_	75	
	INHIBITED	_	3	8	_	3	8	_	_	8	mA
INPUT RIPPLE CURRENT 3	10 kHz - 10 MHz	<u> </u>	20	50	_	20	50	_	_	50	mA p-p
EFFICIENCY	T <sub>C</sub> = 25°C	80	83	_	81	84	_	81	84	_	%
	T <sub>C</sub> = -55°C TO +125°C	77	_	_	78	_	_	78	_	_	%
LOAD FAULT <sup>4, 5</sup>	POWER DISSIPATION	_	_	12	_	_	12	_	_	12	W
SHORT CIRCUIT	RECOVERY <sup>1</sup>	_	1.4	5	_	1.4	5	_	1.4	5	ms
STEP LOAD RESPONSE 5, 6	TRANSIENT	_	±250	±400	_	±350	±500	_	_	±600	mV pk
50% - 100% - 50%	RECOVERY <sup>1</sup>	_	60	200	_	60	200	_	60	200	μs
STEP LINE RESPONSE 1, 5, 7	TRANSIENT	_	±400	±625	_	±500	±750	_	±500	±800	mV pk
V <sub>IN</sub> = 16 - 40 -16	RECOVERY	_	_	350	_	_	350	_	_	350	μs
START-UP <sup>5, 8</sup>	DELAY	_	1.4	5	_	1.4	5	_	_	5	m sec
FULL LOAD	OVERSHOOT <sup>1</sup>	_	0	120	_	0	150	_	0	180	mV pk
CAPACITIVE LOAD <sup>1</sup>	NO EFFECT ON DC			000			000			000	_
$T_C = 25 ^{\circ}C$	PERFORMANCE	_	_	300	_	_	300	_	_	300	μF

- ${\bf 1.} \ {\bf Guaranteed} \ {\bf by} \ {\bf characterization} \ {\bf test} \ {\bf and/or} \ {\bf analysis}. \ {\bf Not} \ {\bf a} \ {\bf production} \ {\bf test}. \ .$
- 2. Operation is limited below 16 volts.
- Tested with 6800 pF ceramic bypass capacitor connected externally from input common to case.
- 4. Indefinite short circuit protection not guaranteed above 125 °C case.
- 5. Recovery time is measured from application of the transient to point at which  $V_{OUT}$  is within 1% of final value.
- 6. Step load transition test is performed at 10 microseconds typical.
- 7. Step line characterization test is performed at 100 microseconds  $\pm$  20 microseconds
- 8. Tested on release from inhibit.

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

Table 9: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, unless otherwise specified.

DUAL OUTPUT MODELS - /883 ONLY		N	MTR2805D		MTR2812D			MTR2815D			LINITO
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	+ V <sub>OUT</sub>	4.850	5.00	5.150	11.64	12.00	12.36	14.55	15.00	15.45	V
	- V <sub>OUT</sub>	4.825	5.00	5.172	11.58	12.00	12.42	14.47	15.00	15.53	<b>"</b>
OUTPUT CURRENT <sup>2</sup>	EITHER OUTPUT	0	2.5	4.5 <sup>1</sup>	0	1.25	2.25 <sup>1</sup>	0	1.00	1.80 <sup>1</sup>	A
V <sub>IN</sub> = 16 TO 40	TOTAL OUTPUT	l –	_	5	_	_	2.5	_	_	2.00	A
OUTPUT POWER <sup>2</sup>	EITHER OUTPUT	0	12.5	22.5 <sup>1</sup>	0	15	27 <sup>1</sup>	0	15	27 <sup>1</sup>	14/
V <sub>IN</sub> = 16 TO 40	TOTAL OUTPUT	-	_	25	-	_	30	_	_	30	W
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	-	20	40	_	30	80	_	25	80	
10 kHz - 2 MHz, ± V <sub>OUT</sub>	T <sub>C</sub> = -55°C TO +125°C	-	40	90	-	40	120	_	40	120	mV p-p
LINE REGULATION <sup>3</sup>	+ V <sub>OUT</sub>	-	10	50	_	10	50	_	10	50	>/
V <sub>IN</sub> = 16 TO 40	- V <sub>OUT</sub>	-	50	100	-	50	150	_	50	180	mV
LOAD REGULATION	+ V <sub>OUT</sub>	_	5	50	_	15	50	_	15	50	>/
NO LOAD TO FULL	- V <sub>OUT</sub>	_	25	100	_	30	150	_	30	180	mV
CROSS REGULATION <sup>1</sup>	SEE NOTE 4	_	4	6	_	4	6	_	4	6	0,4
EFFECT ON -V <sub>OUT</sub> , 25°C	SEE NOTE 5	_	7	12	_	4	8.3	_	3	8	%
INPUT VOLTAGE	CONTINUOUS	16	28	40	16	28	40	16	28	40	V
NO LOAD TO FULL	TRANSIENT 50 ms <sup>1</sup>	0	_	50	0	_	50	0	_	50	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
INPUT CURRENT	NO LOAD	-	35	75	-	50	75	_	50	75	A
	INHIBITED	-	3	8	-	3	8	_	3	8	mA
INPUT RIPPLE CURRENT 6	10 kHz - 10 MHz	l –	15	50	_	20	50	_	20	50	mA p-p
EFFICIENCY	T <sub>C</sub> = 25°C	76	78	_	79	81	_	80	83	_	%
BALANCED LOAD	T <sub>C</sub> = -55°C TO +125°C	73	_	_	76	_	_	77	_	_	/0
LOAD FAULT 7, 8	POWER DISSIPATION	_	10	12	_	10	12	_	10	12	W
SHORT CIRCUIT	RECOVERY <sup>1</sup>	_	1.4	5.0	_	1.4	5.0	_	1.4	5.0	ms
STEP LOAD RESPONSE 8, 9	TRANSIENT	_	±200	±300	_	±150	±300	_	±200	±400	mV pk
50% - 100% - 50%, ± V <sub>OUT</sub>	RECOVERY <sup>1</sup>	_	100	200	_	100	200	_	100	200	μs
STEP LINE RESPONSE <sup>1, 8, 10</sup>	TRANSIENT	_	±200	±400	_	±200	±400	_	±400	±500	mV pk
$V_{IN}$ = 16 TO 40, $\pm V_{OUT}$	RECOVERY	l –	_	300	_	_	300	_	_	300	μs
START-UP	DELAY	_	1.4	5	_	1.4	5	_	1.4	5	ms
FULL LOAD	OVERSHOOT <sup>1</sup>	-	0	180	_	0	120	_	0	150	mV pk
CAPACITIVE LOAD <sup>1, 12</sup>	NO EFFECT ON DC			500			500			500	
$T_C = 25 \degree C$	PERFORMANCE	_	_	500	_	_	500	_	_	500	μF

- 1. Guaranteed by characterization test and/or analysis. Not a production test.
- 2. Up to 90% of the total output current/power is available from either output providing the opposite output is carrying at least 10% of the total output power.
- 3. Operation is limited below 16 volts.
- 4. Effect on negative V<sub>OUT</sub> from 50%/50% loads to 80%/20% or 20%/80% loads.
- 5. Effect on negative  $V_{\rm OUT}$  from 50%/50% loads to 90%/10% or 10%/90% loads. Figure 26 on page 18.
- Tested with 6800 pF ceramic bypass capacitor connected externally from input common to case.
- 7. Indefinite short circuit protection not guaranteed above 125  $^{\circ}\text{C}$  case.
- 8. Recovery time is measured from application of the transient to point at which  $V_{OUT}$  is within 1% of final value.
- 9. Step load transition test is performed at 10 microseconds typical.
- 10. Step line characterization test is performed at 100 microseconds  $\pm$  20 microseconds
- 11. Tested on release from inhibit.
- 12. Applies to each output.

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

Table 10: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, unless otherwise specified.

DUAL OUTPUT MODELS - STAN	DARD AND /ES	MTR2805D <sup>2</sup>		N	MTR2812D			MTR2815D			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	+ V <sub>OUT</sub>	4.850	5.00	5.150	11.64	12.00	12.36	14.55	15.00	15.45	V
	- V <sub>OUT</sub>	4.825	5.00	5.172	11.58	12.00	12.42	14.47	15.00	15.53	<b>"</b>
OUTPUT CURRENT <sup>3</sup>	EITHER OUTPUT	0	2.5	4.5 <sup>1</sup>	0	1.25	2.25 <sup>1</sup>	0	1.00	1.80 <sup>1</sup>	_
V <sub>IN</sub> = 16 TO 40	TOTAL OUTPUT	_	_	5	_	_	2.5	_	_	2.00	A
OUTPUT POWER <sup>3</sup>	EITHER OUTPUT	0	12.5	22.5 <sup>1</sup>	0	15	27 <sup>1</sup>	0	15	27 <sup>1</sup>	W
V <sub>IN</sub> = 16 TO 40	TOTAL OUTPUT	_	_	25	_	_	30	_	_	30	l vv
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	_	20	80	_	30	80	_	25	80	, , , , , , , , , , , , , , , , , , ,
10 kHz - 2 MHz ± V <sub>OUT</sub>	T <sub>C</sub> = -55°C TO +125°C	_	_	_	_	40	120	_	40	120	mV p-p
LINE REGULATION <sup>4</sup>	+ V <sub>OUT</sub>	_	10	50	_	10	50	_	10	50	
V <sub>IN</sub> = 16 TO 40	- V <sub>OUT</sub>	_	50	100	-	50	150	_	50	180	mV
LOAD REGULATION	+ V <sub>OUT</sub>	_	5	50	_	15	50	_	15	50	,,
NO LOAD TO FULL	- V <sub>OUT</sub>	_	25	100	_	30	150	_	30	180	mV
CROSS REGULATION <sup>1</sup>	SEE NOTE 5	_	4	6	_	4	6	_	4	6	0/
EFFECT ON -V <sub>OUT</sub> , 25°C	SEE NOTE 6	_	7	12	_	4	8.3	_	3	8	%
INPUT VOLTAGE	CONTINUOUS	16	28	40	16	28	40	16	28	40	V
NO LOAD TO FULL	TRANSIENT 50 ms <sup>1</sup>	0	_	50	0	_	50	0	_	50	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
INPUT CURRENT	NO LOAD	_	35	50	-	50	75	_	50	75	^
	INHIBITED	_	3	8	_	3	8	_	3	8	mA
INPUT RIPPLE CURRENT <sup>7</sup>	10 kHz - 10 MHz	_	15	40	_	20	50	_	20	50	mA p-p
EFFICIENCY	T <sub>C</sub> = 25°C	76	78	_	78	81	_	80	83	_	%
BALANCED LOAD	T <sub>C</sub> = -55°C TO +125°C	_	_	_	76	_	_	77	_	_	/0
LOAD FAULT 8, 9	POWER DISSIPATION	_	10	12	_	10	12	_	10	12	W
SHORT CIRCUIT	RECOVERY <sup>1</sup>	_	1.4	5.0	_	1.4	5.0	_	1.4	5.0	ms
STEP LOAD RESPONSE 9. 10	TRANSIENT	_	±200	±300	_	±150	±300	_	±200	±400	mV pk
50% - 100% - 50% ± V <sub>OUT</sub>	RECOVERY <sup>1</sup>	_	100	200	_	100	200	_	100	200	μs
STEP LINE RESPONSE <sup>1, 9, 11</sup>	TRANSIENT	_	±200	±400	_	±200	±400	_	±400	±500	mV pk
$V_{IN}$ = 16 TO 40, $\pm V_{OUT}$	RECOVERY	_	_	300	_	_	300	_	_	300	μs
START-UP <sup>9, 12</sup>	DELAY	_	1.4	5	_	1.4	5	_	1.4	5	ms
FULL LOAD	OVERSHOOT <sup>1</sup>	_	0	180	_	0	120	_	0	150	mV pk
CAPACITIVE LOAD <sup>1, 13</sup>	NO EFFECT ON DC			500			500			500	μF
$T_C = 25$ °C	PERFORMANCE		_	300		_	500	_	-	300	μ <sup>Γ</sup>

- 1. Guaranteed by characterization test and/or analysis. Not a production test.
- 2. MTR2805D (standard and /ES) is specified at 25  $^{\circ}\text{C}$  only.
- 3. Up to 90% of the total output current/power is available from either output providing the opposite output is carrying at least 10% of the total output power.
- 4. Operation is limited below 16 volts.
- 5. Effect on negative  $\rm V_{OUT}$  from 50%/50% loads to 80%/20% or 20%/80% loads.
- 6. Effect on negative  $V_{OUT}^{\rm out}$  from 50%/50% loads to 90%/10% or 10%/90% loads. Figure 26 on page 18
- 7. Tested with 6800 pF ceramic bypass capacitor connected externally from input common to case.
- 8. Indefinite short circuit protection not guaranteed above 125 °C case.
- 9. Recovery time is measured from application of the transient to point at which  $V_{\mbox{\scriptsize OUT}}$  is within 1% of final value.
- 10. Step load transition test is performed at 10 microseconds typical.
- 11. Step line characterization test is performed at 100 microseconds  $\pm$  20 microseconds
- 12. Tested on release from inhibit.
- 13. Applies to each output.

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

Table 11: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, unless otherwise specified.

TRIPLE OUTPUT MODEL - MTF	R28512T		5 (MAIN	)	±12	(AUXILIAI	RIES)	LINITS
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		4.85	5.00	5.15	±11.58	12.00	±12.42	V
OUTPUT CURRENT <sup>2</sup>		0.3	_	4.0	0	±0.416	0.750 <sup>1</sup>	_
V <sub>IN</sub> = 16 TO 40	MAX TOTAL AUX	-	_	_	_	_	0.833	Α Α
OUTPUT POWER <sup>2</sup>		0	_	20	0	±5	9.00 <sup>1</sup>	14/
V <sub>IN</sub> = 16 TO 40	MAX TOTAL AUX	-	_	_	_	_	10	W
OUTPUT RIPPLE	T <sub>C</sub> = 25 °C	-	50	125	_	20	60	m\/ n n
10 kHz - 2 MHz	T <sub>C</sub> = -55°C TO +125°C	_	_	180	_	_	60	mV p-p
LINE REGULATION	V <sub>IN</sub> = 16 TO 40	-	10	20	_	25	75	mV
LOAD REGULATION 3, 4		_	10	50	_	30	75	mV
INPUT VOLTAGE	CONTINUOUS	16	28	40	_	_	_	V
	TRANSIENT 50 MS <sup>1</sup>	-	_	50	_	_	_	
INPUT CURRENT	NO LOAD	-	70	110	_	_	_	A
	INHIBITED	_	3.0	6	_	_	_	mA
INPUT RIPPLE CURRENT	10 kHz - 10 MHz	_	20	80	_	_	_	mA p-p
EFFICIENCY	T <sub>C</sub> = 25 °C	72	75	_	_	_	_	%
	$T_C = -55^{\circ}C \text{ TO } +125^{\circ}C$	70	_	_	_	_	_	/0
LOAD FAULT <sup>5, 7</sup>	POWER DISSIPATION	-	_	14	_	_	14	W
ALL OUTPUTS SHORTED	RECOVERY <sup>1</sup>	_	4	6.0	_	4	6.0	ms
STEP LOAD RESPONSE 6, 7, 8	TRANSIENT	-	_	±400	_	_	±1500	mV pk
50% - 100% - 50% each output	RECOVERY <sup>1</sup>	_	_	0.300	_	_	6	ms
STEP LINE RESPONSE 1, 7, 9	TRANSIENT	-	_	±800	_	_	±800	mV pk
V <sub>IN</sub> = 16 - 40 - 16	RECOVERY	-	_	5	_	_	5	ms
START-UP <sup>7, 10</sup>	DELAY	_	4	6.0	_	4	6.0	ms
	OVERSHOOT <sup>1</sup>		_	500	_	_	1500	mV pk

- ${\bf 1.}~{\bf Guaranteed}~{\bf by}~{\bf characterization}~{\bf test}~{\bf and/or}~{\bf analysis}.~{\bf Not}~{\bf a}~{\bf production}~{\bf test}.$
- 2. The sum of the two aux outputs is not to exceed 10 watts. The maximum load per aux output is 9 watts.
- 3. To maintain regulation when operating the  $\pm aux$  at full load, a minimum load of 300 mA is required on the main.
- 4. Measured on each output one at a time with the other outputs at full load.
- 5. Indefinite short circuit protection not guaranteed above 125  $^{\circ}\text{C}$  (case).
- 6. Response of each output as all outputs are simultaneously transitioned.

  Main: 50% 100% 50% of main full load

  Auxiliaries: 25% 50% 25% each, of total auxiliary full load
- 7. Recovery time is measured from application of the transient to point at which  $V_{\text{O}\text{IIT}}$  is within 1% of regulation.
- 8. Step load transition test is performed at 10 microseconds typical.
- 9. Step line characterization test is performed at 100 microseconds  $\pm$  20 microseconds
- 10. Tested on release from inhibit.

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

Table 12: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, unless otherwise specified.

TRIPLE OUTPUT MODEL - MTR28515T		5 (MAIN)			±15 (AUXILIARIES)				
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS	
OUTPUT VOLTAGE		4.85	5.00	5.15	±14.47	15.00	±15.52	V	
OUTPUT CURRENT <sup>2</sup>		0.3	_	4.0	0	±0.333	0.600 1	A	
V <sub>IN</sub> = 16 TO 40	MAX TOTAL AUX	_	_	_	_	_	0.666		
OUTPUT POWER <sup>2</sup>		0	_	20	0	±5	9.00 <sup>1</sup>	W	
V <sub>IN</sub> = 16 TO 40	MAX TOTAL AUX	-	_	_	_	_	10	"	
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	-	50	125	_	20	60	mV p-p	
10 kHz - 2 MHz	T <sub>C</sub> = -55°C TO +125°C	_	_	180	_	_	60		
LINE REGULATION	V <sub>IN</sub> = 16 TO 40	_	10	20	_	30	75	mV	
LOAD REGULATION 3, 4		-	10	50	_	30	75	mV	
INPUT VOLTAGE	CONTINUOUS	16	28	40	_	_	_	V	
	TRANSIENT 50 MS <sup>1</sup>	-	_	50	_	_	_	V	
INPUT CURRENT	NO LOAD	-	70	120	_	_	_	mA	
	INHIBITED	-	3.0	6	_	_	_		
INPUT RIPPLE CURRENT	10 kHz - 10 MHz	-	20	80	_	_	_	mA p-p	
EFFICIENCY	T <sub>C</sub> = 25 °C	73	75	_	_	_	_	%	
	T <sub>C</sub> = -55°C TO +125°C	71	_	_	_	_	_		
LOAD FAULT <sup>5, 7</sup>	POWER DISSIPATION	-	_	14	_	_	14	W	
ALL OUTPUTS SHORTED	RECOVERY <sup>1</sup>	_	4	6.0	_	4	6.0	ms	
STEP LOAD RESPONSE 6, 7, 9	TRANSIENT	-	_	±400	_	_	±1500	mV pk	
50% - 100% - 50% each output	RECOVERY	_	_	0.300	_	_	6	ms	
STEP LINE RESPONSE 1, 7, 8	TRANSIENT	-	_	±800	_	_	±800	mV pk	
V <sub>IN</sub> = 16 - 40 - 16	RECOVERY 7	-	_	5	_	_	5	ms	
START-UP <sup>7, 10</sup>	DELAY	_	4	6.0		4	6.0	ms	
	OVERSHOOT <sup>1</sup>		_	500	_	_	1500	mV pk	

#### Notes

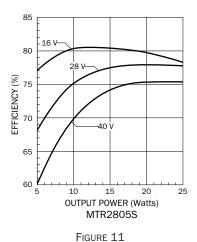
- ${\bf 1.}~{\bf Guaranteed}~{\bf by}~{\bf characterization}~{\bf test}~{\bf and/or}~{\bf analysis}.~{\bf Not}~{\bf a}~{\bf production}~{\bf test}.$
- 2. The sum of the two aux outputs is not to exceed 10 watts. The maximum load per aux output is 9 watts.
- 3. To maintain regulation when operating the  $\pm {\rm aux}$  at full load, a minimum load of 300 mA is required on the main.
- 4. Measured on each output one at a time with the other outputs at full load.
- 5. Indefinite short circuit protection not guaranteed above 125  $^{\circ}\text{C}$  (case).
- 6. Response of each output as all outputs are simultaneously transitioned. Main: 50% 100% 50% of main full load

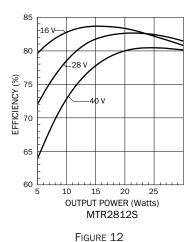
Auxiliaries: 25% - 50% - 25% each, of total auxiliary full load

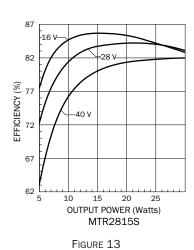
- 7. Recovery time is measured from application of the transient to point at which  $V_{\text{OUT}}$  is within 1% of regulation.
- 8. Step load transition test is performed at 10 microseconds typical.
- 9. Step line characterization test is performed at 100 microseconds ± 20 microseconds
- 10. Tested on release from inhibit.

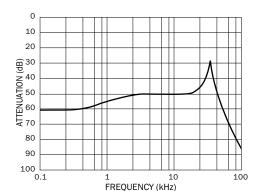
### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

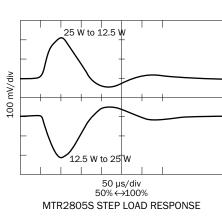
Typical Performance Plots: 28 Vin, 25 °C Case, 100% load, unless otherwise specified. These are examples for reference only and are not guaranteed specifications.

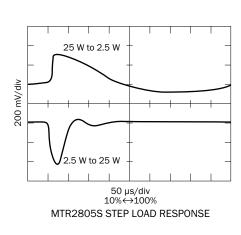










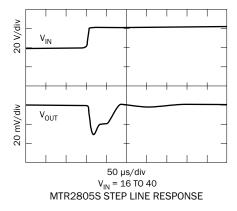




AUDIO REJECTION, MTR SERIES

FIGURE 15

FIGURE 16



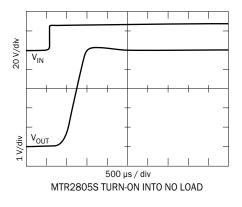
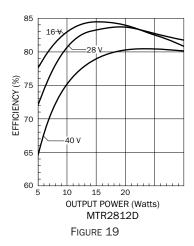


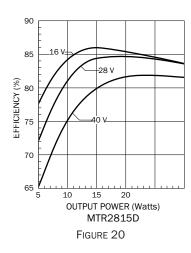
FIGURE 17

FIGURE 18

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

Typical Performance Plots: 28 Vin, 25 °C Case, 100% load, unless otherwise specified. These are examples for reference only and are not guaranteed specifications.





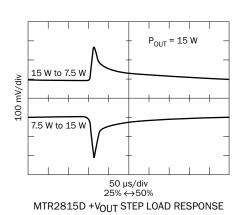
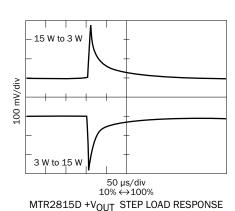
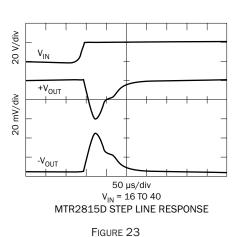


FIGURE 21





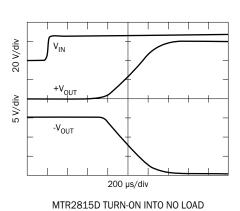


FIGURE 24

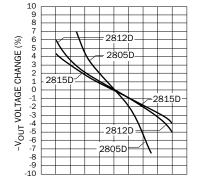


FIGURE 22

2812D -V<sub>OUT</sub> VOLTAGE CHANGE (%) 4 3 COND A 0 2815D -2 -3 2805D -4 -5 2812D -6 OUTPUT LOAD (%) COND. B: 50% LOAD +V, 50% to 10% -V COND. A: 50% LOAD -V, 50% to 10% +V

10% to 90% LOAD +V, 90% to 10% LOAD -V CROSS REGULATION

+POUT 0 3 6 9 12 15 18 21 -POUT 30 27 24 21 18 15 12 9

FIGURE 26

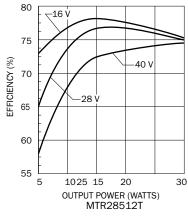
**CROSS REGULATION** 

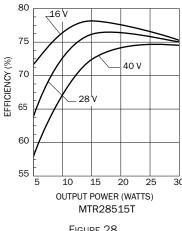
FIGURE 25

**OUTPUT LOAD (Watts)** 

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

Typical Performance Plots: 28 Vin, 25 °C Case, 100% load, unless otherwise specified. These are examples for reference only and are not guaranteed specifications.





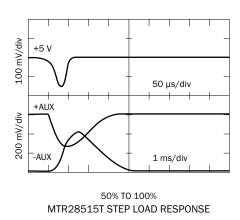
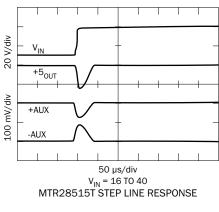
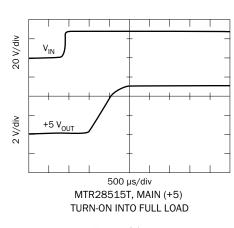


FIGURE 27

FIGURE 28

FIGURE 29





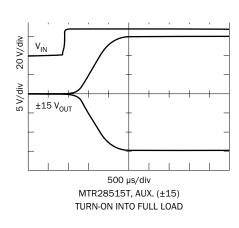


FIGURE 30

FIGURE 31

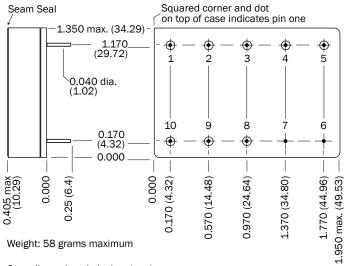
FIGURE 32

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

Typical Performance Plots: 28 Vin, 25 °C Case, 100% load, unless otherwise specified. These are examples for reference only and are not guaranteed specifications.

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

#### BOTTOM VIEW CASE F1



Case dimensions in inches (mm)

Tolerance  $\pm 0.005$  (0.13) for three decimal places  $\pm 0.01$  (0.3) for two decimal places unless otherwise specified

#### **CAUTION**

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

### Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

Gold plating of 50 - 150 microinches included in pin diameter

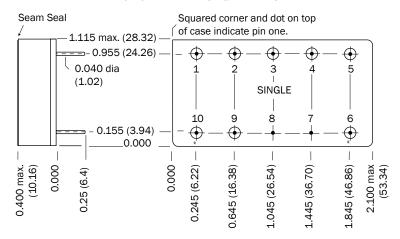
Seal hole  $0.120 \pm 0.002 (3.05 \pm 0.05)$ 

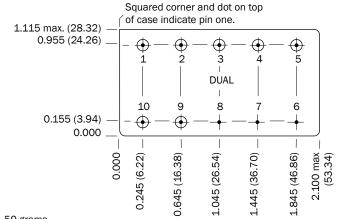
Please refer to the numerical dimensions for accuracy.

FIGURE 33: CASE F1 - TRIPLE MODELS

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

#### BOTTOM VIEW MTR SINGLE AND DUAL





Weight: 50 grams

Case dimensions in inches (mm)

Tolerance ±0.005 (0.13) for three decimal places ±0.01 (0.3) for two decimal places unless otherwise specified

#### CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300 °C for 10 seconds per pin.

### Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

Gold plating of 50 - 150 microinches included in pin diameter

Seal hole  $0.120 \pm 0.002 (3.05 \pm 0.05)$ 

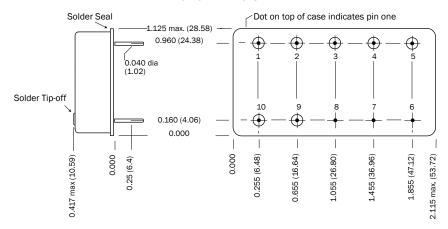
Please refer to the numerical dimensions for accuracy.

FIGURE 34: CASE H2 - SINGLE AND 883 DUAL MODELS

APPLIES TO ALL SINGLE MODELS, APPLIES ONLY TO 883 DUAL MODELS, SEE FIGURE 35 ON PAGE 23, CASE H4, FOR NON-883 DUAL MODELS

### **16 TO 40 VOLT INPUT - 20 TO 30 WATT**

#### **BOTTOM VIEW CASE H4**



Weight 50 grams max.

Case dimensions in inches (mm)

 $\begin{array}{ll} \hbox{Tolerance} & \pm 0.005 \ (0.13) \ \hbox{for three decimal places} \\ & \pm 0.01 \ (0.3) \ \hbox{for two decimal places} \\ & \hbox{unless otherwise specified} \end{array}$ 

#### CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300  $^{\circ}$  C for 10 seconds per pin.

#### Materials

Header Cold Rolled Steel/Nickel/Tin
Cover Cold Rolled Steel/Nickel/Tin
Pins #52 alloy, compression glass seal

Tin plating,150 microinches minimum, included in pin diameter.

Seal hole 0.092 ±0.002 (2.34 ± 0.05)

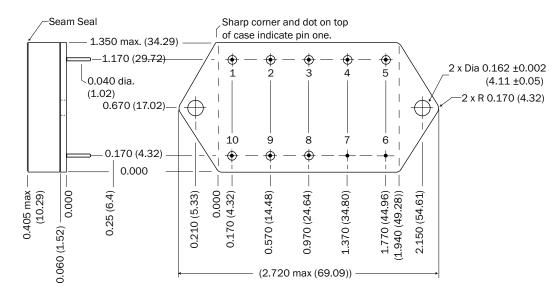
Please refer to the numerical dimensions for accuracy.

FIGURE 35: CASE H4 - DUAL MODELS - NON 883

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

#### **BOTTOM VIEW CASE J1**

Flanged cases: Designator "F" required in Case Option position of model number.



Weight: 62 gramsmaximum

Case dimensions in inches (mm)

Tolerance  $\pm 0.005$  (0.13) for three decimal places

±0.01 (0.3) for two decimal places unless otherwise specified

### **CAUTION**

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

### Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

Gold plating of 50 - 150 microinches included in pin diameter

Seal Hole: 0.120 ±0.002 (3.04 ±0.05)

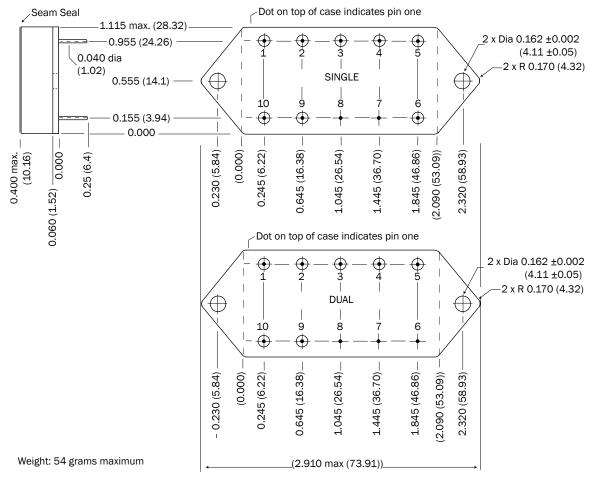
Please refer to the numerical dimensions for accuracy.

FIGURE 36: CASE J1 - TRIPLE MODELS

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

#### BOTTOM VIEW MTR SINGLE AND DUAL FLANGED

Flanged cases: Designator "F" required in Case Option position of model number.



Case dimensions in inches (mm)

Tolerance  $\pm 0.005$  (0.13) for three decimal places

±0.01 (0.3) for two decimal places unless otherwise specified

### CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding  $300\,^{\circ}\mathrm{C}$  for 10 seconds per pin.

#### Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold, ceramic seal.

Gold plating of 50 - 150 microinches included in pin diameter

Seal hole 0.120 ±0.002 (3.04 ±0.05)

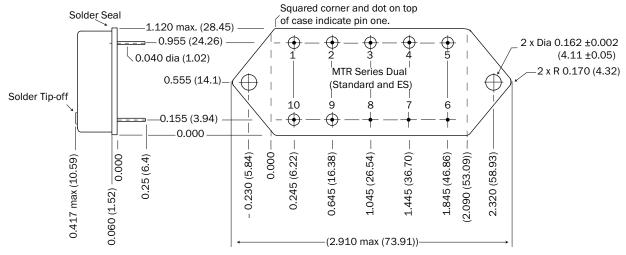
Please refer to the numerical dimensions for accuracy.

FIGURE 37: CASE K3 - SINGLE AND DUAL 883 MODELS
APPLIES TO ALL SINGLE MODELS, APPLIES ONLY TO 883 DUAL MODELS

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

#### BOTTOM VIEW CASE K5

Flanged cases: Designator "F" required in Case Option position of model number.



Weight: 54 grams max.

Case dimensions in inches (mm)

Tolerance  $\pm 0.005$  (0.13) for three decimal places

±0.01 (0.3) for two decimal places unless otherwise specified

#### **CAUTION**

Heat from reflow or wave soldering may damage the device.

Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

#### Materials

Header Cold Rolled Steel/Nickel/Tin
Cover Cold Rolled Steel/Nickel/Tin
Pins #52 alloy, compression glass seal

Tin plating, 150 microinches minimum included in pin diameter.

Seal hole  $0.092 \pm 0.002 (2.34 \pm 0.05)$ 

Please refer to the numerical dimensions for accuracy.

FIGURE 38: CASE K5 - DUAL MODELS - NON 883

### 16 TO 40 VOLT INPUT - 20 TO 30 WATT

ELEMENT EVALUATION TABLES FOR QML PRODUCTS ARE IN "APP-009 QUALITY AND CERTIFICATION", APPENDIX A, IN COMPLIANCE WITH MIL-PRF-38534 REVISION L. (LINK HTTPS://www.craneae.com/quality-assurance-modular-power)

# ENVIRONMENTAL SCREENING HIGH RELIABILITY STANDARD, /ES AND /883 (CLASS H)

	NON-QN	1L <sup>1</sup>	CLASS H QML 2		
TEST PERFORMED	STANDARD	/ES	/883 CH <sup>3</sup>	/883 QML <sup>4</sup>	
Pre-cap Inspection, Method 2017, 2032	•	•		•	
Temperature Cycle (10 times)					
Method 1010, Cond. C, -65°C to +150°C, ambient			•	•	
Method 1010, Cond. B, -55°C to +125°C, ambient					
Constant Acceleration					
Method 2001, 3000 g			•	•	
Method 2001, 500 g					
PIND, Test Method 2020, Cond. A			<b>■</b> 5	<b>■</b> 5	
Burn-in Method 1015, +125°C case, typical <sup>6</sup>					
96 hours					
160 hours				•	
Final Electrical Test, MIL-PRF-38534, Group A,					
Subgroups 1 through 6, -55°C, +25°C, +125°C case			•	•	
Subgroups 1 and 4, +25°C case	•				
Hermeticity Test, Method 1014					
Gross Leak, Cond. C <sub>1</sub> , fluorocarbon			•	•	
Fine Leak, Cond. A <sub>2</sub> , helium			•	•	
Gross Leak, Dip					
Final visual inspection, Method 2009					

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

#### Notes

- 1. Non-QML products may not meet all of the requirements of MIL-PRF-38534.
- 2. All processes are QML qualified and performed by certified operators.
- $\hbox{3. Class H QML products with no SMD number are marked "CH" per MIL-PRF-38534, 3.9.5.8.3, Table III. } \\$
- 4. Class H QML products have an SMD number
- 5. Not required by DLA but performed to assure product quality.
- 6. Burn-in temperature designed to bring the case temperature to  $\pm 125\,^{\circ}\text{C}$  minimum. Burn-in is a powered test.

TABLE 13: Environmental Screening High Reliability Standard, /ES and /883 (Class H)  $\,$ 

