

# 1130 SERIES – 300 MB/S 144 X 144 ECL SWITCH MATRIX

## TECHNICAL FEATURE

### FEATURES

- Non-Blocking, Full Fan-out Architecture
- Single-ended or Differential ECL Input/Output
- Local Front-Panel and Remote TCP/IP Control
- Full Featured Color Graphical Display
- Redundant Power Supplies
- Signal Output Presence Detection



### PERFORMANCE

Configuration ..... Up to 144 inputs x 144 outputs\*  
 ..... (or 72 inputs x 72 outputs - Clock and Data Pairs)  
 Data Rate ..... 10 Mb/s to 300 Mb/s  
 Path Matching ..... 2 ns max  
 Impedance ..... 50 Ohms nominal  
 Data Format ..... ECL  
 Built-in Test ..... Selectable Signal Presence Detection  
 Data Connectors ..... TNC Female  
 Supply Voltage ..... 120/220 VAC, 50/60 Hz  
 Operating Temperature Range ..... 10 to 40°C  
 Package Style ..... 14 U, 19" rack mount, 18" deep

\*Options available for these parameters-contact factory

### DESCRIPTION

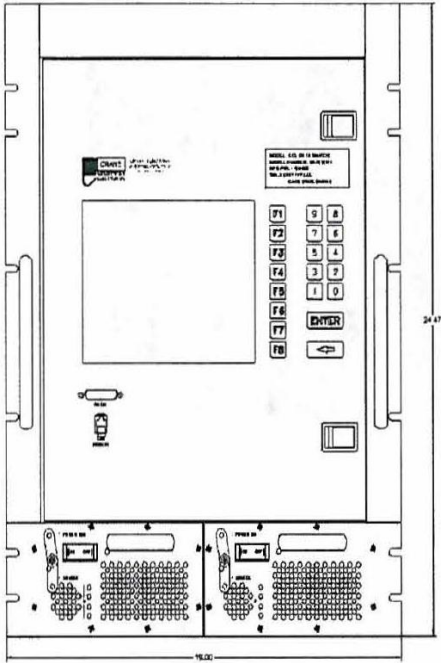
The ECL Switch Matrix provides a solution to complex signal routing problems. This device accepts clock and/or data inputs and routes them to the desired outputs. (Maximum unit configuration has 144 individual inputs and outputs available.) The matrix provides full fan-out operation, allowing each input to be routed to any or all outputs simultaneously. The system is also non-blocking, meaning that any number of input and output combinations can be connected. This device is ideal for large data routing stations.

Designed for critical communications installations, the unit includes several features which enhance system reliability and up-time. Redundant and user replaceable power supplies are a feature of the design. They provide an alarm when failure occurs, allowing that power supply to be powered down and replaced from the front of the unit while the matrix remains operational.

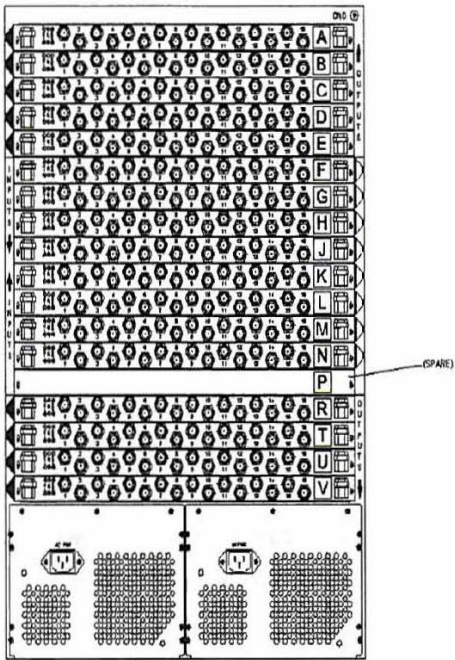
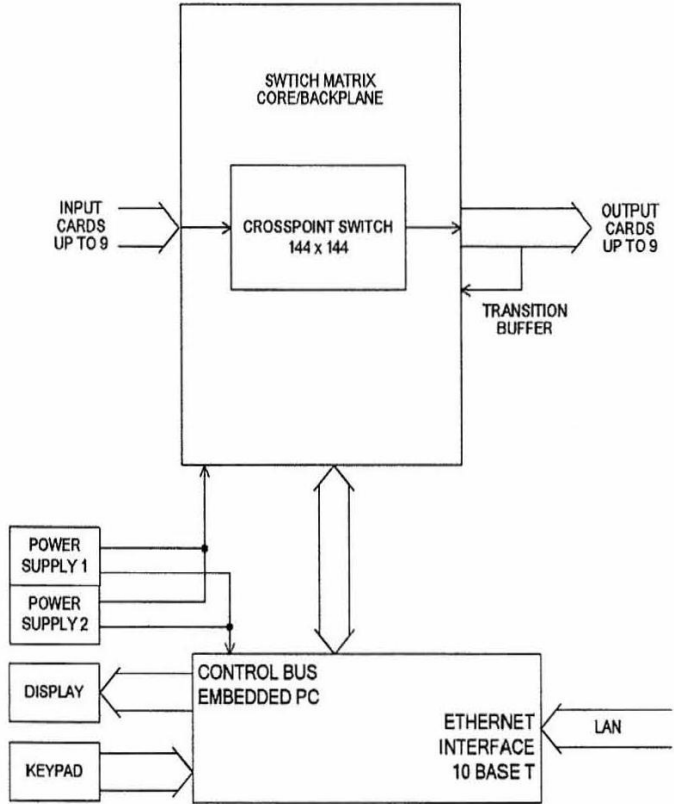
Other important features include user programmable signal presence/loss detection at each output. A modular design allows user serviceability with advanced built-in test features.

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**Unit Outline Drawing**



**Functional Block Diagram**



REAR VIEW

The information in this document is a derivative of a document cleared by the Department of Defense (DoD) Office of Security Review (OSR) for public release. OSR case number 10-S-0983 dated March 11, 2010. DS\_1130 Series\_300 Mb\_s 144 x 144 ECL Switch Matrix\_MW\_031110.doc. This revision supersedes all previous releases. All technical information is believed to be accurate, but no responsibility is assumed for errors. We reserve the right to make changes in products or specifications without notice. Copyright © 2011 Crane Electronics, Inc. All rights reserved.

